

RADIO FREQUENCY CIRCUIT DESIGN ORAL NOTES

By Giacomo Tombolan

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Guide to these notes:

- If you payed for these notes, **you've been scammed**. I always give my notes for free.
- I tried to explain and justify every step of each question, I hope you can follow my reasonings. If there's something that's "taken for granted", it means it was discussed in previous courses (i.e: fundamentals of electronics or analog circuit design)
- PDFs contain typos so beware of that! There are comments as errata corrige
- if my writing isn't clear, well, I'm sorry C: hope it helps anyway. Also, I speak maccheroni and I'm well aware of the English mistakes I made. My priority was to have a clear understanding of the topics.

If you're having any issue with this document just send an email to giacomo.tombolan@mail.polimi.it

Questions for the Oral Examination

RF Circuit Design

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Year 2020/21

RF Front-end Architectures

1. Effects of distortion.
2. Two-tone test and third-order intercept point (IIP3).
3. Theorem of maximum power transfer and its application to the impedance matching of amplifiers. Definition of power gains.
4. Matching networks: Resonant networks.
5. Matching networks: Transformers.
6. Noise figure of lossy circuits and cascaded systems.
7. *RF receivers*: Sensitivity and dynamic range.
8. *Heterodyne receivers*: Advantages. Image problem and filtering. Selectivity/Sensitivity trade-off. Block schematic from antenna to matched filter.
9. *Heterodyne receivers*: Problem of half-IF (IF/2).
10. Second-order nonlinearity. Intercept point IIP2 and link with 2nd-order harmonic distortion.
11. *Dual-IF receivers*: Architecture, advantages and drawbacks. Comparison with single-IF architecture.

12. *Zero-IF receivers*: Architecture, advantages and drawbacks. DC offsets and cancellation techniques.
13. *Zero-IF receivers*: Impact of I/Q mismatches on SNR. Impact of LO leakage.
14. *Image-reject receivers*: Shift-by-90 operation. Hartley architecture and effect of mismatches and Image-Rejection Ratio (IRR).
15. *Image-reject receivers*: Weaver architecture: advantages and drawbacks.
16. *Transmitters*: Effect of I/Q mismatches. Direct-conversion architecture.
17. *Transmitters*: Two-step transmitters. Single-Sideband (SSB) mixer.

Frequency Synthesizers

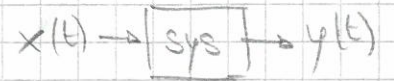
18. AM and FM disturbances of a carrier. Relationship between phase spectrum and voltage spectrum of the carrier.
19. Effects of phase noise in RF receivers and transmitters: EVM degradation. Reciprocal mixing in presence of blockers.
20. Phase detectors based on multiplier. Derivation of the phase model of the PLL. Nonlinear differential equation.
21. *Second-order PLLs*: Analysis of stability and transfer functions. Static phase error after n -th order input signal, frequency response.
22. *Second-order PLLs*: Frequency tracking and lock acquisition.
23. *Charge-pump PLLs*: Phase-frequency detector, phase-domain model, stabilizing zero, analysis of loop dynamics.
24. Limits of validity of the continuous-time model of PLLs.
25. Sources of ripple in a PLL. Reference spur problem in an integer- N loop. Methods to reduce the level of reference spur.
26. Design and simulation of a PLL.

RF Circuits

27. *LNAs*: Scattering parameters, insertion loss, reverse isolation, stability, linearity. Methods to increase reverse isolation.
28. *LNAs*: MOS noise model. Common-gate and shunt-feedback LNA topology.
29. *LNAs*: Inductor-degenerated topology.
30. *LNAs*: Noise canceling technique and application to shunt-feedback topology.
31. *Oscillators*: Feedback model and Barkhausen criterion. Negative-resistance model. Amplitude stabilization methods. Oscillation startup and effective gain.
32. *Oscillators*: Frequency stabilization. Effect of loop delay in oscillators. Meaning of quality factor in oscillators.
33. *Oscillators*: Phase Noise calculation in LC oscillators.
34. *Oscillators*: Noise/Power Trade-off.
35. *Oscillators*: Circuit topologies of voltage-controlled oscillators (VCOs). Noise on tuning voltage: calculation of FM noise.
36. *Oscillators*: Single-transistor and differential LC oscillator topologies: analysis with feedback and negative-resistor model.
37. *Oscillators*: Design and simulation of an RF oscillators in CMOS.
38. *Mixers*: Return-to-zero passive mixers in CMOS, conversion gain, noise.
39. *Mixers*: Single-balanced and double-balanced topologies, port-to-port isolation.
40. *Mixers*: Active mixers in CMOS, conversion gain, noise, port-to-port isolation.

1) Effects of distortion

Consider a memoryless non-linear system:



$$y(t) = \alpha_1 x(t) + \alpha_2 x^2(t) + \alpha_3 x^3(t) + \dots \quad x(t) = A \cos \omega t$$

↳ small signal → DC → 2nd harmonic

$$x^2(t) = A^2 \cos^2 \omega t = \frac{A^2}{2} + \frac{A^2}{2} \cos 2\omega t$$

$$x^3(t) = A^3 \cos^3 \omega t = A^3 \cos \omega t \left(\frac{1 + \cos 2\omega t}{2} \right) = \frac{A^3}{2} \cos \omega t + \frac{A^3}{4} \cos 3\omega t$$

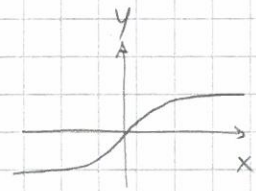
fundamental 3rd harm

$$y(t) = B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t + B_3 \cos 3\omega t$$

$$B_0 = \alpha_2 \frac{A^2}{2} \quad B_1 = \alpha_1 A + \frac{3}{4} \alpha_3 A^3 \quad B_2 = \alpha_2 \frac{A^2}{2} \quad B_3 = \frac{1}{4} \alpha_3 A^3$$

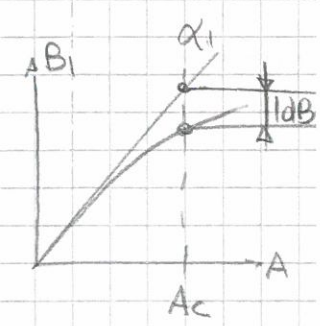
Gain compression phenomenon:

$B_1 = \alpha_1 A + \frac{3}{4} \alpha_3 A^3$ → If $\alpha_1, \alpha_3 < 0$ we experience "gain compression" of the first harmonic $\cos \omega t$ with respect to just the small signal gain $\alpha_1 A$



Def: 1dB comp. point

$$\frac{\alpha_1 A_c + \frac{3}{4} \alpha_3 A_c^3}{\alpha_1 A_c} = \underbrace{10^{-1/20}}_{1\text{dB}}$$



$$1 + \frac{3}{4} \frac{\alpha_3}{\alpha_1} A_c^2 = 0,89$$

$$A_c = -9,6\text{dB} + 10 \log_{10} \left| \frac{4}{3} \frac{\alpha_1}{\alpha_3} \right|$$

Distortion with two tones:

Consider now $x(t) = A_1 \cos \omega_1 t + A_2 \cos \omega_2 t$

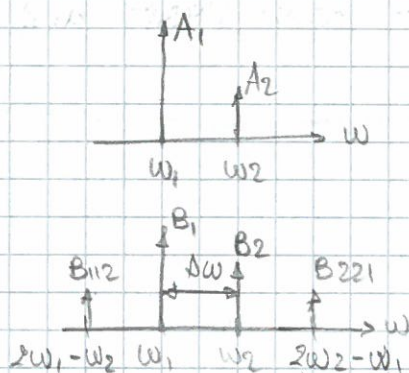
We will experience 4 tones evenly spaced with an equal $\Delta\omega = \omega_2 - \omega_1$

$$B_1 = \alpha_1 A_1 + \frac{3}{4} \alpha_3 A_1^3 + \frac{3}{2} A_1 A_2^2$$

$$B_2 = \alpha_1 A_2 + \frac{3}{4} \alpha_3 A_2^3 + \frac{3}{2} A_1^2 A_2$$

$$B_{112} = \frac{3}{4} \alpha_3 A_1 A_2^2$$

$$B_{221} = \frac{3}{4} \alpha_3 A_1^2 A_2$$



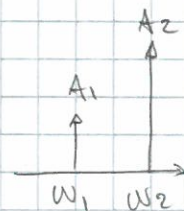
These are called IIP3 (inter-modulation 3rd harmonics)

In RF we don't really care about harmonics (they get filtered out). We care about tones that fall near the signal of interest.

Blocking phenomenon: small wanted A_1 , large unwanted A_2

$$B_1 = \alpha_1 A_1 + \frac{3}{4} \alpha_3 A_1^3 + \frac{3}{2} \alpha_3 A_1 A_2^2 \approx \left(\alpha_1 + \frac{3}{2} \alpha_3 A_2^2 \right) A_1$$

Negligible if $A_1^3 \ll A_1 A_2^2$

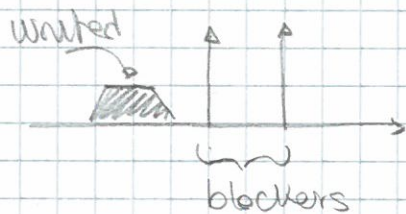


$$\frac{B_1}{A_1} = \alpha_1 + \frac{3}{2} \alpha_3 A_2^2 \rightarrow 0 \text{ for large } A_2$$

↳ harmonic gain of the sys

Intermodulation:

Consider two interferers near the wanted BW



IIP3

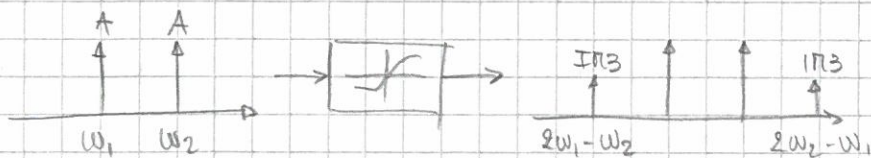


↳ blockers degrade SNDR

SNDR = signal to noise/distortion ratio =

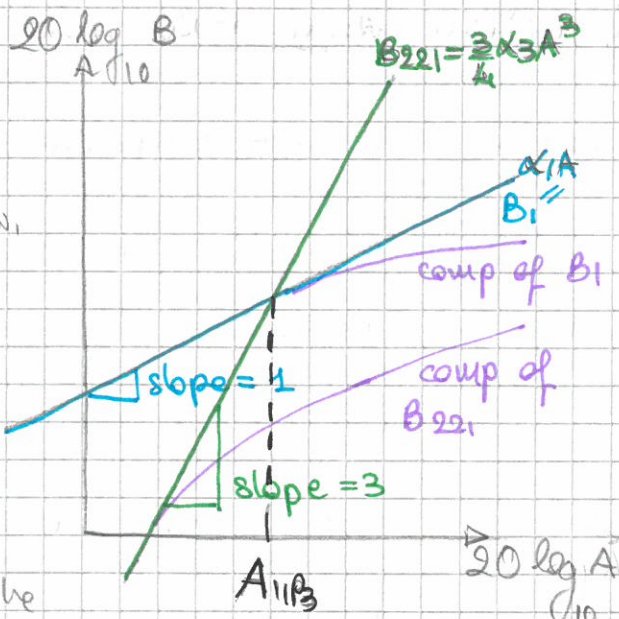
$$\frac{P_{sig}}{P_{noise} + P_{disturb}}$$

2) Two tone test and IIP3



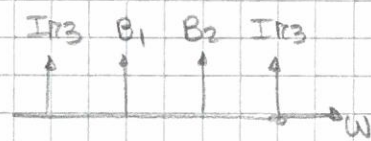
$$B_1|_{A_1=A_2} = \alpha_1 A + o.t$$

$$B_{221}|_{A_1=A_2} = \frac{3}{4} \alpha_3 A^3 + o.t$$



o.t = higher order terms that compress the two lines (see plot)

A_{IIP3} = point where linear and IP3 are equal (ideal)



In reality $A_{IIP3}|_{\text{compressed}} \geq A_{IIP3}|_{\text{IDEAL}}$

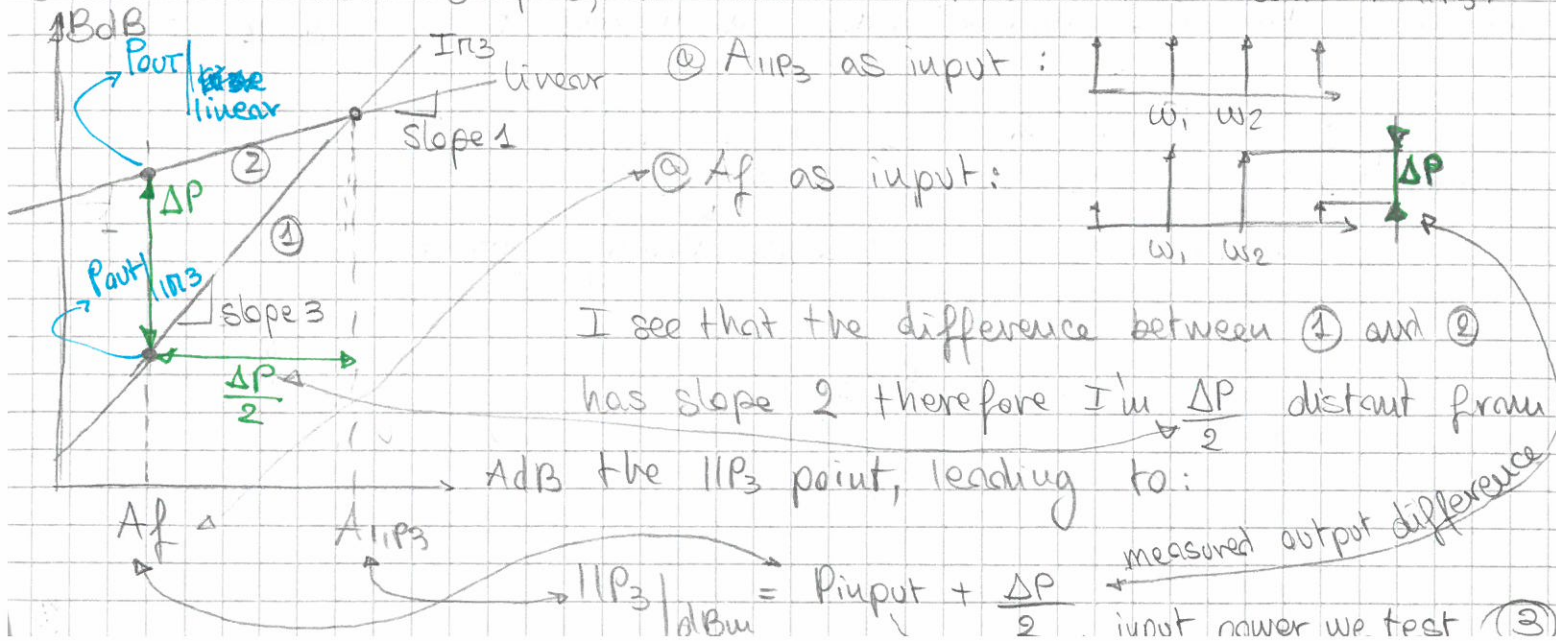
$$\alpha_1 A_{IIP3} = \frac{3}{4} \alpha_3 A_{IIP3}^3 \rightarrow A_{IIP3} = \sqrt{\frac{4}{3} \frac{|\alpha_1|}{|\alpha_3|}}$$

$$IIP3 \text{ dB} = 20 \log_{10} A_{IIP3} = 10 \log_{10} \left(\frac{4}{3} \frac{|\alpha_1|}{|\alpha_3|} \right)$$

We can see that $IIP3 \text{ dB} = A|_{1 \text{ dB comp point}} + 9.6 \text{ dB}$

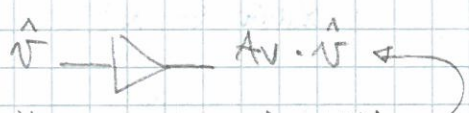
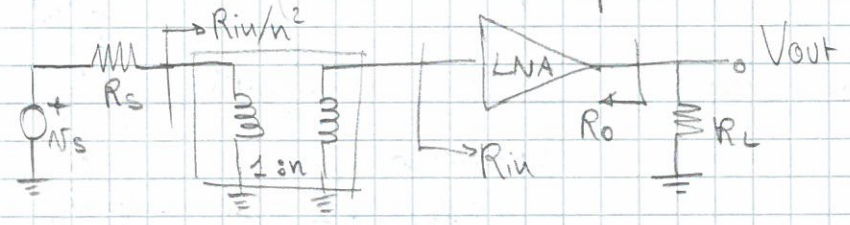
Practical measurements are taken for small amplitudes. Then,

since we know slopes, we can determine the ideal A_{IIP3} :



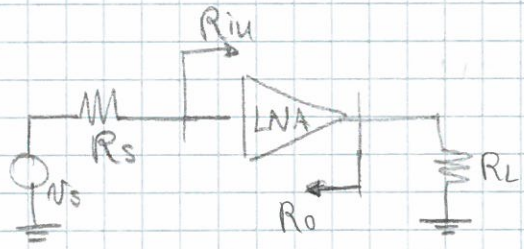
3) Maximum power transfer and appl / power gains

Consider a LNA with input matching



$$\frac{V_{out}}{V_{in}} = \frac{R_{in}/n^2}{R_{in}/n^2 + R_s} \cdot n \cdot A_v \cdot \frac{R_L}{R_o + R_L} = \alpha A_v \cdot \frac{R_L}{R_L + R_o}$$

out voltage division α



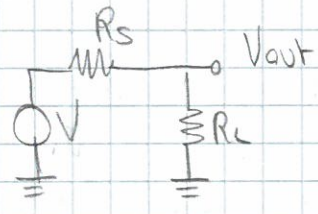
We're used to study

$$\frac{V_{out}}{V_{in}} = \alpha A_v \frac{R_L}{R_L + R_o} \quad \text{where} \quad \alpha = \frac{R_{in}}{R_{in} + R_s} \quad (*)$$

Max gain $\left[\begin{matrix} R_{in} \gg R_L \\ R_L \gg R_o \end{matrix} \right] \rightarrow \frac{V_{out}}{V_s} \Big|_{max} = A_v$

We're led to think that (*) is correct, but:

Max power transfer: consider the divider:



$$P_{out} = \underbrace{V^2 \left(\frac{R_s}{R_s + R_L} \right)^2}_{V_{out}^2} \cdot \frac{1}{R_L} = V^2 \frac{R_L}{(R_s + R_L)^2}$$

We want to max power output with respect to load R_L :

$$\frac{\partial P_{out}}{\partial R_L} = 0 \quad \text{only if} \quad R_L = R_s \rightarrow \text{load is matched to source}$$

This also works for impedances, where $Z_L = Z_s^*$

$$\text{Therefore } P_{out} = P_{load} = \frac{V^2}{(2R_s)^2} \cdot R_s = \frac{V^2}{4 \cdot R_s} = P_{L, \text{available}}$$

It is called output available power, since $V_{out} = V_{in}/2$

$$P_{L, \text{available}} = \frac{V_{out}^2}{8R_s}$$

Therefore we can see that something is off because:

- to max voltage gain $R_L \gg R_s$
- to max power $R_L = R_s$

Consider α from LNA gain expression:

$$\alpha = \left(\frac{R_{in}/n^2}{R_{in}/n^2 + R_s} \right) n = \frac{n R_{in}}{R_{in} + n^2 R_s} \rightarrow \text{maximize } \alpha$$

$$\frac{\partial \alpha}{\partial n} = \frac{R_{in}(R_{in} + n^2 R_s) - n R_{in} \cdot 2R_s}{(R_{in} + n^2 R_s)^2} = 0 \rightarrow R_{in} = n^2 R_s \quad (*)$$

$$n|_{opt} = \sqrt{\frac{R_{in}}{R_s}} \rightarrow \alpha|_{max} = \frac{n R_{in}}{2 R_{in}} = \frac{n_{opt}}{2} = \frac{1}{2} \sqrt{\frac{R_{in}}{R_s}}$$

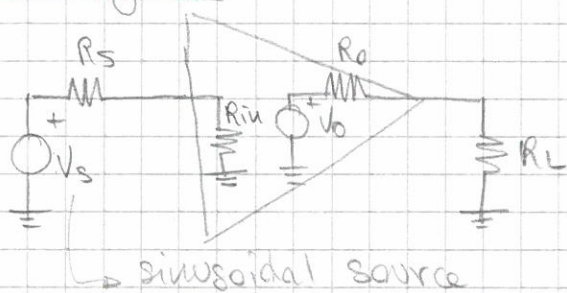
Important (*) $\frac{R_{in}}{n^2} = R_s \rightarrow$ impedances are matched

So, since we have $\alpha_{max} \rightarrow$ Voltage gain is maximised through impedance matching:

$$\left. \frac{V_{out}}{V_s} \right|_{max} = \frac{1}{2} n_{opt} A_v \cdot \frac{R_L}{R_L + R_o} \quad \text{e.g. } \left. \begin{array}{l} R_s = 50 \Omega \\ R_{in} = 100 \Omega \\ n_{opt} = \sqrt{\frac{100}{50}} = \sqrt{2} \end{array} \right\} \rightarrow \alpha = \frac{\sqrt{2}}{2}$$

we can't really control these

Power gain:



$$\frac{V_o}{V_s} = \alpha A_v = A_o$$

$$P_{s, available} = \frac{V_s^2}{8 R_s}$$

$$P_{out, av} = \frac{V_o^2}{8 R_o}$$

$$\left(\frac{V_s \cdot \frac{1}{2}}{\frac{V_s}{\sqrt{2}} \cdot \frac{1}{2}} \right)^2 = \frac{V_s^2}{8 R_s}$$

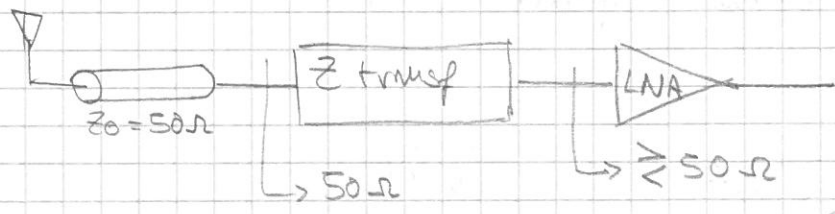
$$\text{Available power gain } G_A = \frac{P_{out, av}}{P_{in, av}} = \frac{V_o^2 / 8 R_o}{V_o^2 / 8 R_s} = \left(\frac{V_o}{V_s} \right)^2 \frac{R_s}{R_o} =$$

$$G_A = (\alpha A_v)^2 \frac{R_s}{R_o} = A_o^2 \frac{R_s}{R_o} \quad \text{when } R_s = R_o \quad G_A = A_o^2 \quad \text{but this}$$

is not true in general

1) Matching networks: resonant networks

If input impedance is not matched \rightarrow reflections



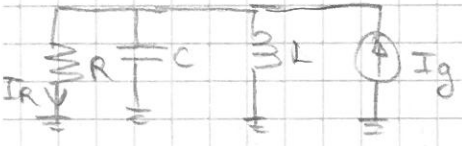
Impedance transformation network \rightarrow downward (low to high Z)
 \rightarrow upward (high to low Z)

Resonant circuits: RLC series/parallel

R models losses inside the RC reso.

$$Q = \omega_0 RC$$

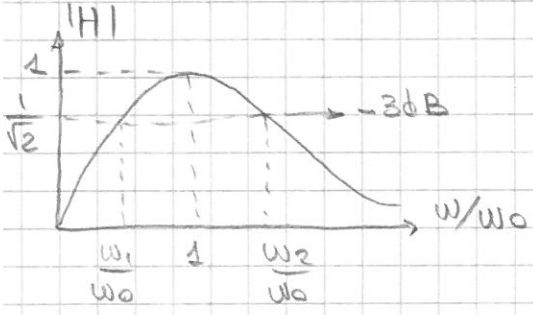
$$\omega_0 = \frac{1}{\sqrt{LC}}$$



$$H(s) = \frac{IR}{I_g} = \frac{s/Rc}{s^2 + \frac{s}{RC} + \frac{1}{LC}}$$

$$= \frac{s \frac{\omega_0}{Q}}{s^2 + s \frac{\omega_0}{Q} + \omega_0^2}$$

Input impedance will be $Z_{in} = \frac{V}{I_g} = R \frac{IR}{I_g} = R \cdot H(s)$



-3dB bandwidth

$$|H(j\omega)^2| = \frac{1}{1 + Q^2 \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right)^2} = \frac{1}{2}$$

$$Q^2 \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right)^2 = 1 \quad Q \left(\frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right) = \pm 1$$

$$\omega^2 + Q\omega\omega_0 - \omega_0^2 = 0 \rightarrow \omega_{1,2} = \omega_0 \left(\mp \frac{1}{2Q} + \sqrt{1 + \frac{1}{4Q^2}} \right)$$

-3dB BW = $\omega_2 - \omega_1 \rightarrow \frac{\omega_2 - \omega_1}{\omega_0} = \frac{\Delta\omega}{\omega_0} = \frac{1}{Q}$

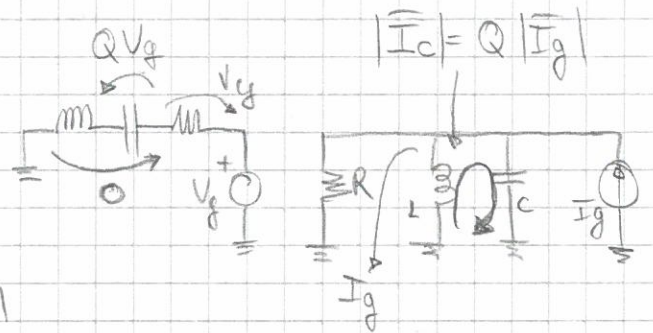
We can see that $Q = \frac{\omega_0}{\Delta\omega}$

$$Q = \omega_0 RC = \omega_0 \frac{\frac{1}{2} C |V|^2}{\frac{1}{2} \frac{|V|^2}{R}} = \omega_0 \frac{\text{E stored}}{P_{\text{diss}}} = 2\pi \frac{\text{E stored}}{\text{E diss, per cycle}}$$

Important definition of Q

For $\omega = \omega_0$, if:

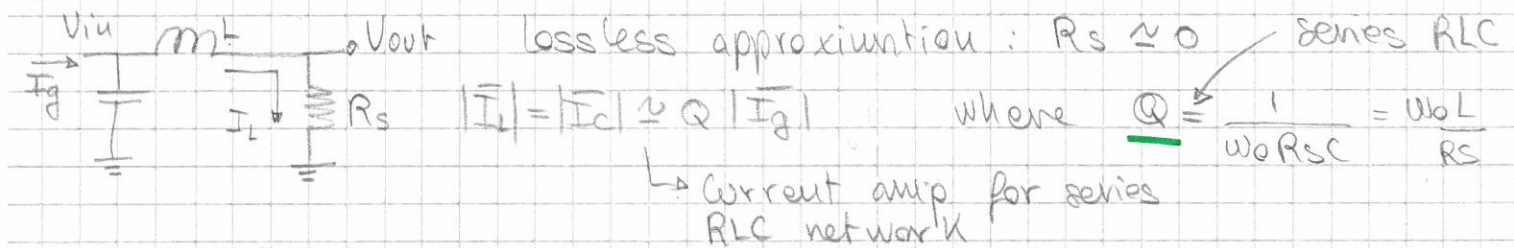
- ① RLC series: voltage amplification
- ② RLC parallel: current amplification



$$② |\bar{I}_c| = \omega_0 C \cdot |\bar{V}| = \omega_0 C R \cdot |\bar{I}_g| = Q |\bar{I}_g|$$

$$③ |\bar{V}_c| = \frac{|\bar{I}|}{\omega_0 C} = \frac{|\bar{V}_g|}{\omega_0 RC} = Q |\bar{V}_g|$$

L-match networks - upward L-match

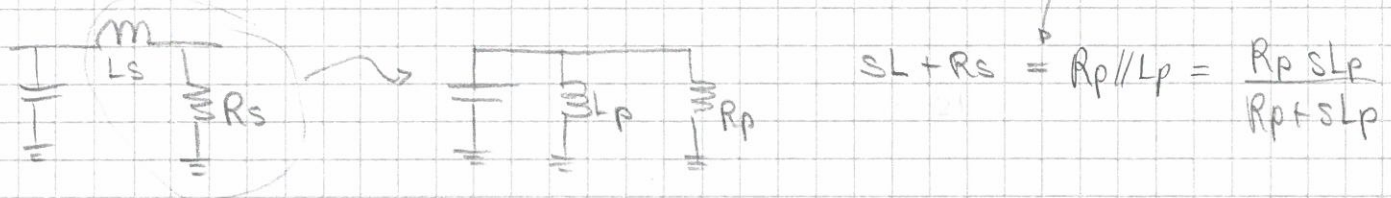


$Q \gg 1$ because $R_s \rightarrow 0$
lossless

$$|\bar{V}_{out}| \approx |\bar{I}_L| R_s = |\bar{V}_{in}| \omega_0 C \cdot R_s = |\bar{V}_{in}| / Q$$

$$\text{So } |Z_{in}| = \frac{|\bar{V}_{in}|}{|\bar{I}_g|} \approx \frac{Q |\bar{V}_{out}|}{|\bar{I}_L| / Q} = \underline{R_s \cdot Q^2}$$

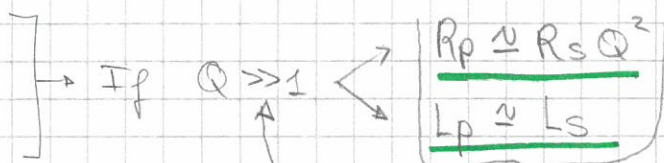
If lossless approx is removed, then:



After calculations and separation for R_p and L_p we will get:

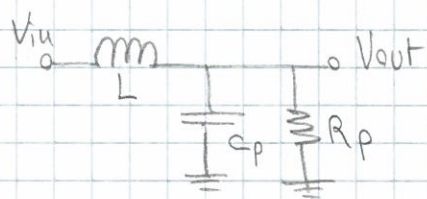
$$\underline{R_p = R_s (1 + Q^2)}$$

$$\underline{L_p = L_s \frac{1 + Q^2}{Q^2}}$$



lossless network approx

L-match networks: downward L-match



We just flipped L with C

Using lossless we can say that $R_p \rightarrow \infty$

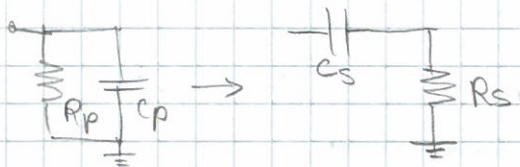
$$|V_{out}| = |V_c| = Q |V_{in}| \quad \text{so } |Z_{in}(j\omega)| \approx R_p / Q^2$$

↳ voltage amp

Where $Q = \frac{R}{\omega L} = \omega RC$

↳ parallel network

If lossless is removed, then



And we get that:

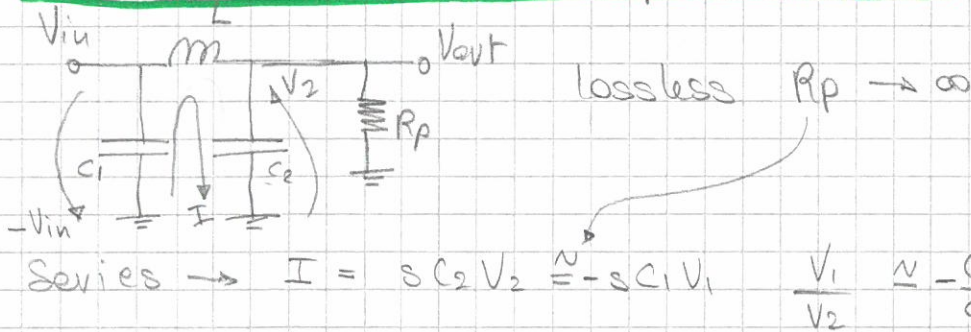
$$\underline{R_s} = \frac{R_p}{1 + Q^2} \quad \underline{C_s} = C_p \cdot \frac{1 + Q^2}{Q^2}$$

How to remember this: $R_s \ll R_p$ always, then:

- upward $L_p > L_s$ by a small amount $\left(\frac{1 + Q^2}{Q^2}\right)$
- downward $C_s > C_p$ " " " "

Always be careful to select the right Q factor (series or //)

π match network or colpitt's network



Consider now R_{in} , the dissipated power in the network is tied to R_p only \rightarrow any equivalent resistor has to have the same dissipation of the circuit:

$$\frac{1}{2} \frac{V_{in}^2}{R_{in}} = \frac{1}{2} \frac{V_s^2}{R_p} \rightarrow \underline{R_{in}} = R_p \left(\frac{V_1}{V_2} \right)^2 \approx R_p \left(\frac{C_2}{C_1} \right)^2$$

By choosing $C_2 \begin{matrix} \nearrow \text{up} \\ \searrow \text{down} \end{matrix} \geq C_1$ we can have upward / downward

To estimate Q we can say:

$$Q = \omega_0 \frac{\text{Estored}}{P_{diss}} \quad \text{Estored} = \frac{1}{2} \left(\frac{C_1 C_2}{C_1 + C_2} \right) |V_0|^2$$

$$V_0 = V_1 - V_2 = -\frac{C_2}{C_1} V_2 - V_2 = -V_2 \left(1 + \frac{C_2}{C_1} \right) \quad V_2 = V_0 \left(\frac{C_1}{C_1 + C_2} \right) = V_R$$

$$P_{diss} = \frac{1}{2} \frac{|V_2|^2}{R_p} = \frac{1}{2} \left(\frac{C_1}{C_1 + C_2} \right)^2 |V_0|^2$$

\hookrightarrow AC voltage

Improvement factor

$$Q = \omega_0 \frac{\frac{1}{2} \left(\frac{C_1 C_2}{C_1 + C_2} \right) |V_0|^2}{\frac{1}{2} \left(\frac{C_1}{C_1 + C_2} \right)^2 \frac{|V_0|^2}{R_p}} = \omega_0 C_2 R_p \left(1 + \frac{C_2}{C_1} \right)$$

L-match typical Q

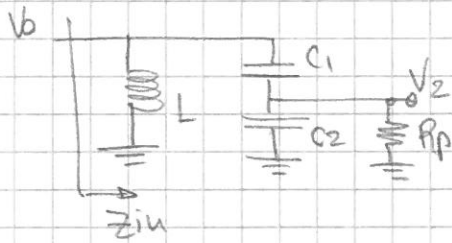
Note:



We can split L and therefore we see 2 L-match networks

This can give more degrees of freedom

Tapped capacitor resonator

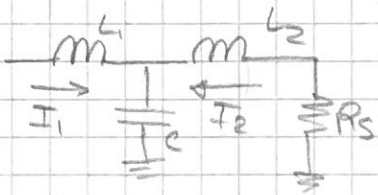


$$\frac{V_2}{V_0} \approx \frac{C_1}{C_2 + C_1} \rightarrow \text{low loss}$$

$$R_{in} = R_p \left(\frac{V_0}{V_2} \right)^2 = \left(1 + \frac{C_2}{C_1} \right)^2$$

→ from power dissipation equations

T-match network



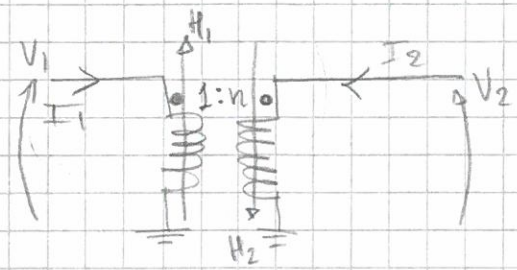
Low-loss $R_s \approx 0$

$$-V = sL_1 I_1 \approx sL_2 I_2 \quad \frac{I_1}{I_2} \approx \frac{L_2}{L_1}$$

From power dissipation $R_{in} = R_s \left(\frac{I_2}{I_1} \right)^2 \approx R_s \left(\frac{L_2}{L_1} \right)^2$

5) Matching Networks: transformers

We use inductor coupling instead of resonance.



In this case H_1, H_2 have the same orientation

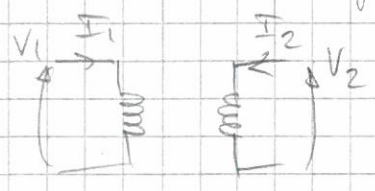
$$\mathcal{E}_m = \frac{\mu}{2} |\vec{H}_1 + \vec{H}_2|^2 dV$$

$\mathcal{E}_m =$ magnetic energy

total field
 $|\vec{H}| = |\vec{H}_1 + \vec{H}_2|$

$$\mathcal{E}_m = \underbrace{\frac{\mu}{2} |\vec{H}_1|^2 dV}_{\text{coil 1 energy}} + \underbrace{\frac{\mu}{2} |\vec{H}_2|^2 dV}_{\text{coil 2 energy}} + \underbrace{\frac{\mu}{2} \cdot 2 |\vec{H}_1| |\vec{H}_2| dV}_{\text{mutual energy}}$$

$$\vec{H} = \frac{\vec{B}}{\mu}$$

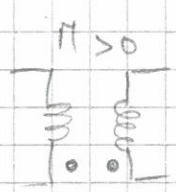
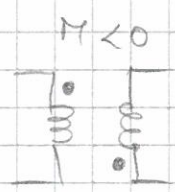
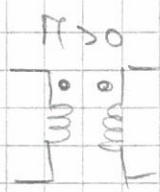


$$\begin{cases} \Phi_1 = L_1 I_1 + M I_2 \\ \Phi_2 = L_2 I_2 + M I_1 \end{cases}$$

$M =$ coupled inductance

$$V_1 = \dot{\Phi}_1 \quad V_2 = \dot{\Phi}_2 \quad \rightarrow \text{Lenz law}$$

$$\mathcal{E}_m = \int_0^t (V_1 I_1 + V_2 I_2) dt' = \frac{1}{2} L_1 I_1^2(t) + \frac{1}{2} L_2 I_2^2(t) + M I_1(t) I_2(t)$$



conservation of energy

$$k = \frac{|M|}{\sqrt{L_1 L_2}} \quad 0 \leq k \leq 1$$

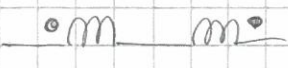
- $k = 1 \rightarrow$ max magnetic coupling
- $k = 0 \rightarrow$ zero coupling

Example:

$$I_1 = I_2 = I \quad L_1 = L_2 = L$$

$$\phi = \Phi_1 + \Phi_2 = L_1 I_1 + M I_2 + L_2 I_2 + M I_1 = (L_1 + L_2 + 2M) I$$

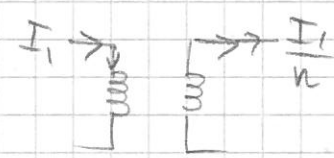
$$L_{TOT} = L_1 + L_2 + 2M \begin{cases} 4L \text{ if } k=1 \\ 2L \text{ if } k=0 \end{cases}$$



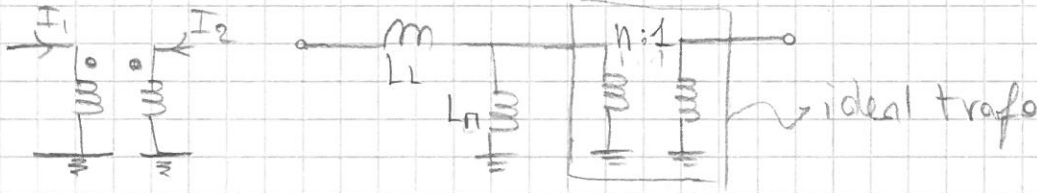
$$L_{TOT} = L_1 + L_2 - 2|M| \begin{cases} 2L \text{ if } k=0 \\ 0 \text{ if } k=1 \end{cases}$$

magneto motive force $\text{MMF} = n_1 I_1 + n_2 I_2 \rightarrow$ Ampere's law

$\frac{I_1}{I_2} = -\frac{n_2}{n_1}$ for 1:n trafo

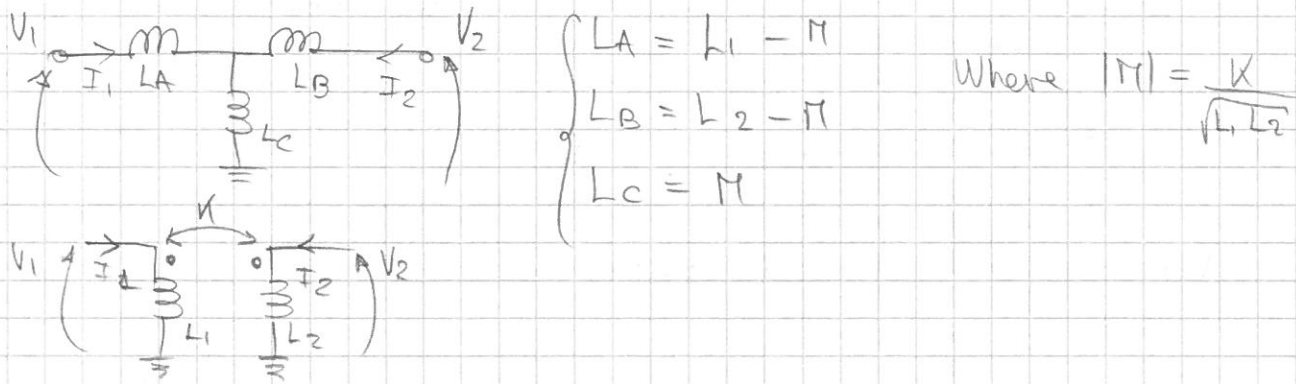


Equivalent model of coupled inductors

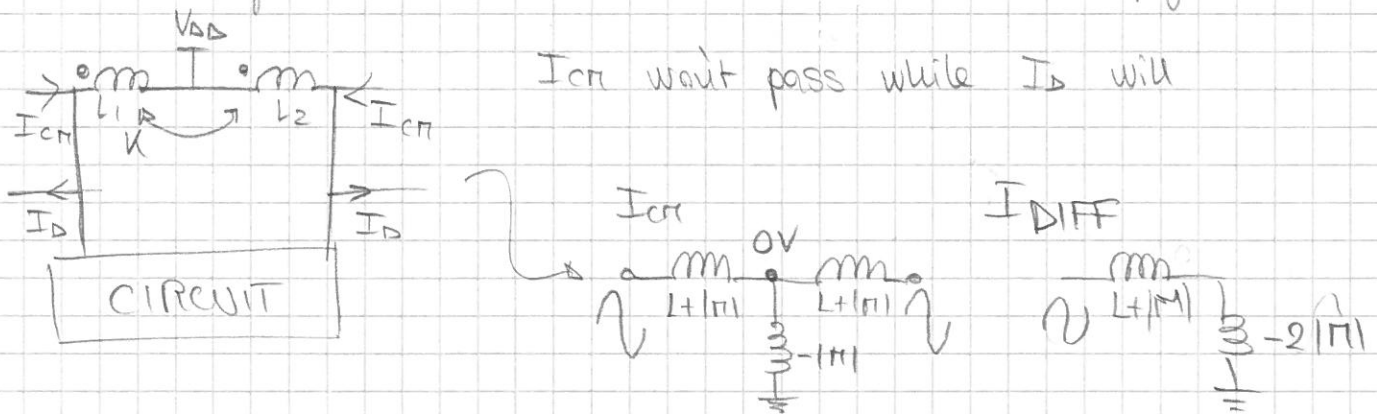


$L_L = (1 - k^2) L_1$ $L_n = k^2 L_1$ $n = k \sqrt{\frac{L_1}{L_2}}$
 $L \rightarrow$ leakage $L_n \rightarrow$ magnetizing

T-circuit for coupled inductors



This is useful with common mode killer configuration



6) NF of lossy circuits and NF of cascaded systems

$$NF \triangleq \frac{SNR_{IN}}{SNR_{OUT}}$$

$NF = 1$ if stage is noiseless
 $NF = \infty$ if input is noiseless \rightarrow NF depends on input noise \rightarrow out total noise

$$NF = \frac{\overline{V_{SIG,IN}^2}}{\overline{V_n^2}_{IN}} \cdot \frac{\overline{V_{n,OUT}^2}}{\overline{V_{SIG,OUT}^2}} = \frac{1}{A_o^2} \cdot \frac{\overline{V_{n,NETWORK}^2 + A_o^2 \overline{V_n R_s^2}}}{\overline{V_n R_s^2}}$$

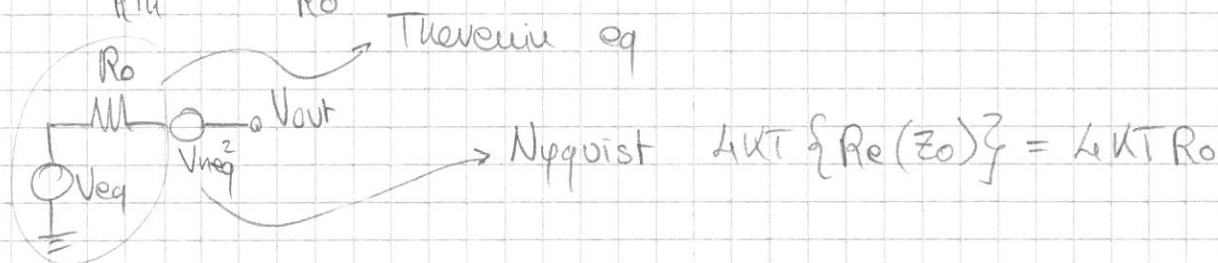
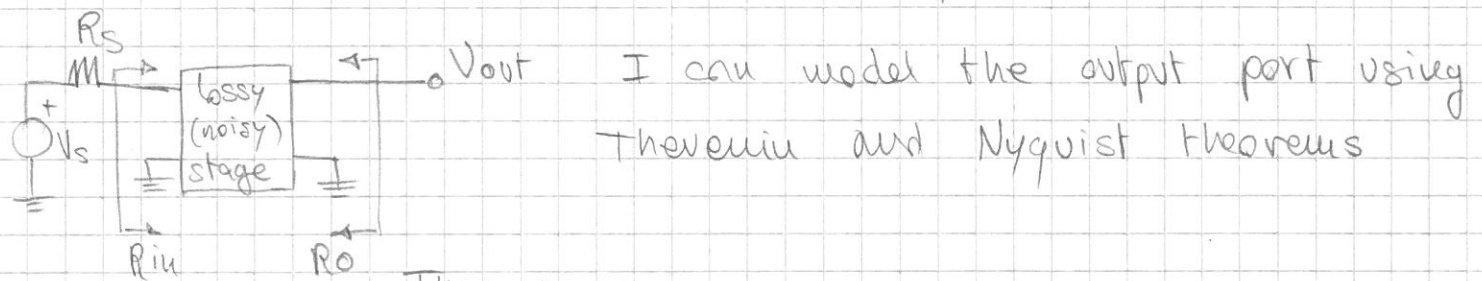
$\frac{V_{SIG,IN}^2}{V_{SIG,OUT}^2} \rightarrow$ in noise

$$\frac{\overline{V_{OUT,n,TOT}^2}}{A_o^2} = NF \cdot \overline{V_n R_s^2} \rightarrow PSD|_{R_o, input referred} = 4kTR_s \cdot NF R_o$$

\rightarrow Resistor wise PSD

\rightarrow Input referred output noise

Where R_o is the output impedance of a noisy stage:



$$NF = \frac{\overline{V_{OUT,TOT}^2}}{A_o^2} \cdot \frac{1}{\overline{V_n R_s^2}} = \frac{\overline{V_{n,eq}^2}}{A_o^2} \cdot \frac{1}{\overline{V_n R_s^2}} = \frac{4kT R_o}{A_o^2} \cdot \frac{1}{4kT R_s}$$

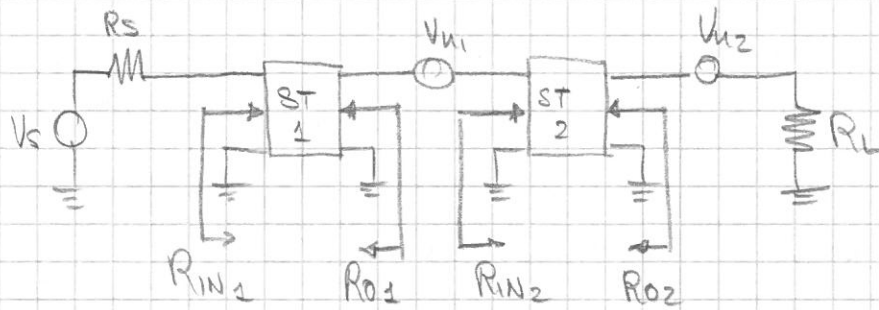
$$NF = \frac{1}{A_o^2 \frac{R_s}{R_o}} = \frac{1}{G_A} = LA \rightarrow \text{available power loss}$$

\rightarrow available power gain

Knowing $G_A, LA \rightarrow$ Noise of the stage is immediate

This is a very powerful result

NF in cascaded stages



$$NF = 1 + \underbrace{\frac{V_{n1}^2}{A_{o1}^2} \cdot \frac{1}{4kTRs}}_{NF_1} + \frac{V_{n2}^2}{A_{o1}^2 A_{o2}^2} \cdot \frac{1}{4kTRs}$$

$NF_2 = 1 + \frac{V_{n2}^2}{A_{o2}^2} \cdot \frac{1}{4kTRo1}$ → Now adjust NF equation using NF_2

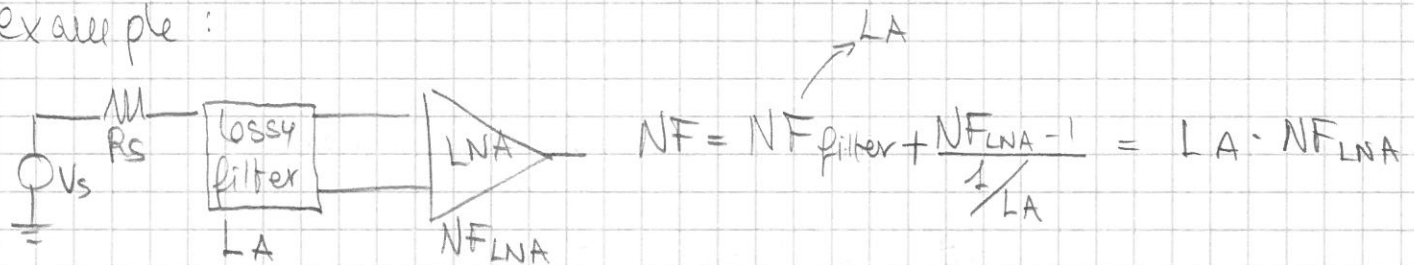
$$NF = NF_1 + \frac{\left(\frac{NF_2}{Ro1} - 1\right) 4kTRo1}{A_{o1}^2 \cdot 4kTRs} = NF_1 + \frac{NF_2 - 1}{GA_1}$$

In general:

$$NF = 1 + (NF_1 - 1) + \frac{NF_2 - 1}{GA_1} + \frac{NF_3 - 1}{GA_1 GA_2} + \dots$$

1st stage is the most critical for NF because the following stages are attenuated by available power gains of the previous stages → more negligible

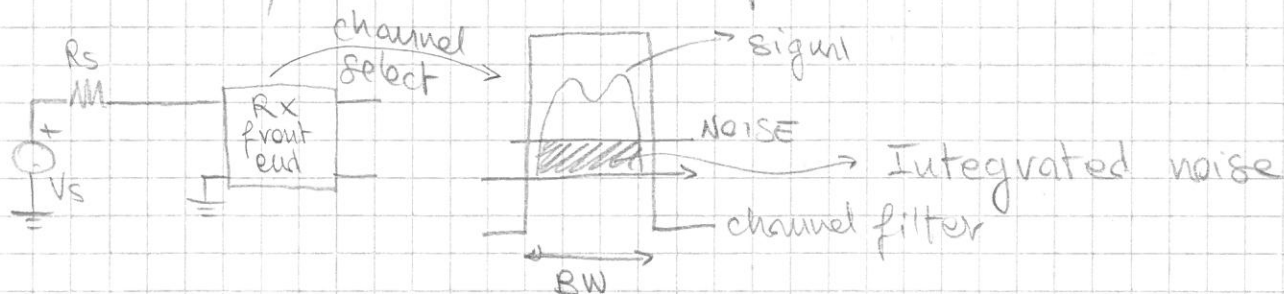
example:



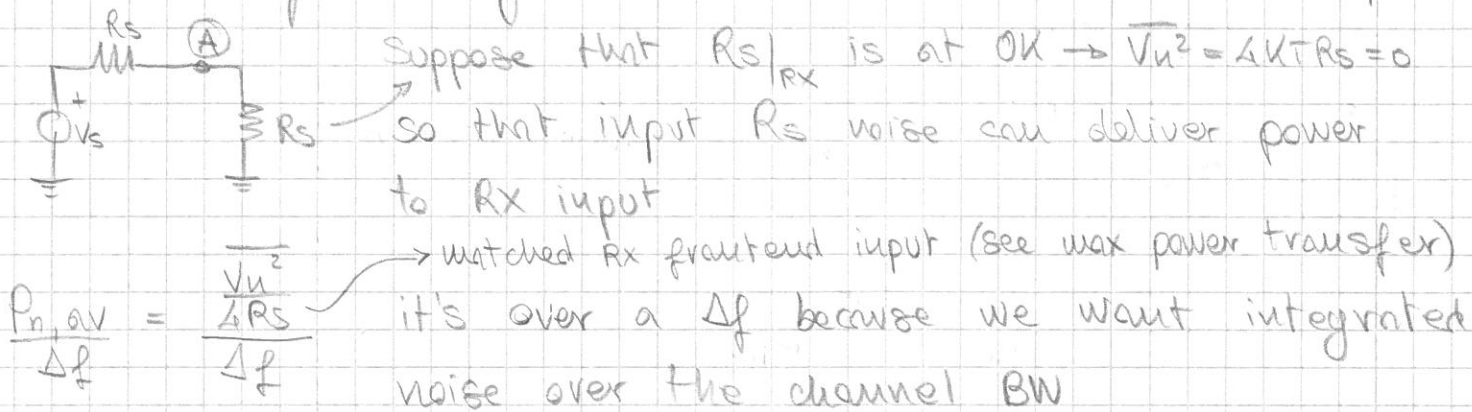
$$NF|_{dB} = LA|_{dB} + NF_{LNA}|_{dB}$$

7) RF receivers: sensitivity and dynamic range

RX sensitivity \triangleq min detectable power



We need to find integrated noise. Consider matched inputs:



Considering the RX front-end:

$$\frac{P_{n,av}}{\Delta f} = \frac{\overline{V_n^2}_{tot}}{4R_s} = \frac{\overline{V_n^2} \cdot N_{FRx}}{4R_s} = \frac{4kTR_s \cdot N_{FRx}}{4R_s \cdot \Delta f} = \frac{kT N_{FRx}}{\Delta f}$$

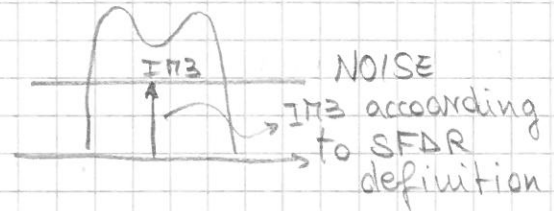
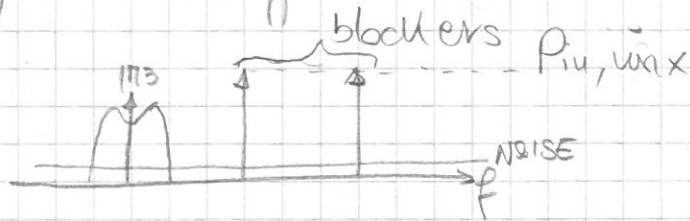
Since $SNR|_{min} = \frac{P_{s,av}(min)}{P_{n,av}}$ \rightarrow integrated power noise

$$P_{s,av}(min) = SNR|_{min} \cdot P_{n,av} = SNR|_{min} \cdot kT \cdot N_{FRx} \cdot BW$$

$$kT|_{dB} = -174 \text{ dBW/Hz} \quad \rightarrow \quad 6 \div 25 \text{ dB typically}$$

$$P_{s,av}|_{min, dB} = -174 \frac{\text{dBW}}{\text{Hz}} + SNR|_{min, dB} + N_{FRx}|_{dB} + 10 \log_{10}(BW)$$

Dynamic Range



SFDR \triangleq spurious free dynamic range $\triangleq P_{iu,max} / P_{iu,min}$ dB
 by the definition of "spurious free":

- $P_{iu,max}$ = blockers power such that $IIP3$ equals noise power
- $P_{iu,min}$ = sensitivity

This way $IIP3$ won't be detected because it's buried in noise

$P_{IIP3} |_{dBm} = P_{iu} |_{\max} + \frac{\Delta P}{2} = P_{iu} + \frac{P_{out} - P_{IIP3,OUT}}{2}$

→ max input blocker power

$P_{IIP3} |_{dBm} = P_{iu} |_{\max} + \frac{P_{out} - (P_{IIP3,IN REFERRED} + GA)}{2}$

$P_{IIP3} |_{dBm} = \frac{3}{2} P_{iu} |_{\max} - \frac{1}{2} P_{IIP3,IN REF}$ but we said $P_{IIP3} = P_n$

where P_n = input referred noise power, so

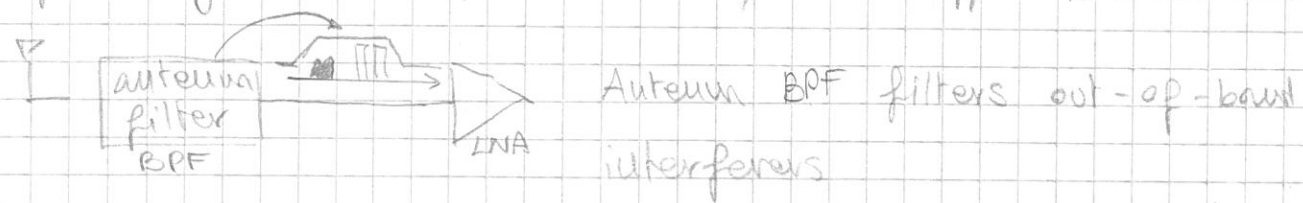
$P_{IIP3} |_{dBm} = \frac{3}{2} P_{iu} |_{\max} - \frac{1}{2} P_n \rightarrow P_{iu,max} = \frac{2 P_{IIP3} + P_n}{3}$

SFDR = $P_{iu,max} - P_{iu,min}$ SNR_{min} = $P_{iu,min} - P_n$ so

SFDR = $\frac{2}{3} P_{IIP3} + \frac{1}{3} P_n - (P_n + SNR_{min})$

SFDR |_{dB} = $\frac{2}{3} P_{IIP3} - \frac{2}{3} P_n - SNR_{min}$
 $\hookrightarrow kT \cdot NF_{RX} \cdot BW$

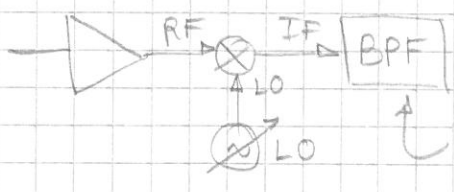
8) Heterodyne receivers: advantages, Image problem and filtering. Selectivity/Sensitivity trade-off. Block schematic



Channel selectivity $\sim 60\text{dB}$ in wireless systems

Can't be achieved with RLC or butterworth BPFs. Solution:

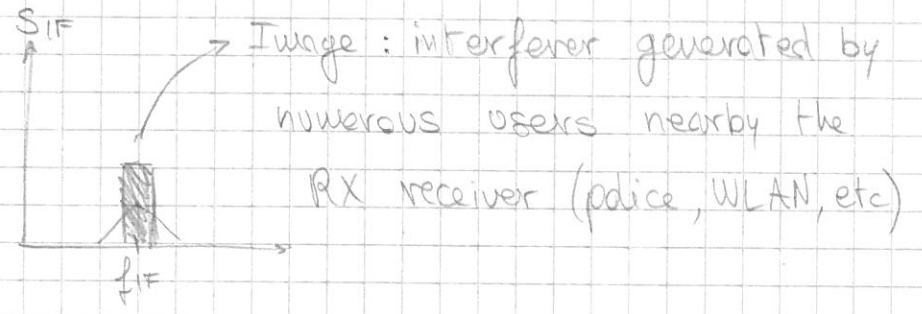
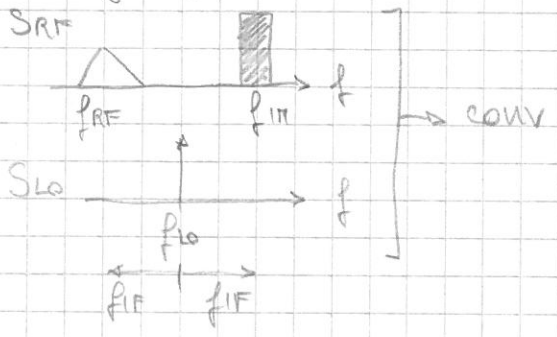
Heterodyne receiver: pros: - $IF \ll RF \rightarrow$ higher selectivity at lower freq



- IF filter can be fixed \rightarrow less complicated and expensive

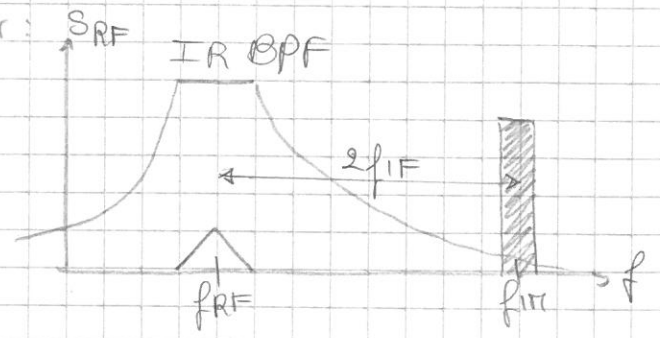
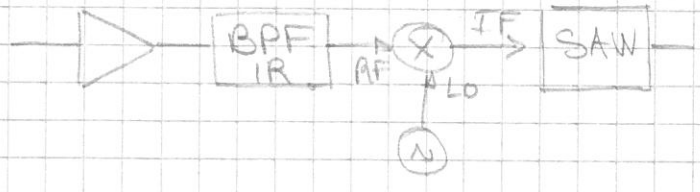
off-chip SAW filter

Image problem:



SNIR = signal to noise and image ratio is degraded.

Solution: Image Rejection filter:



$$2f_{IF} = f_{im} - f_{RF}$$

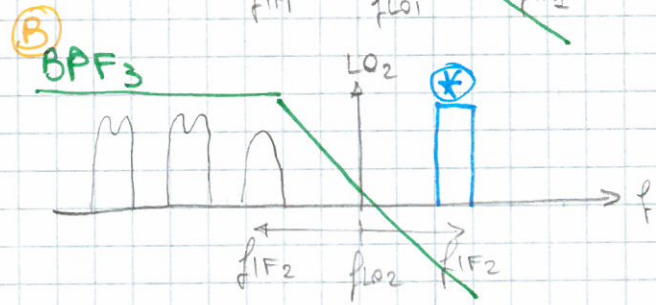
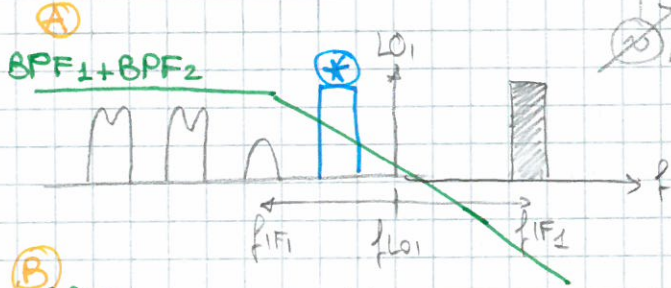
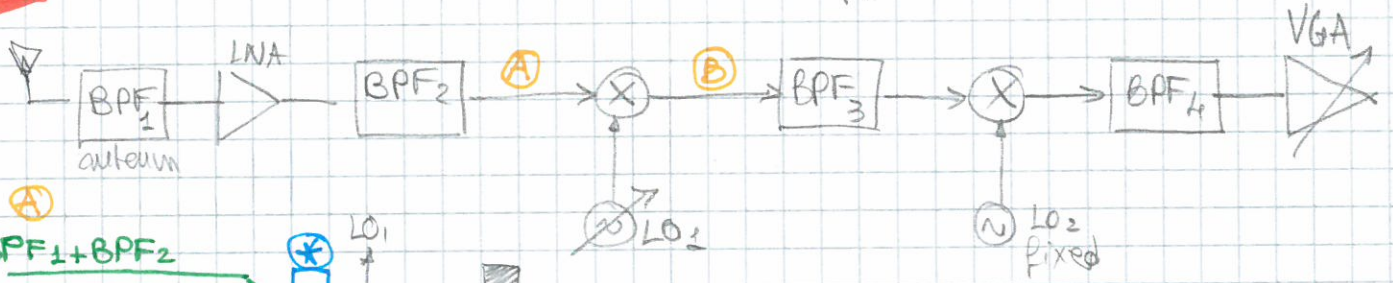
In order to have good rejection we would like high f_{IF}

But:

- Selectivity \rightarrow related to adjacent channels \Rightarrow low f_{IF}
- Sensitivity \rightarrow related to in-band interferers \Rightarrow high f_{IF}

We can see that there is a tradeoff between Sensitivity vs. Selectivity.

11) Dual IF architecture: relax tradeoff (I moved the answer here)



Pros:

- large f_{IF1} : relaxes IR (BPF2)
- small f_{IF2} : relaxes channel selection (BPF4)

Cons:

- more BPF + mixer + LO

Secondary image problem:

⊗ another interferer can couple between f_{RF} and f_{LO1} →

We will get the same problem on f_{IF2}

Solution: use BPF3 to reject the secondary image

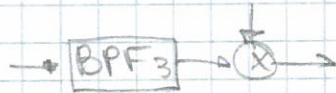
This still works because we're at f_{IF1} → lower freq than

f_{RF}] → BPF3 requirement is still more relaxed:



Single IF

$$Q_1 = \frac{f_{RF}}{2f_{IF}}$$

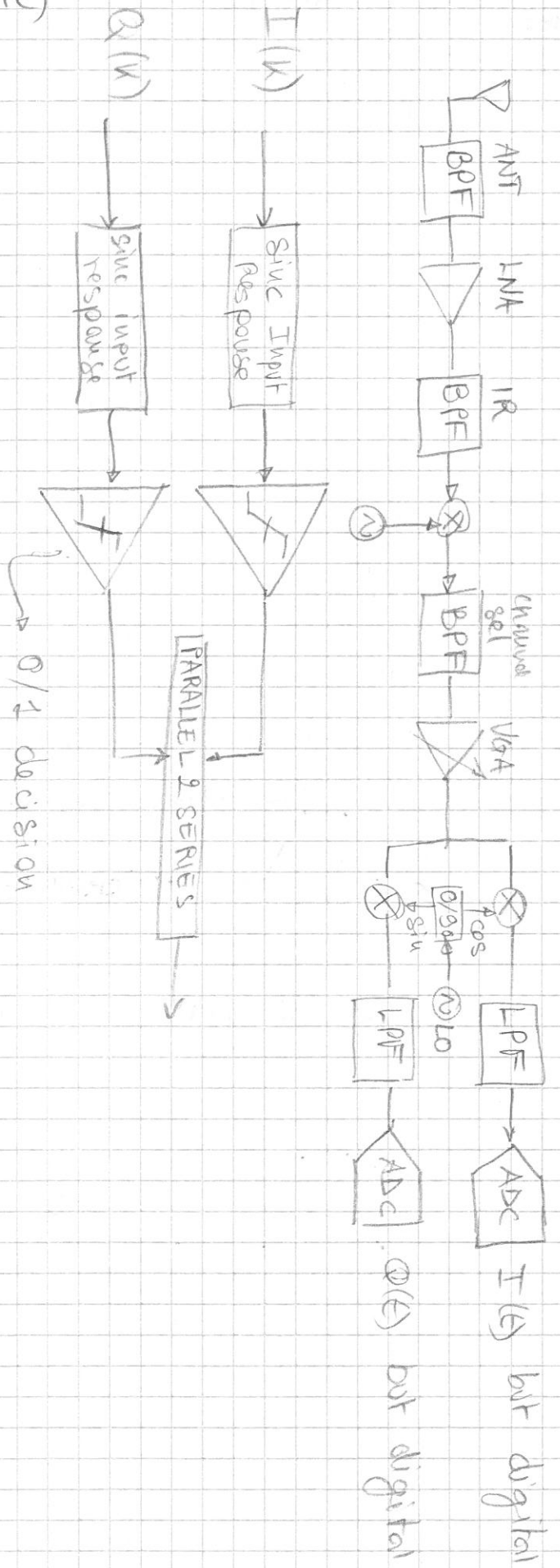


Dual IF

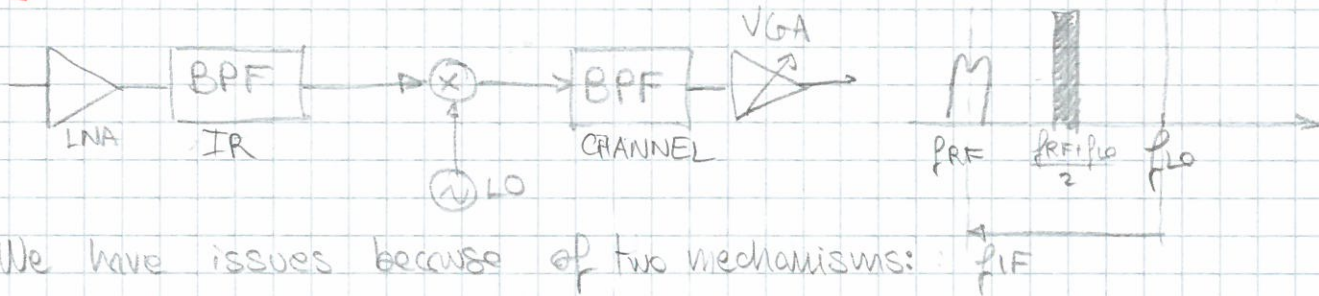
$$Q_2 = \frac{2f_{IF1}}{2f_{IF2}}$$

$$\rightarrow Q_2 \ll Q_1$$

Full architecture (block schematic)

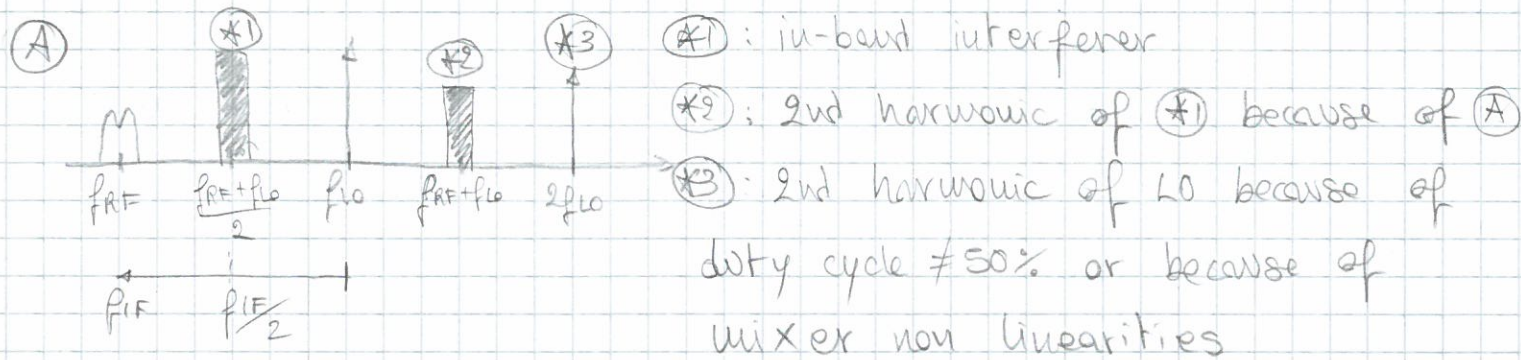


9) Half IF problem

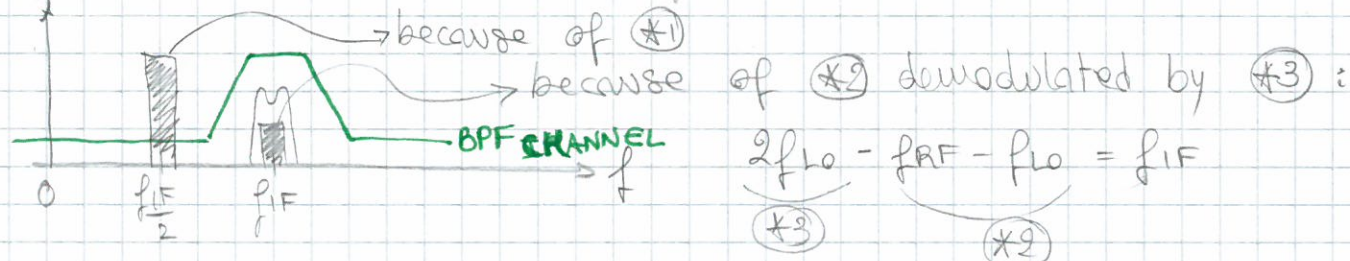


We have issues because of two mechanisms:

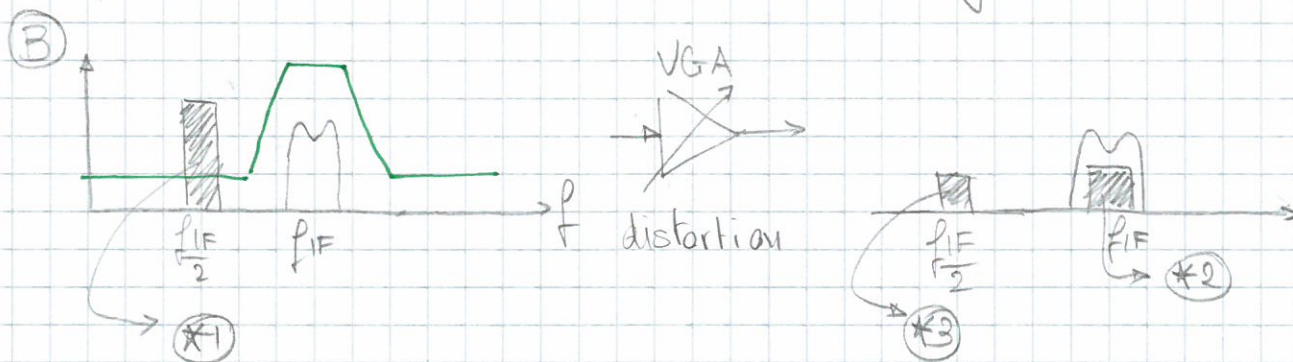
- LO 2nd harmonic + LNA 2nd harmonic (A)
- VGA 2nd order non-linearity (B)



After the mixer we will get



BPF (channel select) can't filter demodulated (A2)
 Result is that we have SNIR degradation.



- (B1): half IF in-band interferer
- (B3): (B1) but gets attenuated by BPF
- (B2): 2nd harmonic of (B3) generated by VGA distortion

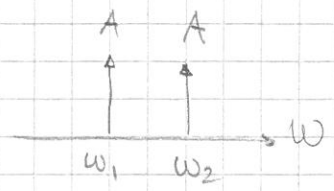
So, after all, also 2nd harmonics can be harmful

10) Second-order non-linearity. IIP2 and link with HD2

Consider $y(t) = \underbrace{\alpha_1}_{\text{linear coefficient}} x(t) + \alpha_2 x^2(t)$

Consider now the usual two tone test, where:

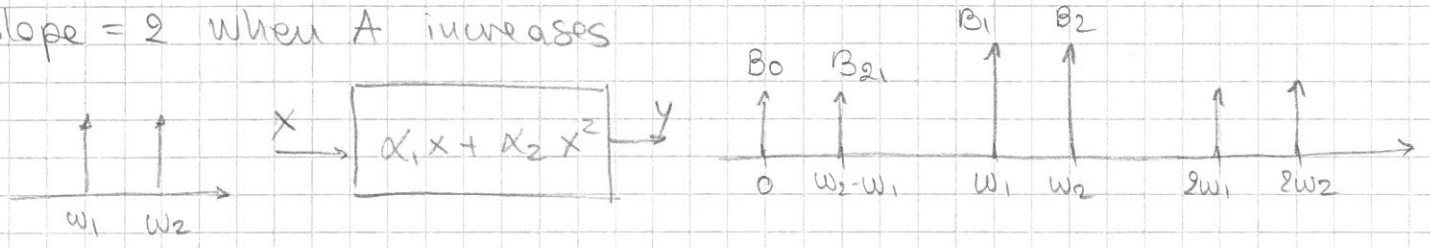
$x(t) = A (\cos \omega_1 t + \cos \omega_2 t)$



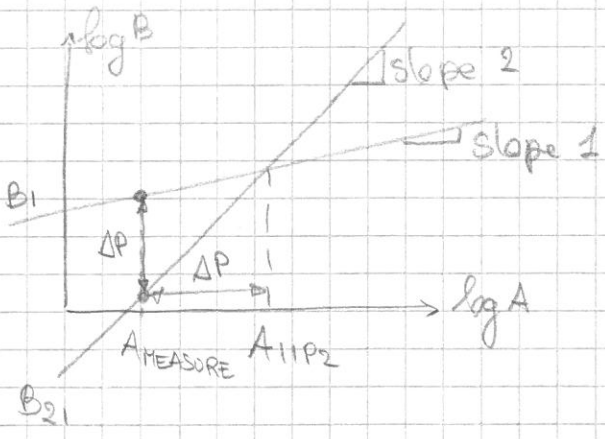
$y(t) = \alpha_1 A (\cos \omega_1 t + \cos \omega_2 t) + \alpha_2 A^2 (\cos(\omega_1 + \omega_2)t + \cos(\omega_1 - \omega_2)t + \dots)$

Quadratic term implies tone generation of low frequencies like $\omega_2 - \omega_1$.

We see that generated tones depend on the square of $A \rightarrow$ slope = 2 when A increases

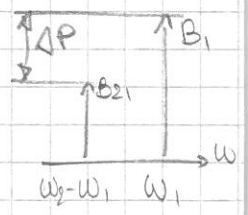


If we calculate B coefficients: $B_1 = B_2 = \alpha_1 A$
 $B_0 = B_{21} = \alpha_2 A^2$

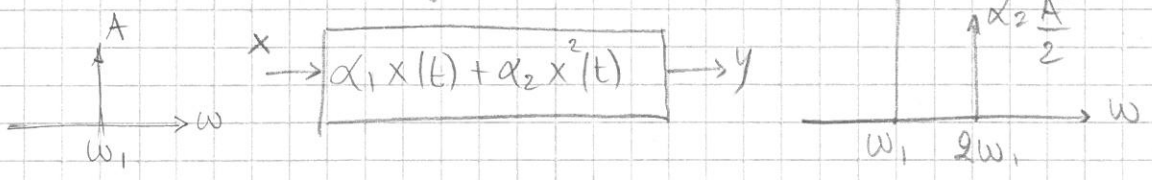


Using the same procedure for IIP3 (with slope difference 1 instead of 2), we found out that

$IIP_2 |_{dBm} = P_{in} |_{dBm} + \Delta P |_{dBm}$



If we feed a single tone instead

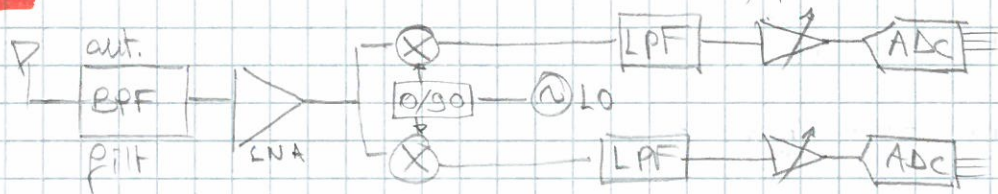


If we want to estimate HD2 through IIP2:

$HD2 = \frac{\alpha_2 A^2 / 2}{\alpha_1 A} = \frac{B_{21}}{B_1} \cdot \frac{1}{2} = \frac{A_{MEASURE}}{A_{IIP2}} \cdot \frac{1}{2}$

$HD2 |_{dB} = -\Delta P |_{dB} - 6dB = P_{in} |_{dB} - IIP2 |_{dB} - 6dB$

12) Zero-IF receivers: architecture, pros/cons, DC offset cancellation



In direct conv. $f_{LO} = f_{RF} \rightarrow f_{IF} = f_{RF} - f_{LO} = 0 \rightarrow \text{Zero-IF}$

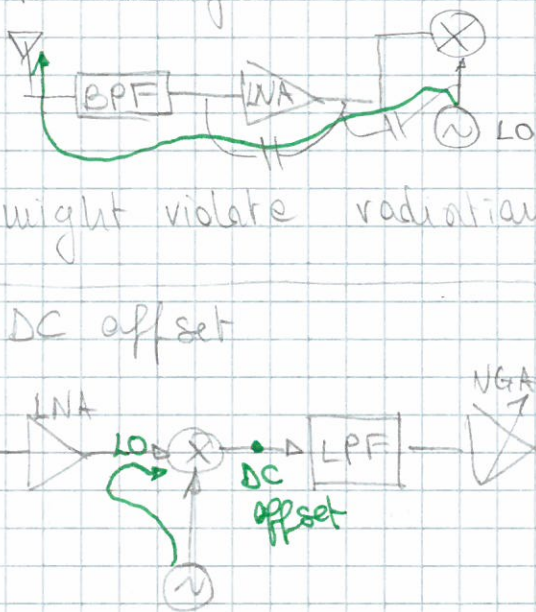
I need two mixers because it wouldn't be possible to recover I/Q

pros: \rightarrow Direct conversion to baseband

- Image problem "apparently" solved \rightarrow No IR filter \rightarrow Suitable for full-integration
- Channel select with: LPF instead of BPF
- No need for off-chip SAW filters
- Mixing spurs are reduced in number \rightarrow simpler to handle
- LNA (integrated) can be optimized for GA, NF, linearity without the 50- Ω requirement

Cons:

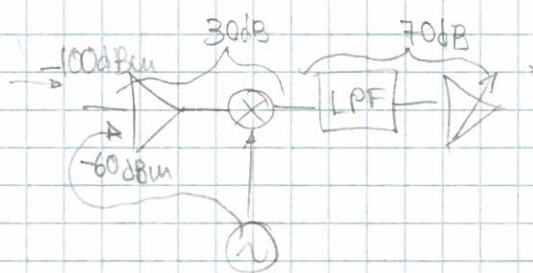
- LO leakage: LO couples through capacitive tracks (mixer, LNA) or the chip substrate and propagates through antenna \rightarrow it might violate radiation limits ($< -50/80 \text{ dBm}$). This can be reduced using differential LOs.
- DC offset: LO couples through RF part of the mixer \rightarrow self mixing introduces DC offsets.



LO couples through RF part of the mixer \rightarrow self mixing introduces DC offsets

eg: signal = -100 dBm
LO leak = -60 dBm

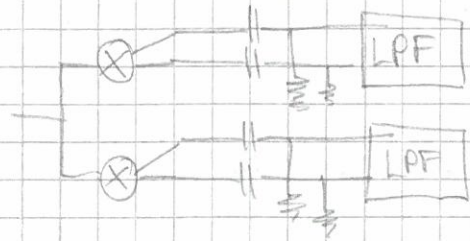
\rightarrow LNA + mixer gain = $+30 \text{ dB}$
 \rightarrow LPF + VGA gain = $+70 \text{ dB}$
 \rightarrow 100 dB gain



At LPF in \rightarrow 100 μV signal
 \rightarrow 10 μV DC os
 \rightarrow 30V at VGA out

DC offset cancellation techniques

- AC coupling: CR-LPF has to be low enough not to degrade BW of signal $\rightarrow \frac{f_{low}}{1000} = f_{CR}$



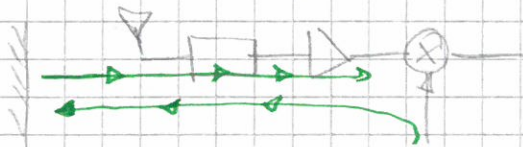
We have differential paths \rightarrow implementation of \approx big capacitors to keep resistor noise low

- Switched cap



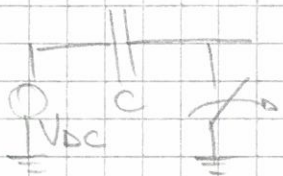
Using Time division of multiple samples (TDMA), we can do a zero setting to cancel DC offset.

Problem: we store interferers during sampling. DC os also varies over time because of reflections



We can average samples over time to isolate DC os.

Problem: switching cap noise



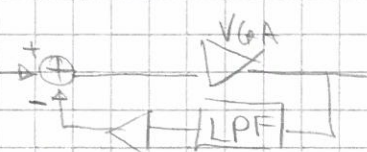
$$V_C = V_{DC} + V_{noise}$$

$$V_{noise}^2 = \frac{KT}{c}$$

$$SNR = \frac{P_s}{\frac{KT}{c}} = \frac{-93dBV @ 50\Omega}{-108dBV} \rightarrow C > 250pF$$

So we have $4 \cdot 250pF = 1nF$ total capacitance \rightarrow still too large

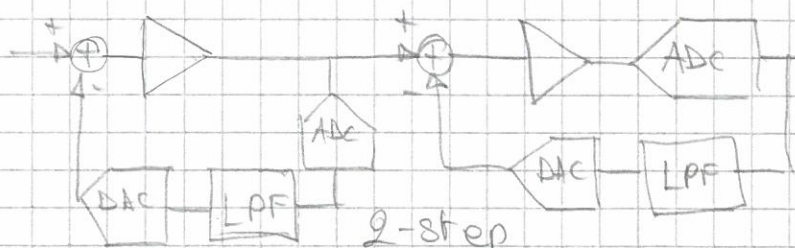
- Feedback



Using analog we can demonstrate that caps for LPF have to be even larger than CR-LPF



1-step

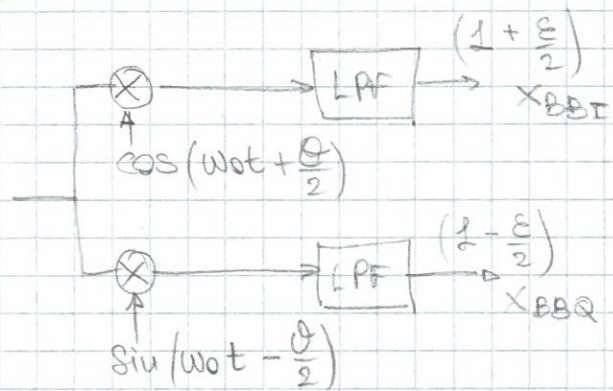


2-step

ADCs for OS estimation: they don't need to be high speed (we want DC only). They have to be precise. It's convenient to have ADC an

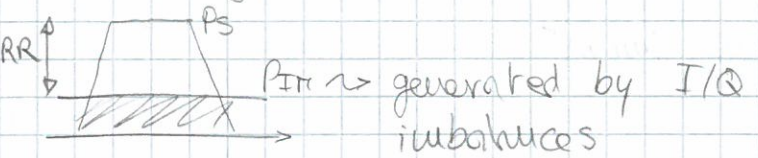
in this two step approach is to avoid V/A estimation

3) Zero-IF: I/Q mismatches on SNR. LO leakage impact



Consider a fixed delay τ on RF-mixer
 $\sin(\omega t - \frac{\theta}{2}) = \sin(\omega t - \frac{\omega_0 \tau}{2})$

θ shift depends on $\omega_0 \rightarrow$ CRITICAL
 When using RF-to-DC conversion



$$X_{RF}(t) = X_I(t) \cos \omega t + X_Q(t) \sin \omega t$$

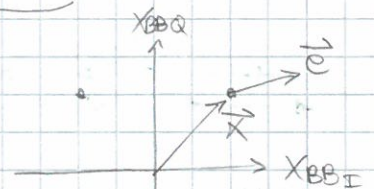
$$X_{LO1} = 2 \left(1 + \frac{\epsilon}{2}\right) \cos \left(\omega t + \frac{\theta}{2}\right) \quad X_{LO2} = 2 \left(1 - \frac{\epsilon}{2}\right) \sin \left(\omega t - \frac{\theta}{2}\right)$$

$$X_{BB1}(t) = X_{RF} \cdot X_{LO1} = X_I(t) \left(1 + \frac{\epsilon}{2}\right) \cos \frac{\theta}{2} - X_Q(t) \left(1 + \frac{\epsilon}{2}\right) \sin \frac{\theta}{2}$$

$$X_{BBQ}(t) = X_{RF} \cdot X_{LO2} = X_Q(t) \left(1 - \frac{\epsilon}{2}\right) \cos \frac{\theta}{2} - X_I(t) \left(1 - \frac{\epsilon}{2}\right) \sin \frac{\theta}{2}$$

wanted

leaked



IRR = Image rejection ratio = $P_S / P_{I\pi}$

$$SNR = IRR = \frac{|X|^2}{|e|^2} = \frac{|X|^2}{|X_{BB} - X|^2} \quad \text{where } |X| = 2$$

for small θ :

$$X_{BB1} \approx X_I \left(1 + \frac{\epsilon}{2}\right) \cos \frac{\theta}{2} - X_Q \left(1 + \frac{\epsilon}{2}\right) \sin \frac{\theta}{2}$$

$$X_{BBQ} \approx X_Q \left(1 - \frac{\epsilon}{2}\right) \cos \frac{\theta}{2} - X_I \left(1 - \frac{\epsilon}{2}\right) \sin \frac{\theta}{2}$$

$$SNR = \frac{|X|^2}{|e|^2} = \frac{|X|^2}{|X_{BB} - X|^2} = \frac{2}{(X_{BB1} - X)^2 + (X_{BBQ} - X)^2} = \frac{4}{\left[\left(1 + \frac{\epsilon}{2}\right)\left(1 - \frac{\theta}{2}\right) - 1\right]^2 + \left[\left(1 - \frac{\epsilon}{2}\right)\left(1 - \frac{\theta}{2}\right) - 1\right]^2} = \frac{4}{\epsilon^2 + \theta^2} = \frac{1}{\left(\frac{\epsilon}{2}\right)^2 + \left(\frac{\theta}{2}\right)^2}$$

We can't get over ≈ 30 dB total.

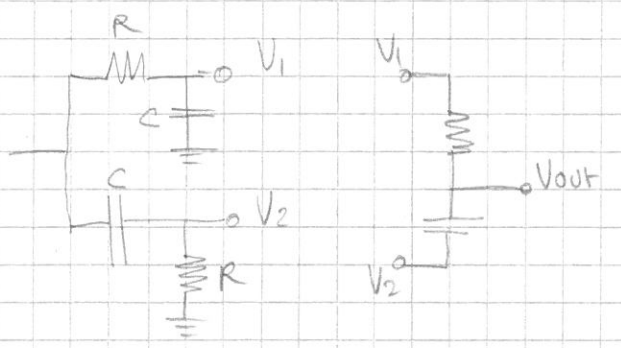
e.g: $\epsilon \approx 0,1$, $\theta = 1^\circ = 0,0174$ rad $IRR = 10 \log \left(\frac{4}{\epsilon^2 + \theta^2}\right) = 25,9$ dB

Mixers used in direct conversion have to be accurate in order

not to introduce large phase imbalances.

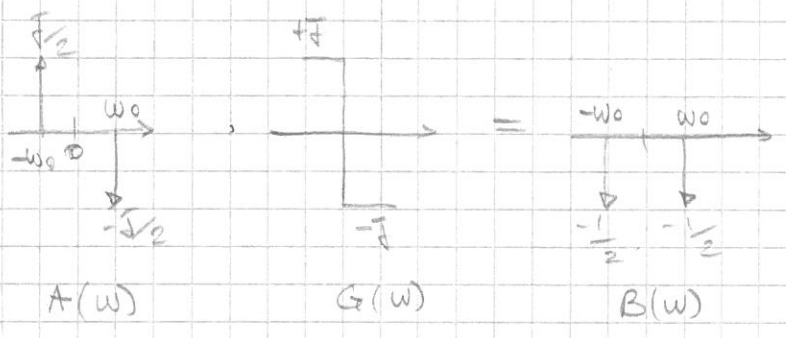
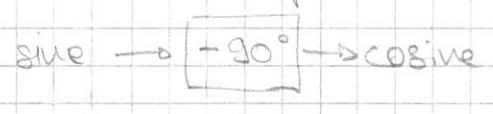
14) Image-reject receivers: 90° -shift. Hartley and IRR effects

Heterodyne receivers \rightarrow OFF-chip SAW required (therefore 50- Ω matching required). Hartley receiver solves this



Two possible 90° shift implementations
(Note: works for 1 frequency only)

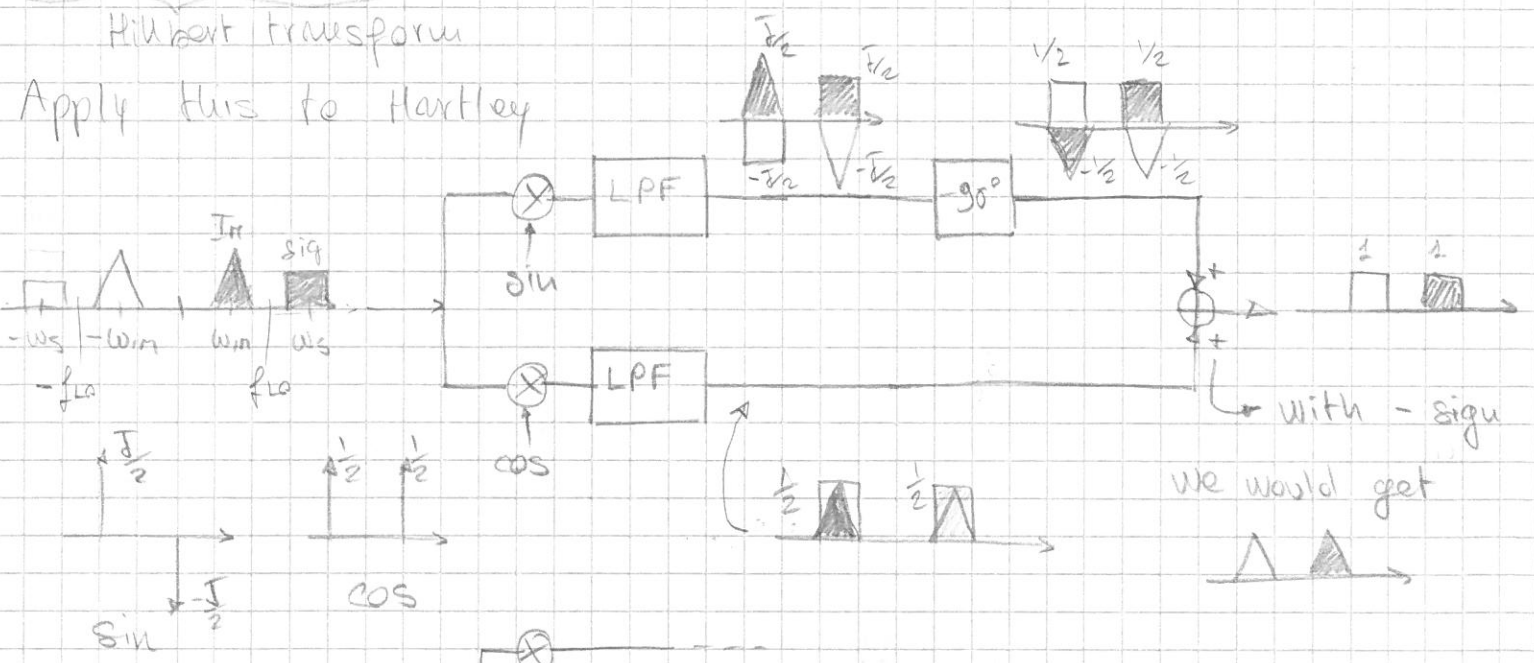
Hilbert transform:



$B(w) = G(w) \cdot A(w)$ where $G(w) = -j \text{sign}(w)$

Hilbert transform

Apply this to Hartley

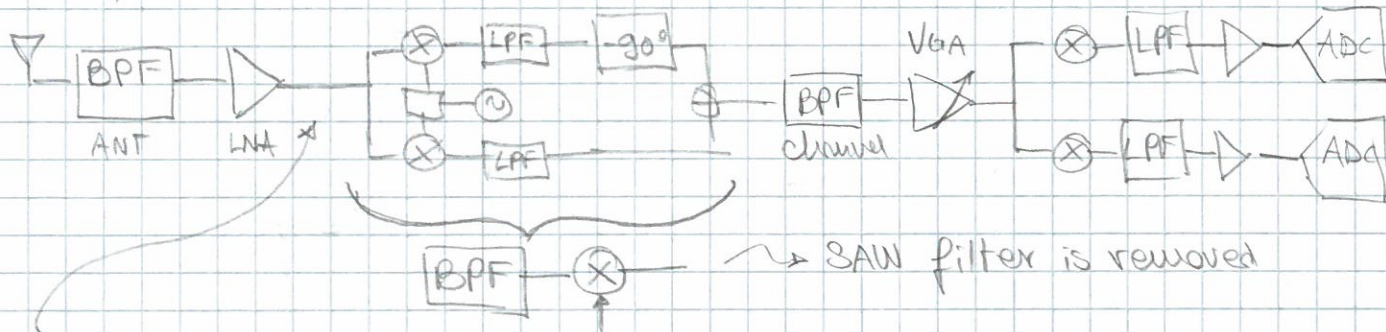


If we consider $(1 - \frac{\epsilon}{2}) \sin(\omega t - \frac{\theta}{2})$ and $(1 + \frac{\epsilon}{2}) \cos(\omega t + \frac{\theta}{2})$ Then $IRR = \frac{P_s}{P_{in}} = \frac{4}{\epsilon^2 + \theta^2}$

Hartley architecture can't go over 30dB image suppression.

On the other hand, if Hartley + BPF is used, BPF requirements will be more relaxed thanks to the 30dB IRR

Hartley IR receiver



An additional IR BPF goes here in case 30dB IRR is not enough.

15) Image-reject receivers: Weaver architecture

Hartley cons:

- phase shifting works only at $f_{pole} \rightarrow$ narrow BW
- sensitive to RC absolute accuracy $\frac{\Delta T}{T} = \frac{\Delta R}{R} + \frac{\Delta C}{C} \rightarrow$ low IRR
- phase shift introduces noise + power loss

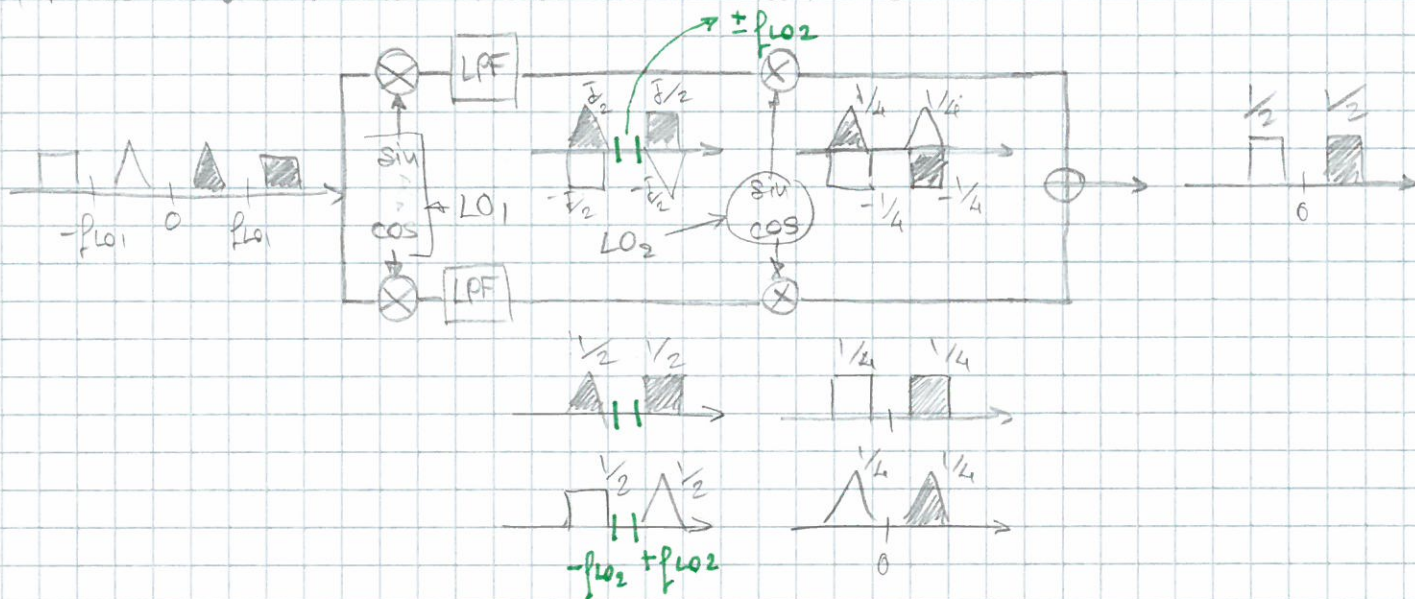
Weaver architecture pros:

- Solves phase shifter issue

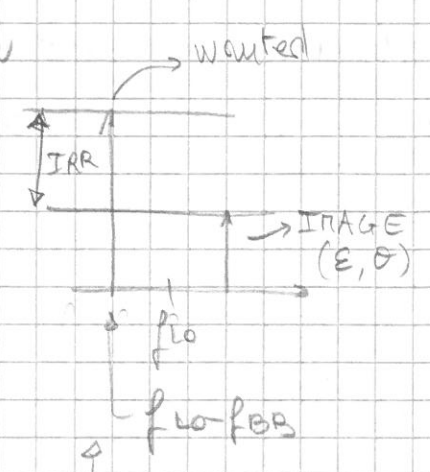
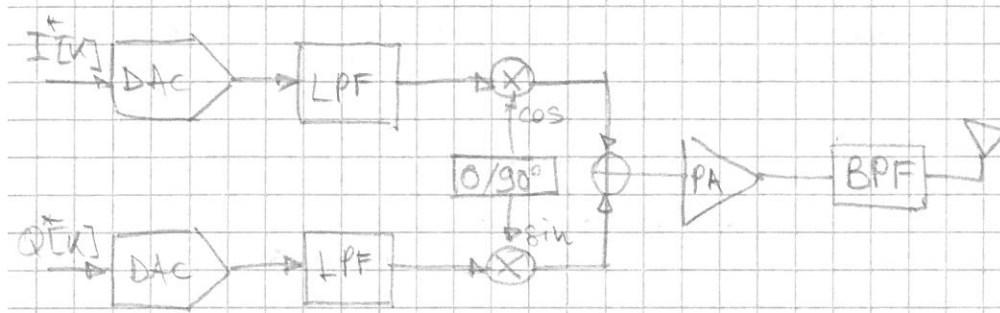
Weaver cons:

- requires more mixers
- suffers from secondary image problem \rightarrow to mitigate this we would need BPFs instead of LPFs or a zero-IF IR

(This would mean two more mixers)



16) TX: I/Q mismatch effects. Direct - conv



If $I(t) = \cos \omega_c t$ $Q(t) = \sin \omega_c t$ then

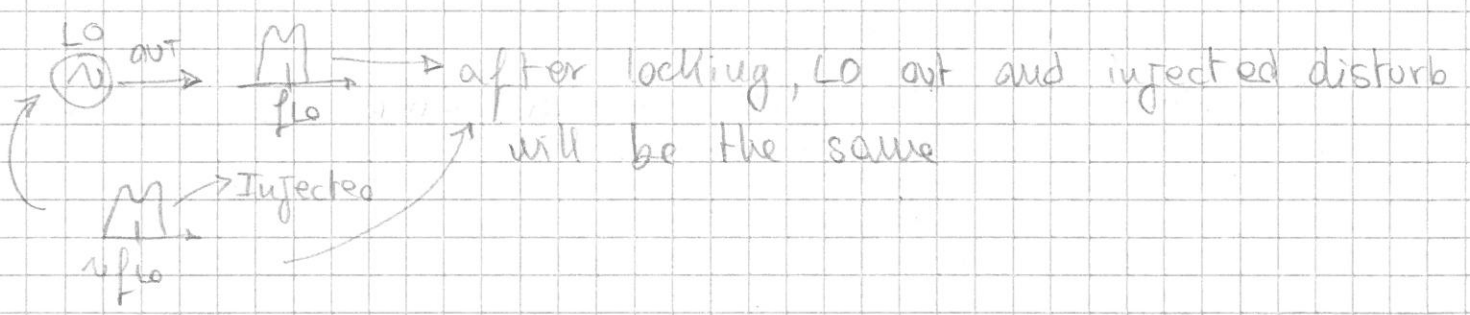
$$\cos \omega_c t \cos \omega_c t - \sin \omega_c t \sin \omega_c t = \cos(2\omega_c t)$$

If we introduce ϵ, θ imbalances we find $IRR = \frac{P_s}{P_{in}} = \frac{4}{\epsilon^2 + \theta^2}$

In a direct TX, signal after PA self couples back into LO by em coupling or through IC substrate \rightarrow Injection locking

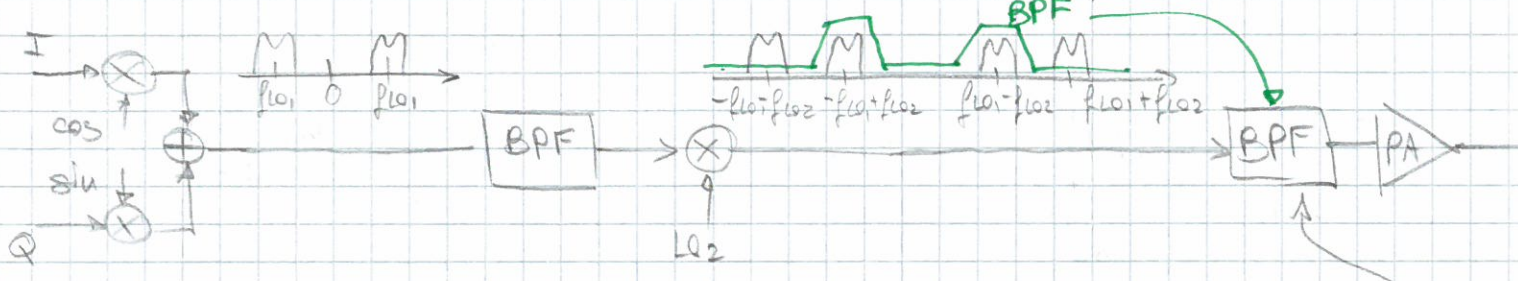
If coupled freq is $\approx f_{LO}$, LO will tend to follow that injection like in a PLL. This depends on Q factor of the osc.

Same happens if injection isn't just a frequency but a band:



17) 2-step TX SSB mixer

We shift PA frequency band in order not to have it similar to LO frequency (thus reducing injection locking)



f_{lo2} does not need to be really high. Its purpose is to change the $f_{out} = f_{lo1} - f_{lo2}$ frequency in order not to be similar to f_{lo2} . BPF is used to reject the unwanted side-band

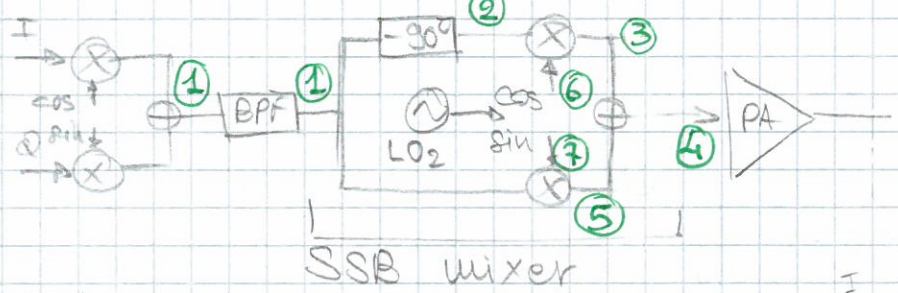
Pros:

- Avoids LO_1 pulling from PA injection
- Improves I/Q matching

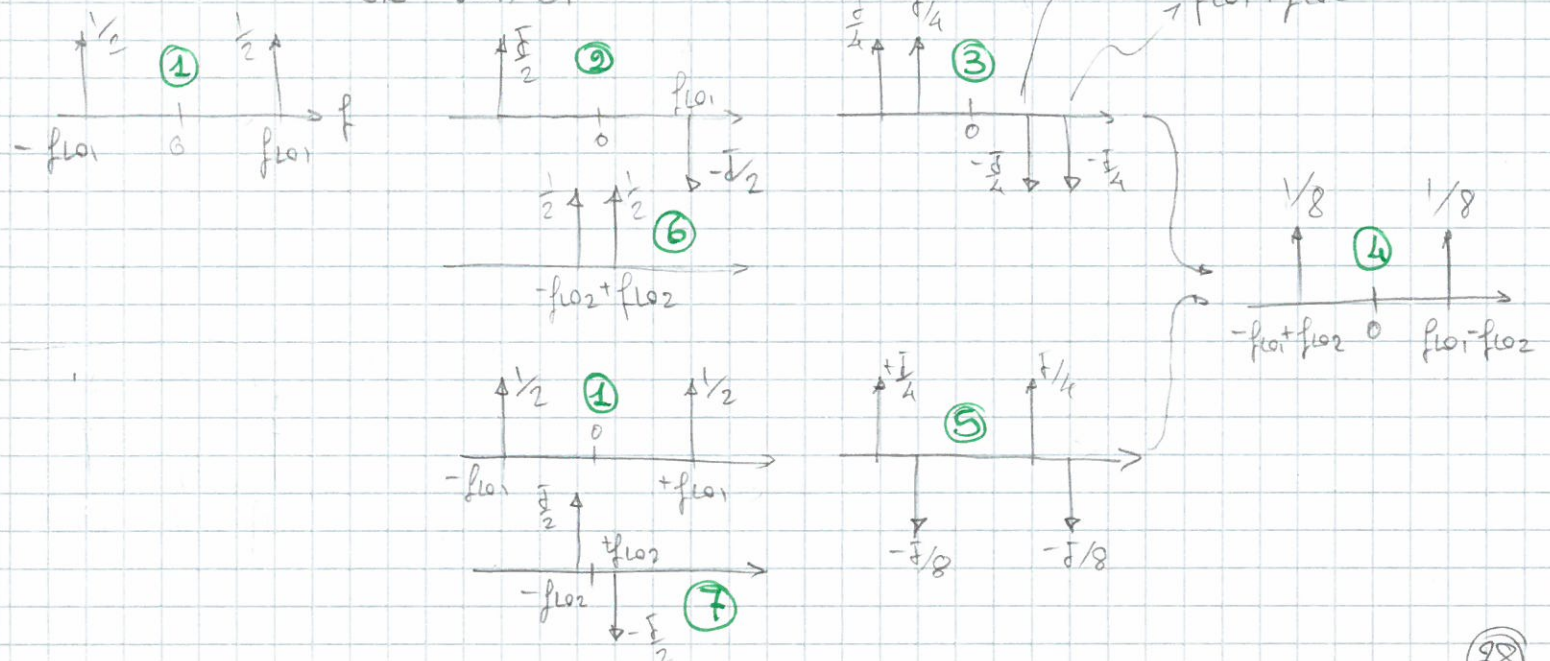
Cons:

- BPF used for reducing the side-band has to provide 50/60 dB

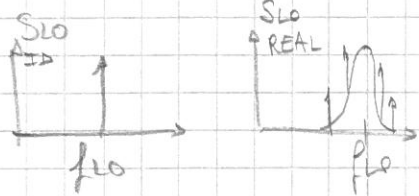
To solve the latter problem we use SSB (Single Sideband Mixer):



Suppose that ④ is a cosine. (not a band)

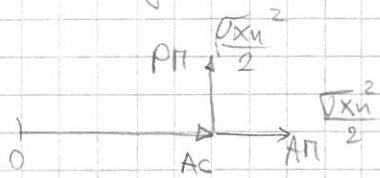


18) AM / FM disturbances. Phase/voltage spectrum relationship



Real LO spectrum will have a bandwidth + spurious tones that damage the ideal (wanted) S for modulation.

According to Rice's theorem, noise will split into AM/PM:



AM can be avoided by hard limiting the carrier (the second order effect of PM by clipping AM signals is not considered here).

Why phase noise is a problem?

Recall $\varphi_n(t) = \int_{-\infty}^t \omega_n(\tau) d\tau \rightarrow$ phase noise comes from the integration of frequency noise

If we consider spectrums, then:

$S_\varphi = |H(f)|^2 S_x$ for a LTI system. In our case we want integration of S_x , therefore $H(s) = \frac{1}{s} \rightarrow |H(f)|^2 = \frac{1}{4\pi^2 f^2}$

$S_\varphi = \frac{S_x}{4\pi^2 f^2} \rightarrow$ If S_x is white $\Rightarrow S_\varphi$ has $\frac{1}{f^2}$ component

called random walk noise

Noise integration can build up a phase shift that can go to ∞ , thus generating issues like sync etc..

Quantify PM noise in a spectrum

consider:

In general: disturbs + noise (unwanted)

$$x_c(t) = A_c \cos[\omega_c t + \varphi_m(t)] \text{ where } \varphi_m(t) \text{ is a sinusoidal}$$

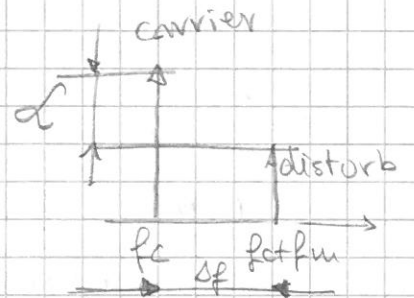
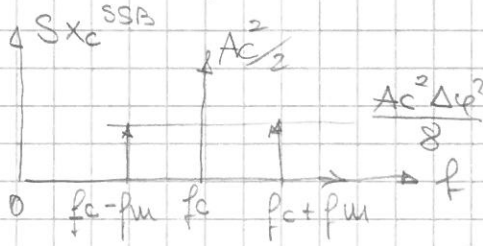
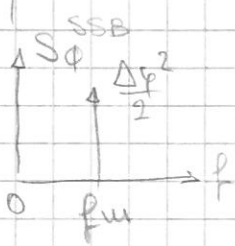
spurious tone $\varphi(t) = \Delta\varphi \cos \omega_m t$

We then have:

$$x_c(t) \approx A_c \cos \omega_c t - A_c \Delta\varphi \cos \omega_m t \cos \omega_c t =$$

NB FM } $\Delta\varphi \ll 1 \text{ rad}$

$$A_c \cos \omega_c t - \frac{A_c \Delta\varphi}{2} \cos(\omega_m + \omega_c)t + \frac{A_c \Delta\varphi}{2} \cos(\omega_m - \omega_c)t$$



We can see that disturbance $\varphi_m(t)$ generates two spurs.

Let's define a sort of SNR called α , in this case:

$$\alpha(\Delta f) = \frac{S(f_c \pm f_m)}{P_c} \rightarrow \text{Spectrum at a single band}$$

P_c \rightarrow power of the carrier

Δf \rightarrow frequency offset Δf from the carrier frequency f_c

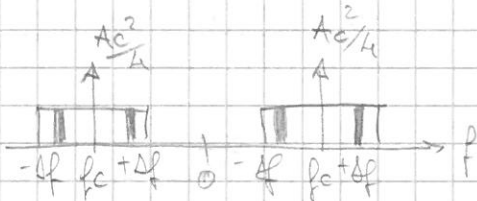
$$\Delta f = (f_m + f_c) - f_c = f_m$$

$\alpha(\Delta f) \triangleq$ single sideband to carrier ratio (SSCR)

$$\text{In our case } \alpha(f_m) = \frac{\frac{A_c^2 \Delta\varphi^2}{4}}{\frac{A_c^2}{2}} = \frac{\Delta\varphi^2}{4}$$

Consider now white noise: We can apply the same reasoning

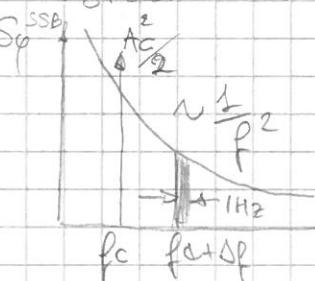
by selecting just a narrow band of



the white noise at $\pm \Delta f$ offset from f_c

typically defined in a 1 Hz bandwidth

Consider now 1/f noise.



Same reasoning. Note that for low f noise diverges

so NB FM cannot be applied anymore

(we assumed $\Delta\varphi \ll 1 \text{ rad}$)

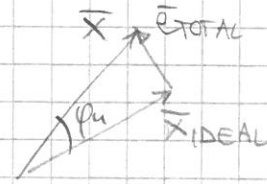
18) EVM degradation, reciprocal mixing

Amplitude error ϵ :

- can come from phase shifter (cos/sine have different amp)
- " " " mixer amplitude mismatches

Phase error θ :

- can come from tracks delay after mixers
- can come from bad phase shifting



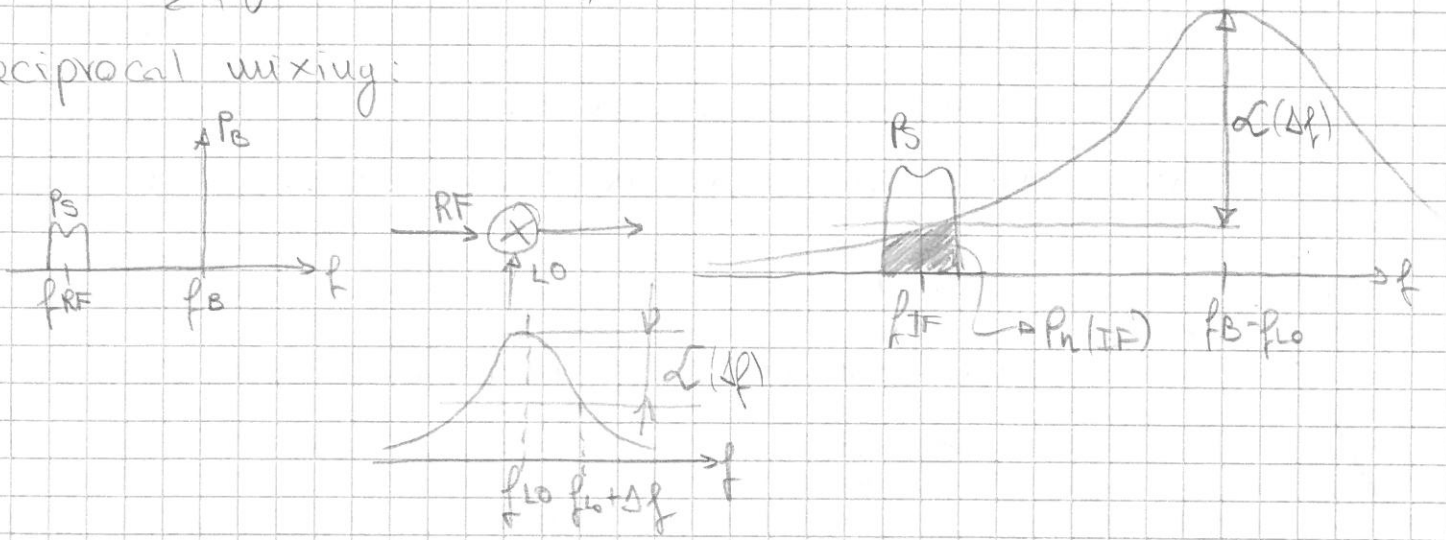
$EVM = \text{error vector magnitude} = \frac{1}{SNR}$

$$EVM = \frac{\frac{1}{N} \sum_{i=1}^N |\bar{e}_i|^2}{\text{Powerage}} = \frac{|\bar{e}_{TOTAL}|^2}{|X_{ID}|^2} = \frac{(\sum |X_{ID}| \cos(\theta_m))^2}{|X_{ID}|^2} \approx \sigma_\theta^2$$

Regardless of power generated we still have the same EVM

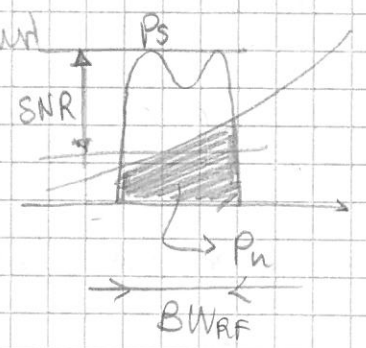
$EVM = \frac{4}{\epsilon^2 + \theta^2}$ \rightarrow see question number 13 for demonstration

Reciprocal mixing:



A blocker couples in the mixer \rightarrow power from blocker * LO spectrum can be downconverted directly down to signal band

We defined: $\alpha(\Delta f) = \frac{P_{noise}(f_{IF}) \text{ in } 1\text{Hz BW}}{P_{blocker}}$



So, we can say that:

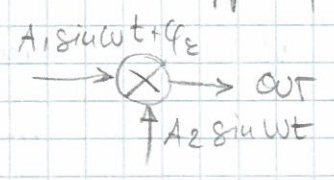
$$SNR = \frac{P_s}{P_{noise}(f_{IF}) |_{\text{full BW}}} = \frac{P_s}{\alpha(\Delta f) \cdot P_B \cdot BW_{RF}}$$

\rightarrow Not just 1Hz $SNR|_{dB} = P_s|_{dB} - P_B|_{dB} - \alpha(\Delta f)|_{dbc} - 10 \log_{10}(BW_{RF})$

Be careful

20) PD based on multiplier. Phase model of PLL. Non-linear-diff eq.

Multiplier: multiplies input and modulation sig
 Consider:

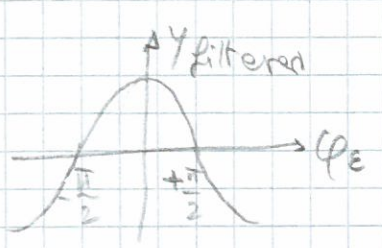


$x_1 = A_1 \sin(\omega t + \phi_e)$ $x_2 = A_2 \sin(\omega t)$ then
 $y = x_1 \cdot x_2 = \frac{A_1 A_2}{2} \left(\underbrace{-\cos(2\omega t + \phi_e)}_{2\omega} + \underbrace{\cos(\phi_e)}_{\text{DC sig}} \right)$



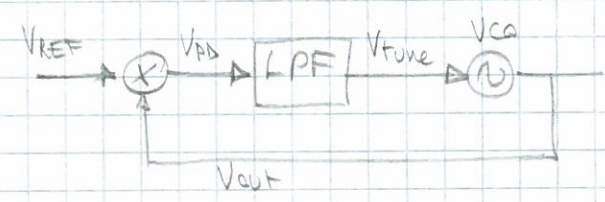
If we filter y with $f_{\text{pole}}|_{\text{LPF}} \ll 2\omega$ then

$y|_{\text{filtered}} \approx \frac{A_1 A_2}{2} \cos(\phi_e) \rightsquigarrow$ no ω dependence



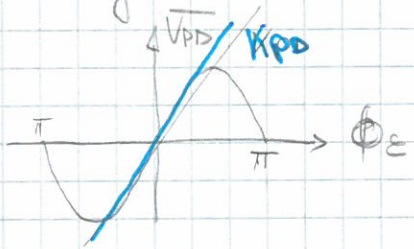
Change of notation:

$V_{\text{REF}} = A_{\text{REF}} \sin(\omega_{\text{ref}} t + \phi_{\text{ref}})$
 $V_{\text{out}} = A_{\text{out}} \cos(\omega_{\text{out}} t + \phi_{\text{out}})$
 $V_{\text{TUNE}} = K_{\text{PD}} \sin(\phi_{\text{ref}} - \phi_{\text{out}}) = K_{\text{PD}} \sin \phi_e$
 $\phi_e = \phi_{\text{ref}} - \phi_{\text{out}} = \text{phase error}$



V_{out} is a cos instead of a sine because to have a "static" situation, $V_{\text{PD}} = 0$ therefore V_{REF} and V_{out} must be shifted by $\pm \frac{\pi}{2}$

Using this notation we get a new plot



for small ϕ_e $\bar{V}_{\text{PD}} = V_{\text{TUNE}} = K_{\text{PD}} \sin \phi_e \sim K_{\text{PD}} \phi_e$
 $K_{\text{PD}} = \text{gain of the phase detector}$

Since $\omega_{\text{out}} = \omega_{\text{FR}} + K_{\text{VCO}} \cdot V_{\text{TUNE}}$ and since $\omega = \frac{d\phi}{dt}$ we can say:

$\frac{d\phi_{\text{out}}}{dt} = \omega_{\text{FR}} + K_{\text{VCO}} V_{\text{TUNE}}(t)$ and therefore get ϕ_e derivative

$\frac{d\phi_e}{dt} = \frac{d\phi_{\text{REF}}}{dt} - \frac{d\phi_{\text{out}}}{dt} = (\omega_{\text{REF}} - \omega_{\text{FR}}) - K_{\text{VCO}} V_{\text{TUNE}}(t)$
 $= \Delta\omega - \underbrace{K_{\text{VCO}} K_{\text{PD}}}_{K} \sin \phi_e =$

operator is linear

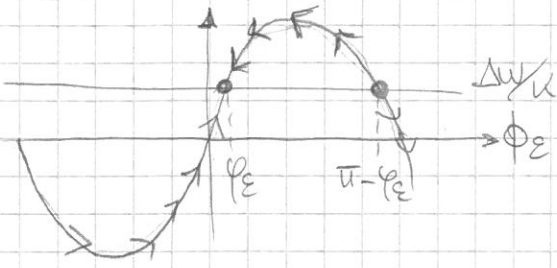
$\frac{d\phi_e}{dt} = \Delta\omega - K \sin \phi_e \rightarrow$ first order differential equation

(considering LPF contribution would complicate things)

Brief analysis of the PLL non lin differential eq.

- analyze stable equilibrium points

$$\dot{\Phi}_E = \Delta\omega - K \sin \Phi_E \rightarrow \dot{\Phi}_E = 0 \rightarrow \sin \Phi_E = \frac{\Delta\omega}{K}$$



Two equilibrium points at Φ_E and $\pi - \Phi_E$

- $\sin \Phi_E > \frac{\Delta\omega}{K} \rightarrow \dot{\Phi}_E$ is decreasing
 - $\sin \Phi_E < \frac{\Delta\omega}{K} \rightarrow \dot{\Phi}_E$ is increasing
- System will tend to Φ_E equilibrium point.

If $|\frac{\Delta\omega}{K}| > 1 \rightarrow$ no crossing with the plot \rightarrow sys is out of lock

- lock state $\dot{\Phi}_E = 0$ $\omega_E = \omega_{REF} - \omega_{out} = 0 \rightarrow \underline{\omega_{REF} = \omega_{out}}$

For small phase perturbations; non linear diff eq becomes

$$\dot{\Phi}_E = \Delta\omega - K \sin \Phi_E \approx -K \Phi_E \rightarrow \text{Laplace } s \Phi_E = -\Phi_E \cdot K$$

↑
linearize

Same thing happens for Φ_{out} .

$$s \Phi_{out} = \omega_{REF} + K \sin \Phi_E \approx K \Phi_E = K [\Phi_{REF} - \Phi_{out}] = s \Phi_{out} \quad (*)$$

We want to find out response vs input:

$$T(s) = \frac{\Phi_{out}}{\Phi_{REF}} = \frac{K}{s+K}$$

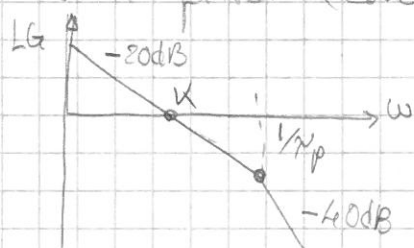
Φ_{out} "follows" Φ_{ref} until pole frequency at K

If Φ_{ref} is noisy, we will see that Φ_{out} "filters" ref phase noise

2) 2nd order PLLs: stability and $T(s)$. Static phase error after n -th order input, freq response

• Without filter $\rightarrow LG(s) = K_{PD} \cdot F(s) \cdot \frac{K_{VCO}}{s} = \frac{K}{s}$

• With filter (consider single-pole LPF) $LG(s) = \frac{K}{s} \cdot \frac{1}{1+sT}$

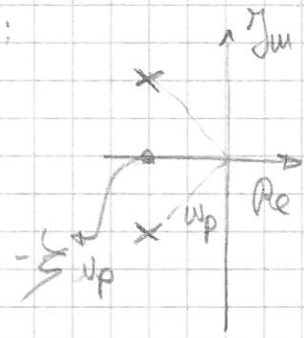


\rightarrow It has to cut at -20dB/dec , otherwise system will not be stable

Assume good phase margin

Let's design a system with 45° conjugate poles to have a good trade-off between overshoot / settling time:

$$T(s) = \frac{\phi_{out}}{\phi_{ref}} = \frac{LG(s)}{1+LG(s)} = \frac{1}{s^2 \frac{T}{K} + s \frac{T}{K} + 1} = \frac{1}{\frac{s^2}{\omega_p^2} + \frac{2\zeta s}{\omega_p} + 1}$$



$$\omega_p = \sqrt{\frac{K}{T}} \quad \zeta = \frac{1}{2\sqrt{KT}} = \frac{\sqrt{2}}{2} \rightarrow K = \frac{1}{2T}$$

trade-off

We see that K has to be one octave before pole $\frac{1}{T}$

$$\omega_p = \sqrt{\frac{K}{T}} = \sqrt{2} K \rightarrow \text{BW is now } \sqrt{2} K, \text{ slightly higher}$$

Note: $K = 1/2T$

BW of closed loop is often approximated to just 0dB cut of $LG(s)$.

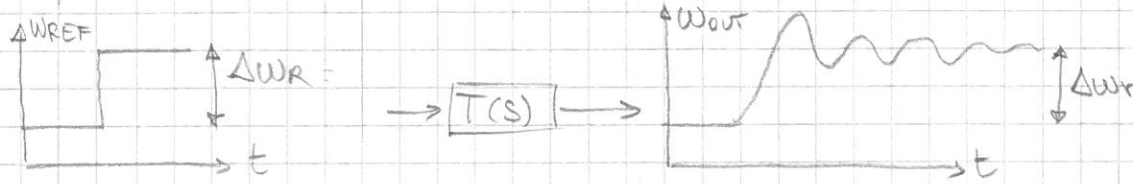
Phase margin $\phi_m = 90^\circ - \arctan\left(\frac{\omega_0}{\omega_p}\right) \stackrel{\omega_0 = \frac{1}{2}\omega_p}{=} 90^\circ - 27^\circ \approx 63^\circ$

ϕ_m is limited by the overshoot / settling time condition

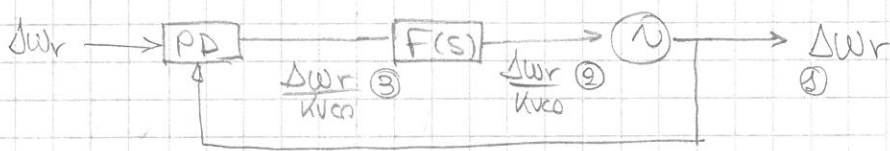
Static phase error for a 2nd order PLL

$$T(s) = \frac{\phi_{out}}{\phi_{ref}} \Rightarrow \frac{s \cdot \phi_{out}}{s \cdot \phi_{ref}} = \frac{\omega_{out}}{\omega_{ref}} \Rightarrow T(s) \text{ is valid as reference}$$

to output transfer function. We can then analyze the frequency response for a reference step.



Like it happens for the phase, we experience overshoot and settling time for output frequency ω_{out} .



Qualitative approach: consider steady-state condition where

$$\omega_{out} = \Delta\omega_r \rightarrow \omega_{TUNE} = \frac{\Delta\omega_r}{K_{VCO}} \text{ at steady-state } F(s) = 1 \text{ so}$$

$$\omega_{PD} = \frac{\Delta\omega_r}{K_{VCO}} \text{ as well } \rightarrow \phi_E = \frac{\Delta\omega_r}{K_{VCO}} = \frac{\Delta\omega_r}{K} \rightarrow \text{There is a small, finite error}$$

This error is not null because of the final value theorem:

$\lim_{t \rightarrow \infty} y(t) = \lim_{s \rightarrow 0} s \cdot Y(s)$ where $Y(s)$ is the Laplace transform of $y(t)$. In our case $Y(s) = \frac{\phi_E(s)}{\phi_{REF}(s)} \cdot \phi_{REF}(s)$

Ingredients we need:

• Transfer function $\rightarrow \frac{\phi_E(s)}{\phi_{REF}(s)} = \frac{1}{1 + LG(s)} = 1 - T(s) = \frac{s(1+sT)}{K + s(1+sT)}$

• Input step $\rightarrow \frac{d\phi_{REF}}{dt} = \omega_{REF} \xrightarrow{\mathcal{L}} s\phi_{REF} = \omega_{REF} \rightarrow$ if ω is an input step

then (using Laplace) $\omega_{REF}(s) = \frac{\Delta\omega_r}{s} \rightarrow \phi_{REF}(s) = \frac{\omega(s)}{s} = \frac{\Delta\omega_r}{s^2}$

We can assemble the two, so $Y(s) = \phi_E(s) = \frac{\phi_E(s)}{\phi_{REF}(s)} \cdot \phi_{REF}(s)$

$$\phi_E(s) = \frac{\Delta\omega_r}{s^2} \cdot \frac{s(1+sT)}{K + s(1+sT)}$$

Let's now apply the theorem:

$$\lim_{t \rightarrow \infty} \varphi_e(t) = \lim_{s \rightarrow 0} s \cdot \Phi_e(s) = \lim_{s \rightarrow 0} \cancel{s} \frac{\Delta \omega_r}{\cancel{s}} \cdot \frac{\cancel{s}(1+s^N)}{K + \cancel{s}(1+s^N)} = \frac{\Delta \omega_r}{K}$$

static phase error

We found the same result we got intuitively

What would happen if we applied a phase step instead?

$$\Phi_{REF}(s) = \frac{\Delta \phi}{s} \rightarrow \lim_{s \rightarrow 0} \cancel{s} \frac{\Delta \phi}{\cancel{s}} \cdot \frac{s(\quad)}{K + \cancel{s}(\quad)} = 0$$

- frequency step \rightarrow error is $\Delta \omega_r / K$
- phase step \rightarrow null error

If we wanted to null the frequency step static phase error?

In general: $\lim_{s \rightarrow 0} s \cdot \frac{\Delta}{s^m} \frac{s^n H(s)}{s^n H(s) + K} = \lim_{s \rightarrow 0} \frac{\Delta}{K} s^{n-m+1}$

n : number of integrators

m : order of input perturbation



$$\lim_{s \rightarrow 0} \frac{\Delta}{K} s^{n-m+1} \begin{cases} \rightarrow \Delta/K & \text{for } n=m-1 \\ \rightarrow 0 & \text{for } n \geq m \end{cases}$$

Static phase error is null if ~~*~~ integrators is at least equal to the input perturbation order

22) lock acquisition and frequency tracking

$$X_{REF}(t) = \cos(\omega_{fr}t) \text{ before a step}$$

$$X_{OUT}(t) = \sin(\omega_{fr}t) \text{ out is locked to input (in quadrature)}$$

Let's now apply a step $\Delta\omega$

$$X_{REF}(t) = \cos(\omega_{fr} + \Delta\omega)t \quad \text{where } (\omega_{fr} + \Delta\omega)t = \phi_{ref}$$

$$X_{OUT}(t) = \sin\left(\omega_{fr} + \int_{-\infty}^t K_{VCO} V_{TUNE}(\tau) d\tau\right)$$



→ We expect output to rise, overshoot and settle, but suppose that step is fast and filter is

very slow in its action, we can express $X_{OUT}(t)$ like:

$$X_{OUT}(t) = \sin\left(\omega_{fr} + \int_{-\infty}^t \dots \right) \approx \sin(\omega_{fr} + 0.t) \quad \leftarrow \text{omitted terms}$$

neglect

So output is still at ω_{fr} , it does not change. Therefore, initially:

$$V_{PD} = X_{OUT} \cdot X_{REF} \approx K_{PD} \sin(\Delta\omega t - 0.t)$$

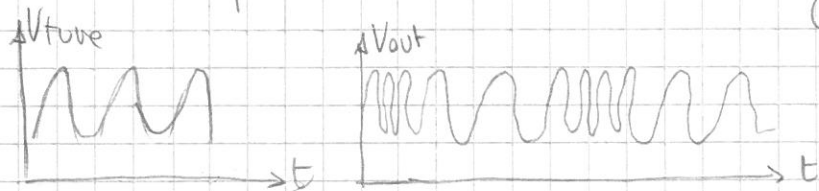
Note: there's a sinusoidal behaviour between input phase step and V_{PD} .

Filtering introduces amplitude/delay change in the signal, so:

$$V_{TUNE} \approx K_{PD} |F(\Delta\omega)| \sin[\Delta\omega t + \underbrace{\angle F(\Delta\omega)}_{\text{phase change}} + 0.t]$$

amp. change

The sinusoidal behaviour is the same that we found when we are out of lock → ω_{out} will change in a sinusoidal manner:



But let's look for a locking range:

$$V_{TUNE} = K_{PD} |F(\Delta\omega)| \sin[...]$$

Since $|\sin| \leq 1$ then

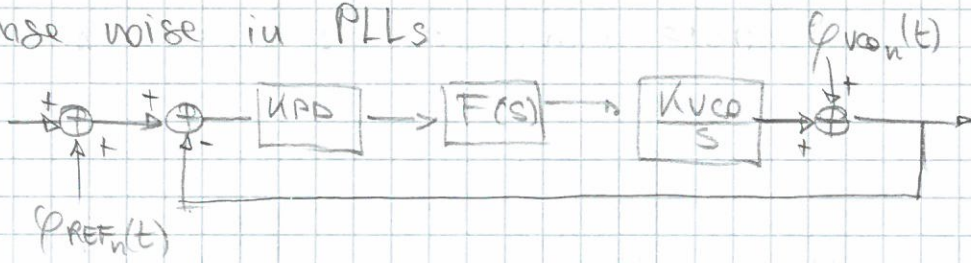
$$|V_{TUNE}| \leq K_{PD} |F(\Delta\omega)| \rightarrow \left| \frac{\Delta\omega}{K_{VCO}} \right| \leq K_{PD} |F(\Delta\omega)|$$

There is a limit $\Delta\omega$ that can generate a lock:

$$\Delta\omega_c = K_{VCO} K_{PD} |F(\Delta\omega_c)| = K |F(\Delta\omega_c)| \rightsquigarrow \text{LPF narrows the original } \Delta\omega_c = K$$

↳ capture range

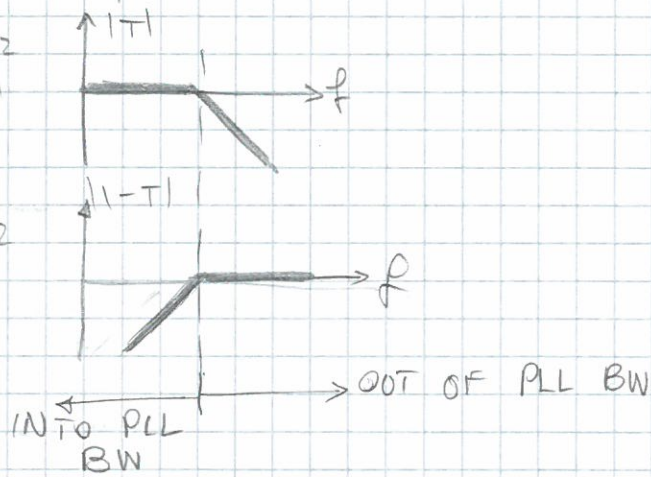
Phase noise in PLLs



If we consider VCO and REF phase noise sources:

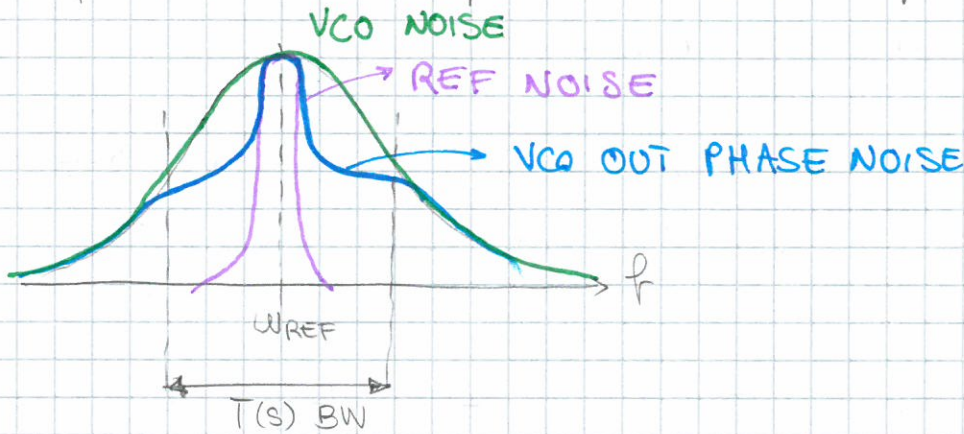
$$S_{\text{out}}(f) \Big|_{\text{REF NOISE}} = S_{\phi_{REF_n}} |T(f)|^2$$

$$S_{\text{out}}(f) \Big|_{\text{VCO NOISE}} = S_{\phi_{VCO_n}} |1-T(f)|^2$$



Meaning:

- within PLL BW: VCO follows REF phase noise
- out of PLL BW: VCO follows its own phase noise

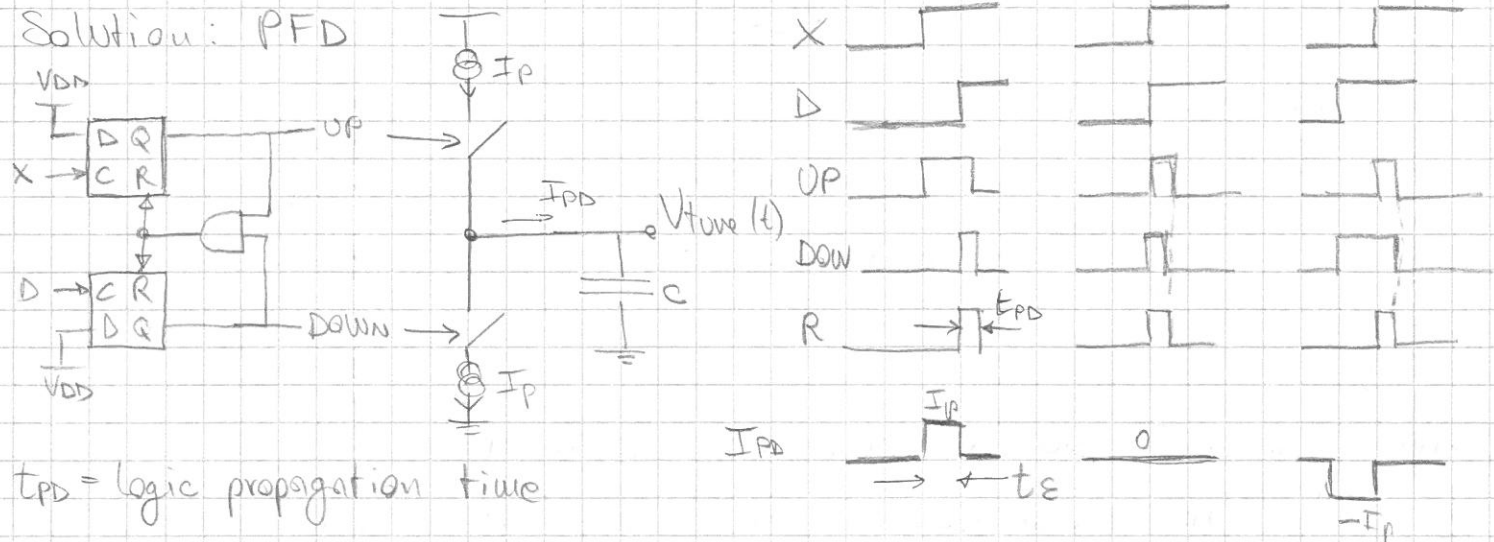


93) Charge pump PLLs: PFD, phase-domain model, stabilizing zero, loop dynamics

Problems:

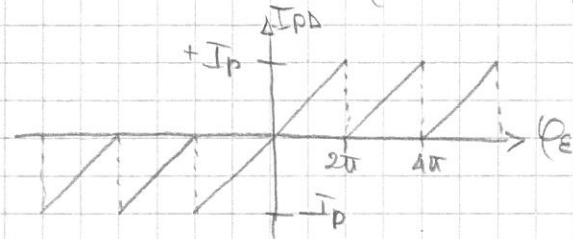
- conventional PDs do not track frequency (phase only)
- " " generate spurs at $2\omega_{REF}$ (analog mixer / XOR) or at ω_{REF} (SR-FF). Type II PLL keeps only $\overline{V_{PD}} = 0$, but does not solve the $2\omega_{REF}/\omega_{REF}$ issue

Solution: PFD



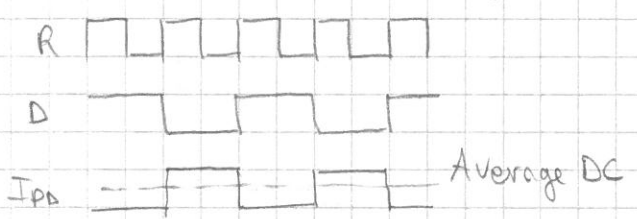
t_{PD} = logic propagation time

We know that $\phi_E = 2\pi \frac{t_E}{T_R}$ therefore:



How can it detect frequency? Suppose

$$2f_{REF} = f_{DIV}$$

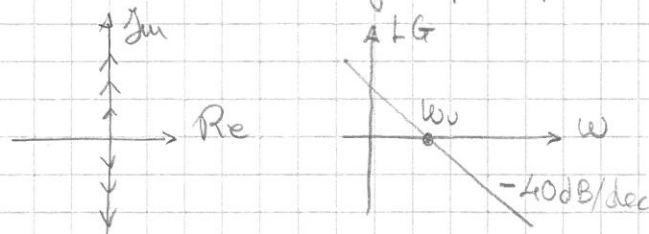


It can detect frequency because

when $f_{REF} \gg f_{DIV}$ output current is always > 0 (DC) \rightarrow VCO will increase out frequency therefore letting f_{DIV} approach f_{REF} . This means that it always (ideally) locks.

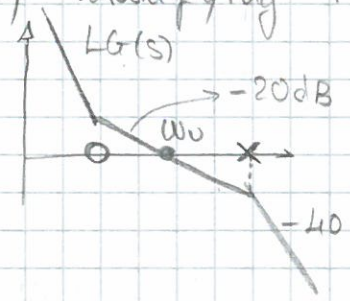
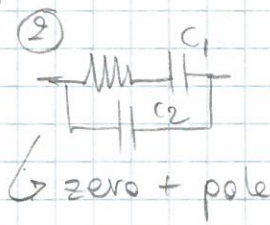
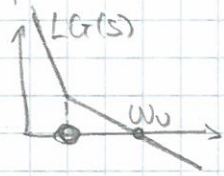
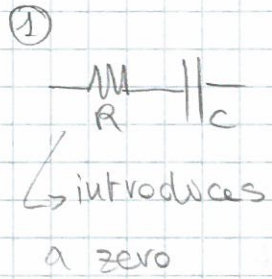
$K_{PD} = \frac{I_P}{2\pi}$ If we just have the charge pump + 1 capacitor:

$$LG(s) = -K_{PD} \cdot \frac{KVCO}{s} \cdot \frac{1}{sC}$$



This $LG(s)$ cuts ω axis with 40dB/dec slope \rightarrow unstable

We can think of recovering stability by modifying the filter:



$$LG(s) \Big|_{\text{②}} = K_{PD} \frac{K_{VCO}}{s} \cdot \frac{1+sRC}{sC} \stackrel{\text{High freq}}{=} K_{PD} \frac{K_{VCO}}{s} \cdot R$$

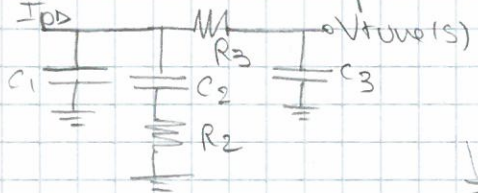
Issue: R could be high value $\rightarrow I_p R = V$ could go above PLL's dynamic range

Issue: shallow filtering (-20dB/dec) of high frequency spurious tones \rightarrow VCO will show unwanted modulation tones at its out

$$LG(s) \Big|_{\text{⑨}} = K_{PD} \frac{K_{VCO}}{s} \cdot \frac{1}{s(C_1+C_2)} \cdot \frac{1+sRC}{1+sR \frac{C_1 C_2}{C_1+C_2}}$$

Second solution lowers R value and with its -40dB/dec filtering on HF, attenuates HF spurs more than solution ①

Note: another pole can be added with the following:



$$Z_1(s) = C_1 \parallel (C_2 + R_2) = \frac{1}{s(C_1+C_2)} \cdot \frac{1+s\tau_z}{1+s\tau_p}$$

$$\frac{V_{tone}(s)}{I_{PD}(s)} = \frac{Z_1(s)}{Z_1(s) + R_3 + \frac{1}{sC_3}} = \text{heavy calculations}$$

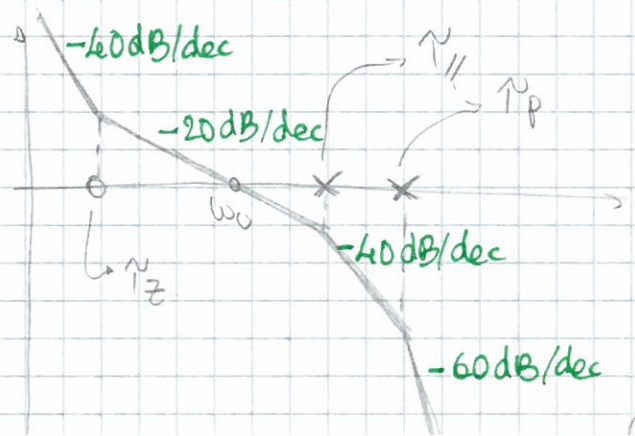
and rearrangements = $\frac{1}{s(C_1+C_2)} \cdot \frac{1+s\tau_z}{(1+s\tau_{11})(1+s\tau_p) + \frac{C_3}{C_1+C_2}(1+s\tau_z)}$

Where $\tau_p = R_3 C_3$ $\tau_z = R_2 C_1$ $\tau_{11} = R_3 \frac{C_1 C_2}{C_1+C_2}$

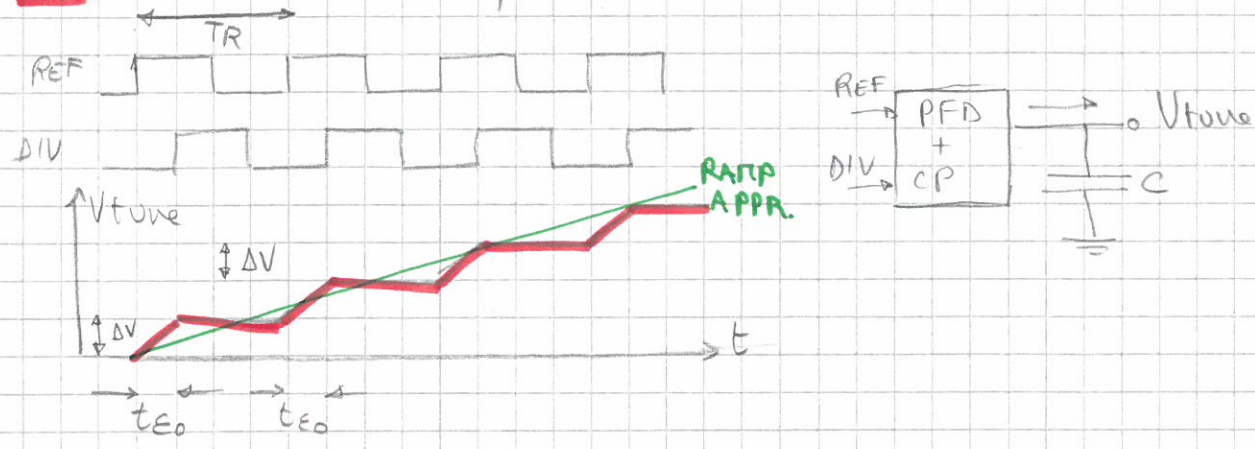
If $C_3 \ll C_1 + C_2$ and $C_3 \ll C_1 + \frac{R_3}{R_1} (1 + \frac{C_1}{C_2}) C_3$ the third pole does not interact, leading to

$$\frac{V_{tone}(s)}{I_{PD}(s)} = \frac{1}{s(C_1+C_2)} \cdot \frac{1+s\tau_z}{(1+s\tau_{11})(1+s\tau_p)}$$

-60dB/dec slope further attenuates high frequency spurs (see answer 25)



2.1 Limits of validity of continuous-time PLL model



CP is simple because:

- Performs sum with currents → No need for opamps

Vtune is a discrete-time process that updates every TR

$$C = \frac{dV}{Q} \quad i = \frac{dQ}{dt} \quad \rightarrow \quad \Delta V = \frac{tE \cdot I_{po}}{C} \quad \rightarrow \quad I_{po} \text{ peak current}$$

We can approx multiple steps (discrete time integrator) to an average single step (CT integrator → ramp) if the

time scale of interest is much longer than input period. Therefore the "train" of pulses in Vtune's discrete derivative can be averaged to just one

Gardner estimated that BW = $f_{REF} / 10$
 PLL, Gardner's limit = $f_{REF} / 20$ (safer)

Exploiting this:

$$tE0 = \frac{\varphi_{E0} \cdot TR}{2\pi}$$

$$V_{tune} \approx \frac{\Delta V}{TR} \cdot t = \frac{tE0 \cdot I_{po}}{C \cdot TR} \cdot t = \frac{I_{po}}{C} \cdot \frac{\varphi_{E0}}{2\pi} t$$

by using Laplace transform

$$V_{tune}(s) = \frac{I_{po}}{C} \cdot \frac{\varphi_{E0}}{2\pi} \cdot \frac{1}{s^2} = \frac{I_{po}}{2\pi} \cdot \frac{1}{sC} \cdot \frac{\varphi_{E0}}{s}$$

$$\frac{I_{po}}{2\pi} = K_{PD}$$

$$\rightarrow \frac{V_{tune}(s)}{\varphi_E(s)} = \frac{I_{po}}{2\pi} \cdot \frac{1}{sC} = K_{PD} Z(s)$$

$\frac{1}{sC}$ = Z(s) of the CP

$\frac{\varphi_{E0}}{s}$ = phase error input step

It can be shown that, using Z-transform for PLLs with BW above Gardner's limit can

still give a good estimate (to be verified in simulations)

25) Sources of ripples in PLLs: ref spur problem and methods to reduce the level

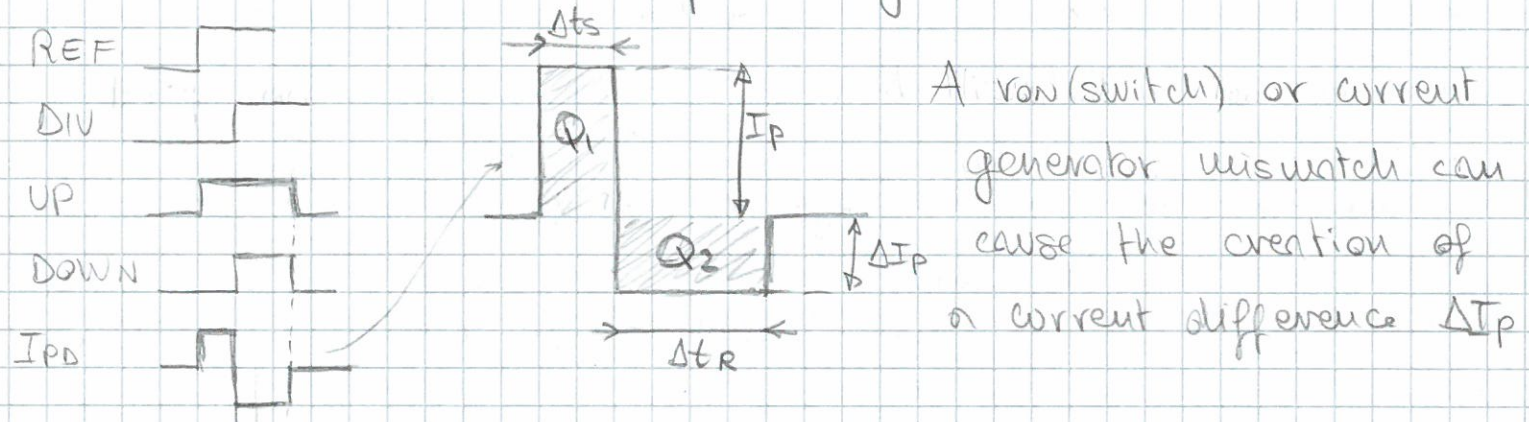
Sources of ripples:

1. varactor current leakage in VCO discharge the capacitor
2. disturbances at WREF coupled through GND or PSU lines
3. CP imbalances

Solutions:

1. Use MOS varactors, they greatly reduce leakage current
2. Shield Vtune line from disturbances
3. Better fabrication process and calibration of PFD + CP

CP current mismatch and spurs magnitude



At steady state no net current should enter the filter \rightarrow charges Q_1 and Q_2 must be the same.

Therefore the PLL automatically adjust REF / DIV delay

$$\Delta t_s \text{ so that } Q_1 = I_p \cdot \Delta t_s = Q_2 = \Delta I_p \cdot \Delta t_r$$

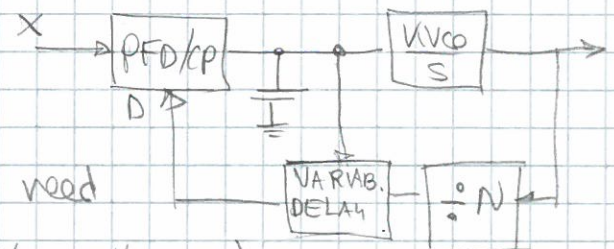
That way, on average $\langle I_{PD} \rangle = 0 \rightarrow$ steady state

Spur reduction methods:

- add poles (see answer 23)
- "sampling loop filter" (\rightarrow see Razavi section 10.4)
- Add a variable delay in the path (see Razavi section 10.4).

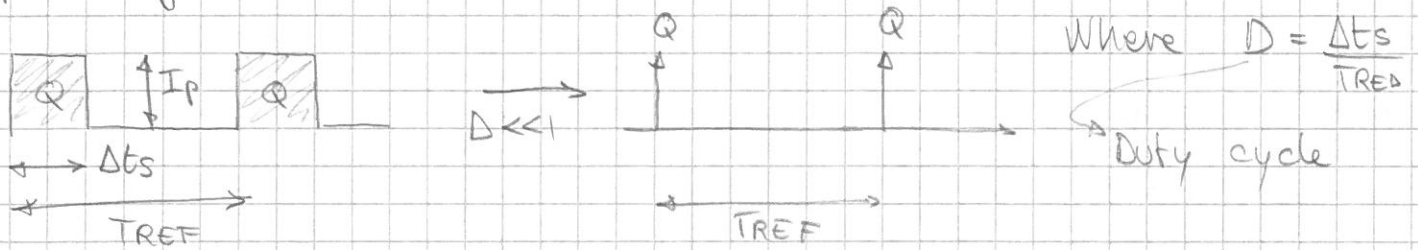
Loop gain will see a zero without the need

for an additional resistor. $LG(s) = \frac{I_p}{2\pi SC} \frac{1}{s} \left(\frac{K_{VCO}}{s} + K_p \cdot W_{REF} \right)$



William \uparrow delay $\rightarrow P_n + K_p/V_{tune} \rightarrow K_p/V_{tune}$

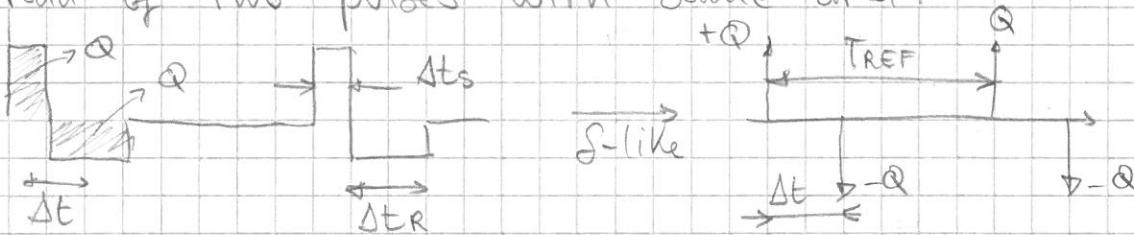
Spur magnitude estimation:



Fundamental harmonic of the train of pulses will be:

$$\frac{2}{\pi} I_p \sin(\pi D) \underset{\Delta \ll 1}{\approx} 2 I_p \frac{\Delta t_s}{T_{REF}} = \frac{2 Q}{T_{REF}}$$

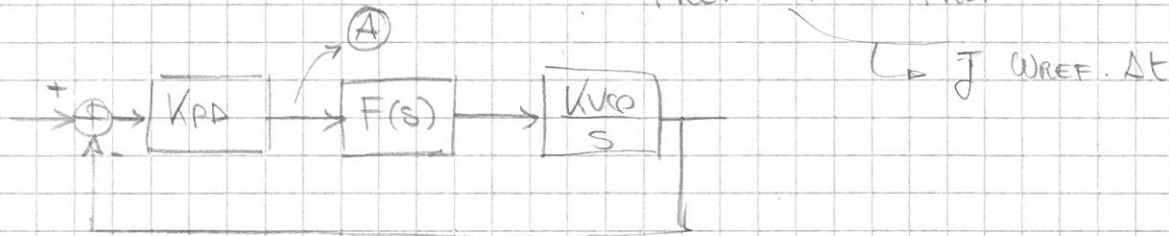
Train of two pulses with same area:



Assuming ideal deltas: $\Delta t = \frac{\Delta t_s + \Delta t_R}{2}$ then the first

harmonic will be $I_p^{(w_{REF})} = \frac{2 Q}{T_{REF}} (1 - e^{-j 2\pi \Delta t / T_{REF}})$ $x \rightarrow 0$

For low Δt we say $I_p^{(w_{REF})} \approx \frac{2 Q}{T_{REF}} (j 2\pi \frac{\Delta t}{T_{REF}}) 1 - e^{-x} \approx x$



$$\mathcal{L}(w_x) = \frac{S_{\varphi}}{2} = \frac{1}{2} \frac{K_{VCO}^2}{w_x} |F(jw_x)|^2 \cdot \frac{|I(w_x)|^2}{2} \left| \frac{1}{1 - \text{loop}(w_x)} \right|^2$$

Loop is usually negligible because spurs at $\sim w_{REF}$ are well above the BW_{PLL} → loop is practically open

For a leakage current we have



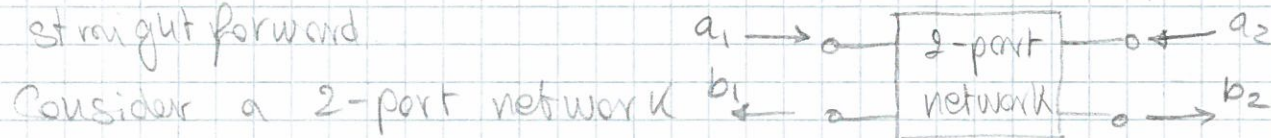
Δt_s will be adjusted to match the two areas (charges):

procedure that follows will be the same as the single pulse train

27) LNA: scattering parameters, insertion loss, reverse isolation, stability, linearity. Methods to increase reverse isolation

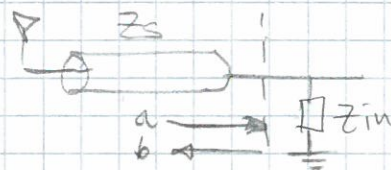
Microwave design focusses on power transfer instead of voltage or current transfer

HF measurement of I/V is very difficult while power is more straight forward



a = incident power wave at port 1/2

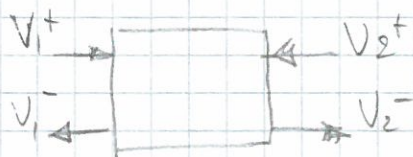
b = reflected " " " " "



We can say, for a single port $b = \Gamma a$ where $\Gamma = \frac{Z_{in} - Z_s}{Z_{in} + Z_s}$

If we consider power transfer between parts:

$$\begin{cases} b_1 = S_{11} a_1 + S_{12} a_2 \\ b_2 = S_{21} a_1 + S_{22} a_2 \end{cases} \quad \begin{pmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{pmatrix} \rightarrow \text{Scattering parameters matrix}$$



We can express S-parameters in voltages

$$\begin{cases} V_1^- = S_{11} V_1^+ + S_{12} V_2^+ \\ V_2^- = S_{21} V_1^+ + S_{22} V_2^+ \end{cases} \rightarrow S_{11} = \frac{b_1}{a_1} = \frac{V_1^-}{V_1^+} \Big|_{V_2^+ = 0}$$

When defining S_{11} , $V_2^+ = 0$ means that load is assumed to be matched at port 2.

With

What do S-parameters represent?

- S_{11} : accuracy of input matching

$$\text{Input return loss } RL_{IN} = 10 \log_{10} \frac{1}{|S_{11}|^2} = -20 \log_{10} \{ |S_{11}| \}$$

- S_{12} : how much output couples through input network

$$\text{Reverse isolation} = -20 \log_{10} \{ |S_{12}| \}$$

- S_{22} : accuracy of output matching

$$\text{Output return loss } RL_{out} = -20 \log_{10} \{ |S_{22}| \}$$

- S_{21} : gain of input-to-output transfer

$$\text{Forward gain} = -20 \log_{10} \{ |S_{21}| \}$$

Why don't we use V/I to measure in/out port impedances?

For an impedance measurement we need to short or open in/out ports. At HF this is detrimental because:

- short ports \rightarrow magnetic coupling (no matching)
- open ports \rightarrow capacitive coupling (no matching)

Stability: based on conditions of the environment (user hand on phone, etc...) antenna impedance slightly changes.

LNA has to take into account this. \rightarrow LNA must be stable for all frequencies

If LNA is stable only in a small frequency range (the working one), non-stability on other frequencies can cause oscillations \rightarrow it becomes non-linear and it heavily compresses the gain even in the working narrow band.

For this reason, we care a lot about reducing reverse gain. (S_{12}).

Common gate and shunt feedback are two robust

topologies that are naturally stable \rightarrow no stability issues
Reverse isolation of an LNA is important to reduce LO leakage into LNA input (causing LO propagation to the antenna)

28) MOS noise model. Common-gate and shunt-feedback

MOS noise:

Transconductance definition $g_{do} = \left. \frac{\partial I_D}{\partial V_{GS}} \right|_{V_{DS}=0}$

• Triode region $I_D = \frac{1}{2} \mu C_{ox} \frac{W}{L} [2V_{ov}V_{DS} - V_{DS}^2]$

$$g_{do} = \mu C_{ox} \frac{W}{L} V_{ov} = \frac{1}{V_{ov}}$$

• Saturation region $I_D = \frac{1}{2} \mu C_{ox} \frac{W}{L} V_{ov}^2$

$$g_m = g_{do}$$

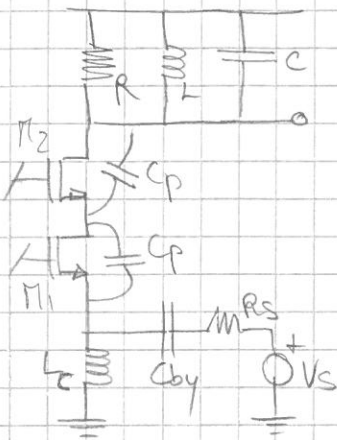
Consider carrier saturation (HV field, short channel MOS)

$$g_m = \alpha \cdot g_{do} < g_{do} \begin{cases} \rightarrow \alpha = 1 \text{ no saturation} \\ \rightarrow \alpha < 1 \text{ with carrier saturation} \end{cases}$$

• No saturation $\rightarrow \text{PSD}|_{\text{MOS}} = 4kT \cdot g_{do}$

• In saturation $\rightarrow \text{PSD}|_{\text{MOS}} = 4kT \frac{g_m}{\alpha}$

Common-gate Topology



L_c = choke inductor

RLC = tuned load to maximize gain

M_2 = cascode to improve reverse isolation because of parasitic capacitances C_p

• Matching input: $\frac{1}{g_{m1}} = R_s$

• Voltage output = $\frac{V_{out}}{V_s} = \frac{R_L}{2R_s}$

R_L comes from the LC resonator and it's limited by the Q of the reso $R_L = Q\omega_0 L$ $R_L \sim 100\Omega \div 1k\Omega$

Since $R_s = 50\Omega$ then $A_0 \sim 1 \div 10 \Rightarrow A_0 \sim 0 \div 20dB$ max

Maximum gain of the transistor can't be achieved at RF

eg: $f_0 = 1GHz$ $L = 1nH$ $Q = 10 \rightarrow R_L = 62,8\Omega$

• Noise:

$NF = 1 + \frac{LkT/gm \cdot \frac{1}{gm}}{LkTRs} + \frac{LkTRL}{LkTRs \cdot \left(\frac{R_L}{2R_s}\right)^2}$

$NF = 1 + \frac{1}{\alpha} + L \frac{R_s}{R_L}$

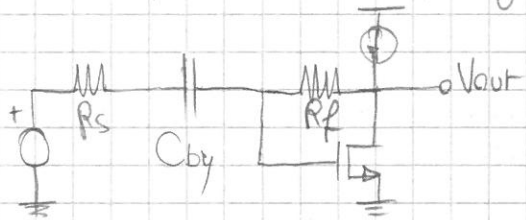
$R_s = 1/gm$

Since $A_0 = \frac{R_L}{2R_s} \rightarrow L \frac{R_s}{R_L} = \frac{1}{A_0} \rightarrow NF = 1 + \frac{1}{\alpha} + \frac{1}{A_0}$

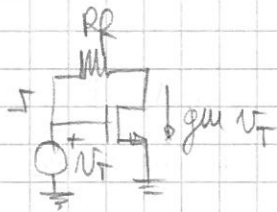
Power gain $\triangleq \frac{P_{out,av}}{P_{in,av}} = \frac{\frac{1}{8} \frac{V_{out}^2}{R_L}}{\frac{1}{8} \frac{V_{in}^2}{R_s}} = A_0^2 \cdot L \frac{R_s}{R_L} = \frac{R_L}{R_s}$

\hookrightarrow because of input matching

Shunt-feedback topology

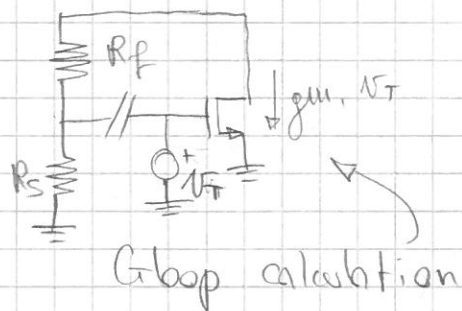


• Input impedance $Z_{in} = \frac{1}{g_{m1}}$



• $A_o = \frac{T_{ID}}{1 - G_{loop}} + \frac{T_{DIRECT}}{1 - G_{loop}}$ → forward gain is not high, T_{DIRECT} will have an effect

$$= \frac{-\frac{R_f}{R_s}}{1 + g_{m1} R_s} + \frac{1}{1 + g_{m1} R_s} = \frac{1 - g_{m1} R_f}{1 + g_{m1} R_s}$$

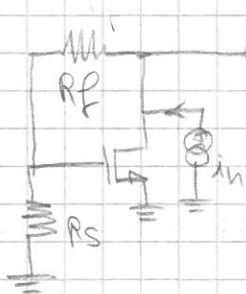


Consider matching input $\frac{1}{g_m} = R_s$, therefore:

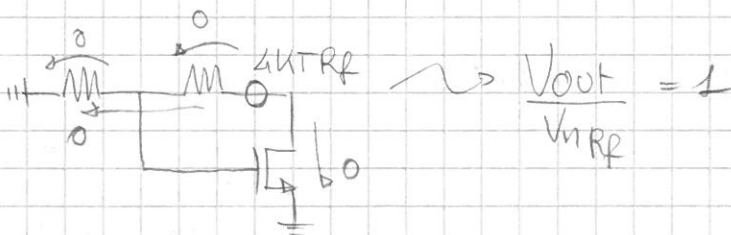
• $A_o|_{MATCHED} = \left(1 - \frac{R_f}{R_s}\right) \frac{1}{2} = -\frac{R_f}{2R_s}$

• Noise: $R_f \gg R_s$ $V_{out} = Z_{out} = \frac{R_f + R_s}{1 + G_{loop}} = \frac{R_f + R_s}{2}$

$$i_{n|ROS}^2 = 4kTg_m \frac{\delta}{\alpha}$$



$$V_n^2|_{R_f} = 4kTR_f$$

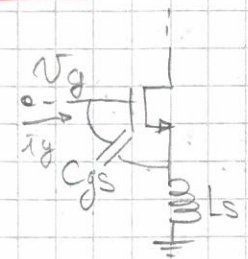


$$NF = 1 + \frac{4kT \frac{\delta}{\alpha} \cdot g_{m1} \left(\frac{R_f + R_s}{2}\right)}{4kTR_s \frac{1}{4} \left(1 - \frac{R_f}{R_s}\right)^2} + \frac{4kTR_f}{4kTR_s \frac{1}{4} \left(1 - \frac{R_f}{R_s}\right)^2}$$

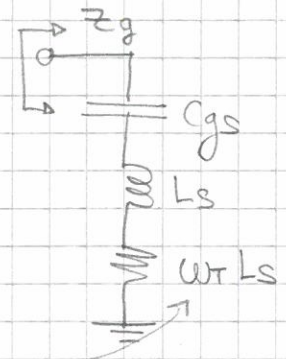
$NF = 1 + \frac{\delta}{\alpha} + \frac{4R_s}{R_f}$ → the same as common-gate

Consider $\frac{1}{4} \left(1 - \frac{R_f}{R_s}\right)^2 \approx \frac{R_f^2}{4R_s^2}$ (with $R_f \gg R_s$)

29) LNA: inductor degenerated CS topology



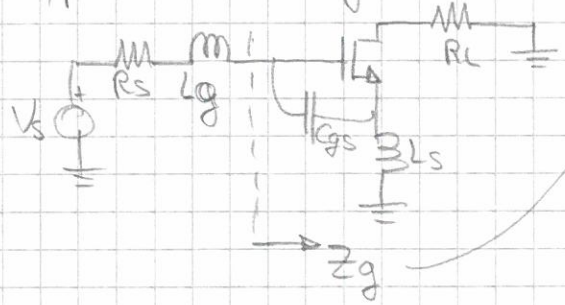
$$\begin{cases} V_g = V_{gs} + sL_s (g_m V_{gs} + i_g) \\ i_g = sC_{gs} \cdot V_{gs} \end{cases}$$



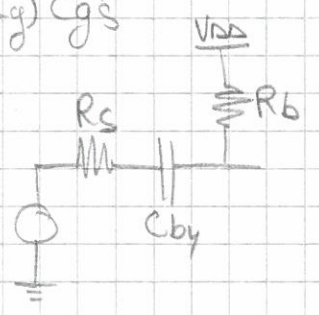
$$Z_g = \frac{V_g}{i_g} = \frac{1}{sC_{gs}} + \underbrace{sL_s}_{\text{inductor}} + \underbrace{g_m L_s}_{\text{Real impedance}} \frac{1}{C_{gs}}$$

$$W_T \approx \frac{g_m}{C_{gs}} \rightarrow \text{Neglects } C_{gs} \text{ take into account } L_g$$

Typical matching condition

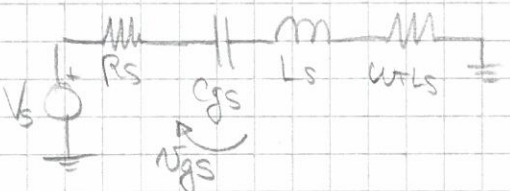


$$\begin{cases} \omega_0 = \frac{1}{\sqrt{L_s C_{gs}}} = \frac{1}{\sqrt{(L_s + L_g) C_{gs}}} \\ W_T L_s = R_s \end{cases}$$



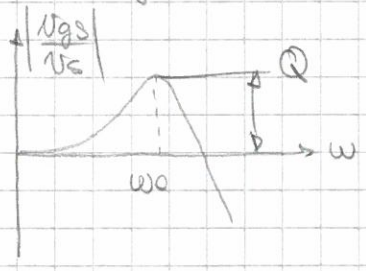
Lg is used to limit biasing network noise, that is:

We will neglect Lg for the following calculations:



$$V_{out} = -g_m V_{gs} R_L = -g_m R_L \cdot Q V_s$$

where $Q = \frac{1}{\omega_0 C_{gs} 2R}$ input matching



$$A_0 = \frac{V_{out}}{V_s} = -g_m R_L Q = \frac{-g_m R_L}{\omega_0 C_{gs} 2R_L} = -\frac{W_T}{\omega_0} \frac{R_L}{R_s}$$

Where $\frac{-R_L}{2R_s} =$ common gate A_0 , $\frac{W_T}{\omega_0} =$ boost factor

The boost factor is given by the resonant network that adds another gain without impacting on the NOS!

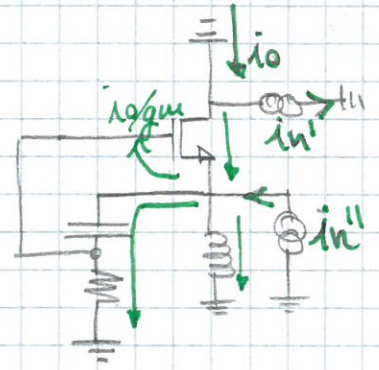
Since L, C are noiseless \rightarrow NF will be better

NF of degenerated CS network

To compute MOSFET noise, we can use Norton

This way we don't need to take into account R_L

NF will be output noise current i_o

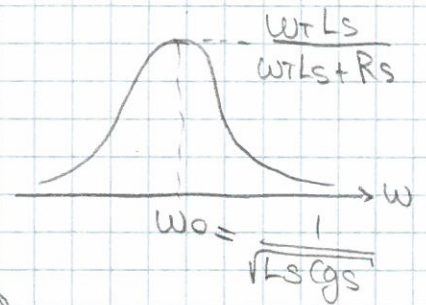


$i_n', i_n'' \rightarrow$ superposition of effects

$$i_n'' + i_o = \underbrace{-\frac{i_o s C_{gs}}{g_m}}_{\text{current in } C_{gs}} + \underbrace{\frac{-\frac{i_o s C_{gs} \cdot R_s - \frac{i_o}{g_m}}{s L_s}}{s L_s}}_{\text{current in } L_s}$$

$$i_o \left(1 + \frac{s C_{gs}}{g_m} + \frac{R_s C_{gs}}{g_m L_s} + \frac{1}{s g_m L_s} \right) = -i_n''$$

$$\frac{i_o}{i_n''} = \frac{-s g_m / C_{gs}}{s^2 + s \left(\frac{g_m}{C_{gs}} + \frac{R_s}{L_s} \right) + \frac{1}{L_s C_{gs}}}$$



Since $\omega_T L_s = R_s \rightarrow \frac{i_o}{i_n''} = -\frac{1}{2}$ while $\frac{i_o}{i_n'} = 1$

Therefore $\frac{i_o}{i_n} = \frac{i_o}{i_n'} + \frac{i_o}{i_n''} = 1 - \frac{1}{2} = \frac{1}{2} \rightarrow$ half of i_n recirculates

$$\frac{i_{n, out}^2 |_{MOS}}{i_{n, out}^2 |_{R_s}} = \frac{4 k T \frac{\delta}{\alpha} g_m \cdot \left(\frac{1}{2}\right)^2}{4 k T R_s \cdot \left(\frac{g_m}{\omega_0 C_{gs} 2 R_s}\right)^2} = \frac{\delta}{\alpha} \frac{R_s}{g_m} \omega_0^2 C_{gs}^2 = \frac{\delta}{\alpha} \frac{\omega_0}{\omega_T} \cdot \frac{1}{Q_L} \rightarrow Q_L \text{ of } Z_g \text{ in matching conditions}$$

NF will be:

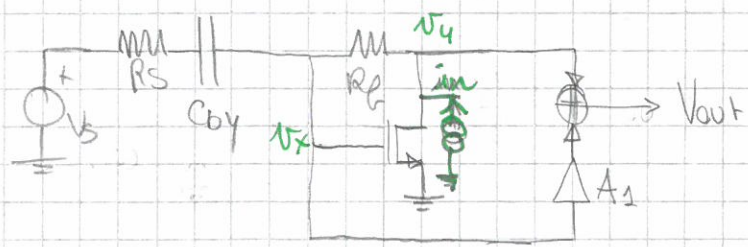
→ Reduction factor →

$$NF |_{DEGEN. CS} = 1 + \frac{\delta}{\alpha} \frac{\omega_0}{\omega_T} \cdot \frac{1}{Q_L} + \frac{4 R_L}{R_s} \left(\frac{\omega_0}{\omega_T} \right)^2 \rightarrow NF |_{R_L}$$

Compare to common gate: $NF |_{CG} = 1 + \frac{\delta}{\alpha} + \frac{4 R_s}{R_L}$ We clearly see

that the task to amplify given to RLC network instead of the FET reduces the noise that would be otherwise introduced by a CG or shunt-feedback

30) noise cancelling applied to shunt-feedback



We would like to cancel FET in by exploiting the the shunt.

v_x has some gain, different than $v_y \rightarrow v_{out} = 0$ by A_1 compensation:

$$\frac{v_y}{i_{in}} = \frac{R_f + R_s}{2} = Z_{out} \quad \frac{v_x}{i_{in}} = \frac{v_y}{i_{in}} \cdot \frac{R_s}{R_s + R_f} = \frac{R_s + R_f}{2} \cdot \frac{R_s}{R_s + R_f} > 0$$

$$\frac{v_{out}}{i_{in}} = A_1 v_x + v_y = A_1 \frac{R_s}{2} + \frac{R_s + R_f}{2} \rightarrow v_{out} = 0 \text{ if } A_1 = -\left(1 + \frac{R_f}{R_s}\right)$$

What about signal transfer v_{out} / v_s ?

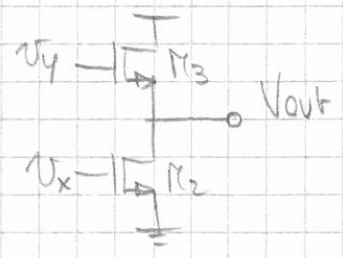
$$\frac{v_{out}}{v_s} = \frac{v_y}{v_s} + A_1 \frac{v_x}{v_s} = \frac{1}{2} \left(1 - \frac{R_f}{R_s}\right) - \left(1 + \frac{R_f}{R_s}\right) \cdot \frac{1}{2} = -\frac{R_f}{R_s}$$

$\underbrace{\frac{1}{2} \left(1 - \frac{R_f}{R_s}\right)}_{A_0 \text{ (untched)}} \quad \underbrace{- \left(1 + \frac{R_f}{R_s}\right)}_{A_1} \quad \underbrace{\frac{1}{2}}_{v_x = \frac{1}{2} v_s \text{ (untched)}}$

$\frac{v_{out}}{i_{in}} = 0$ while $\frac{v_{out}}{v_s} = -\frac{R_f}{R_s} \rightarrow$ Double signal gain, zero noise transfer (for FET's)

This is why we used v_x and v_y , we want to kill noise without damaging the signal transfer

A_1 + summer can be obtained by using two mosfets:



By using superposition principle:

$$\frac{v_{out}}{v_y} \approx 1 \text{ (buffer)} \quad \frac{v_{out}}{v_x} = -\frac{g_{m2}}{g_{m3}} \text{ (CS with active load)}$$

NF for noise cancelling topology:

We need to verify that π_2, π_3 do not introduce more noise

$$NF = 1 + \frac{4kT R_f}{4kT R_s \left(\frac{R_f}{R_s}\right)^2} + \frac{4kT \frac{1}{\alpha} (g_{m2} + g_{m3}) \left(\frac{1}{g_{m3}}\right)^2}{4kT R_s \left(\frac{R_f}{R_s}\right)^2} + 0 \quad \left| \pi_1 \text{ current noise} \right.$$

$$\frac{\frac{1}{\alpha} (g_{m2} + 1) \frac{1}{g_{m3}}}{\frac{R_f^2}{R_s}} \rightarrow \frac{g_{m2}}{g_{m3}} = 1 + \frac{R_f}{R_s}$$

$$\frac{\frac{1}{\alpha} \left(\frac{R_f}{R_s} + 1\right) \frac{1}{g_{m3}}}{\frac{R_f^2}{R_s}} \approx \frac{1}{\alpha} \frac{1}{R_f g_{m3}} \quad \left(\frac{R_s}{R_f} \rightarrow 0 \right)$$

$R_f \gg R_s$

$$NF \Big|_{R_f \gg R_s} \approx 1 + \frac{1}{\alpha} \frac{1}{R_f g_{m3}}$$

To have $NF \Big|_{\text{NOISE CANCEL.}} < NF \Big|_{\text{SHUNT-FEEDBACK}}$ We need that

$$g_{m3} > \frac{1}{R_f} \rightarrow \frac{1}{g_{m3}} < R_f \quad \left[\rightarrow \text{more current is drawn, more power is consumed, large area occupied} \right.$$

Moreover, gates of π_2 and π_3 have parasitic capacitances and we need to compensate those using inductors.

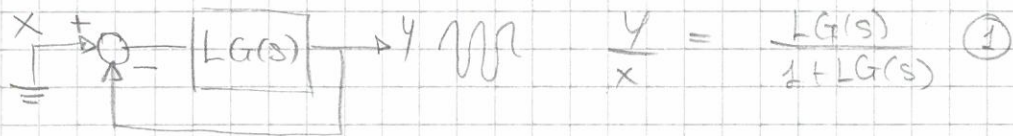
So, in order to have noise cancelling:



- $\pi_1 \rightarrow g_{m1} = 1/R_s$ input matching
- $R_f \gg R_s \rightarrow$ to have higher gain
- $\pi_3 \rightarrow g_{m3} > 1/R_f$ to have low NF

31) Oscillators: feedback model + Barkhausen. $R < 0$ model.

Amp stabilization methods, Osc startup and effective gain

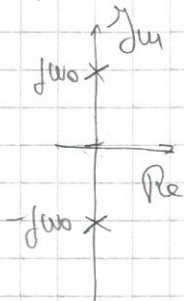


In order to oscillate $Y(j\omega_0) \neq 0$ with no input applied $X(j\omega_0) = 0$

So, from (2) we get the following

$$1 + LG(j\omega_0) = 0 \rightarrow LG(j\omega_0) = -1$$

$$\begin{cases} |LG(j\omega_0)| = 1 \\ \angle LG(j\omega_0) = \pi = 180^\circ \end{cases}$$



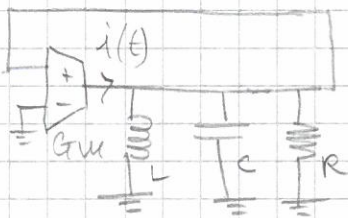
This is called the Barkhausen's criterion

It means that, in order to get proper oscillation, a negative feedback system should show $\pm j\omega_0$ solutions only

for a positive feedback sys: $|LG(j\omega_0)| = 1 \quad \angle LG(j\omega_0) = 0^\circ$

Consider a LC osc, where R models the energy dissipated in the tank:

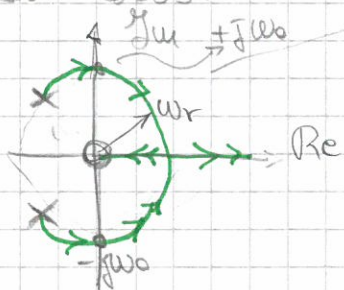
$$Z(s) = R \frac{s \omega_r}{s^2 + s \frac{\omega_r}{Q} + \omega_r^2} \quad \text{Bandpass } T(s)$$



$$\text{where } \omega_r = \frac{1}{\sqrt{LC}} \quad \text{and } Q = \omega_r RC$$

$$LG(s) = G_m R \frac{s \omega_r}{s^2 + s \frac{\omega_r}{Q} + \omega_r^2} \rightarrow \text{we cut the loop at } G_m \text{ input}$$

Root locus:



Cut on the y axis is $\omega_0 = \omega_r$ ($s = \pm j\omega_0$)

$$1) \angle LG(j\omega_0) = 0 \rightarrow \frac{s \omega_r / Q}{s^2 + s \omega_r / Q + \omega_r^2} \Big|_{s=j\omega_0} = \frac{\pi - \arctan \frac{\omega_0 \omega_r / Q}{\omega_r^2 - \omega_0^2}}{2} = 0$$

Solution will be $\omega_r = \omega_0$

$$2) |LG(j\omega_0)| = 1 \quad \text{If } \omega_r = \omega_0 \quad \frac{G_m R \frac{\omega_0 / \omega_r / Q}{\sqrt{(\omega_r^2 - \omega_0^2)^2 + (\omega_0 \omega_r / Q)^2}}}{1} = 1$$

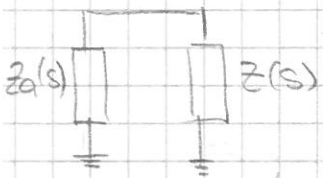
It becomes $G_m R$

Negative resistance mode

It's based on an energy approach:

balance between dissipated and active power.

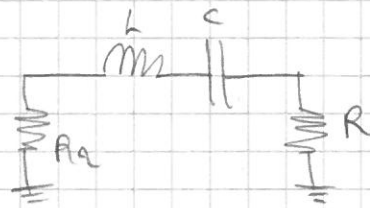
$$\frac{1}{2} \frac{A_0^2}{R} = \frac{1}{2} G_m A_0^2 \rightarrow G_m = \frac{1}{R}$$



Osc condition: $Z_a(j\omega_0) + Z(j\omega_0) = 0 \rightarrow Z(j\omega_0) = -Z_a(j\omega_0)$

$$\text{Re}(Z_a(j\omega_0)) = -\text{Re}(Z(j\omega_0))$$

$$\text{Im}(Z_a(j\omega_0)) = -\text{Im}(Z(j\omega_0))$$



- L, C resonates $\omega_0 L + \frac{1}{\omega_0 C} = 0 \rightarrow \omega_0 = \frac{1}{\sqrt{LC}}$

- Real part $R_a = -R$

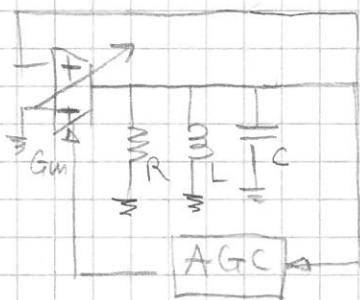
Amplitude compensation

If $G_m \frac{A_0}{2} < \frac{A_0^2}{2R}$ oscillation fades out (active power < dissipated)

If $G_m \frac{A_0}{2} > \frac{A_0^2}{2R}$ oscillation diverges

We to have a sinusoid that is constant we must adjust

the gain:



AGC = automatic gain control that reads output amplitude and automatically adjust the G_m to have $G_m R = 1$

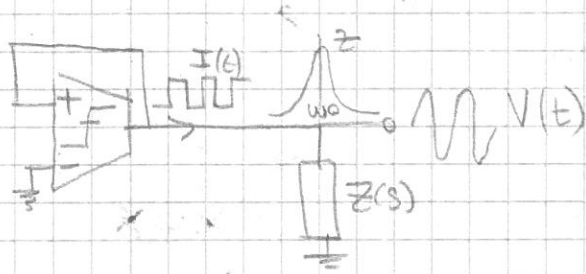
Hard limiting for amplitude stabilization:

Instead of a linear G_m we can think of a nonlinear:



We feed a sine input and get a square output. This becomes particularly useful considering that:

RLC tank \rightarrow high $Q \rightarrow$ narrow band \rightarrow only certain harmonics survive (ideally just the oscillation one):



Even though behaviour is non linear, if we just take into account only ω_0 harmonic, we can still use linear analysis tools.

\bar{I}_1, \bar{V}_1 = current/voltage of the 1st harmonic.

Apply the osc condition:

$$\bar{I}_1 Z(\omega_0) = \bar{V}_1 \quad Z(\omega_0) = \frac{\bar{V}_1}{\bar{I}_1} \equiv G_{mh} \quad \text{where } G_{mh} \text{ takes}$$

the name of "harmonic transconductance".

$$\text{Therefore } Z(j\omega_0) = \frac{1}{G_{mh}}$$

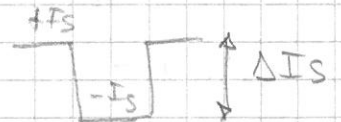
Consider:

- hard limiting $I(V(t)) = I_s \cdot \text{sign}\{V(t)\}$



- $V(t) = A_0 \cos \omega_0 t \rightarrow \bar{V}_1 = A_0$

- Square current $\pm I_s$ with 50% Duty cycle

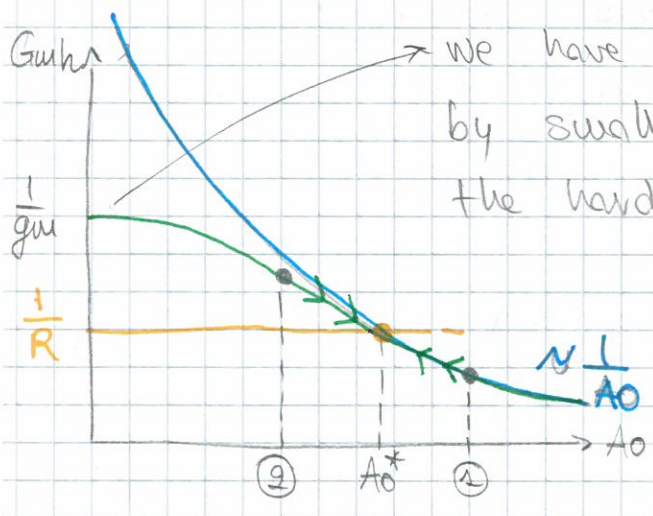


$$\text{therefore } \bar{I}_1 = \frac{2}{\pi} \Delta I_s = \frac{4}{\pi} I_s$$

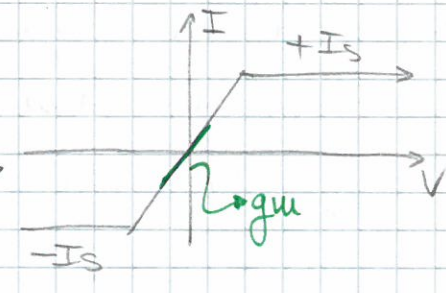
Now apply the Barkhausen condition for the 1st harmonic

$$LG_h(j\omega_0) = 1 \quad \begin{cases} G_{mh} R = 1 \rightarrow G_{mh} = \frac{\bar{I}_1}{\bar{V}_1} = \frac{4}{\pi} I_s \cdot \frac{1}{A_0} \\ \omega_0 = \frac{1}{\sqrt{LC}} \end{cases}$$

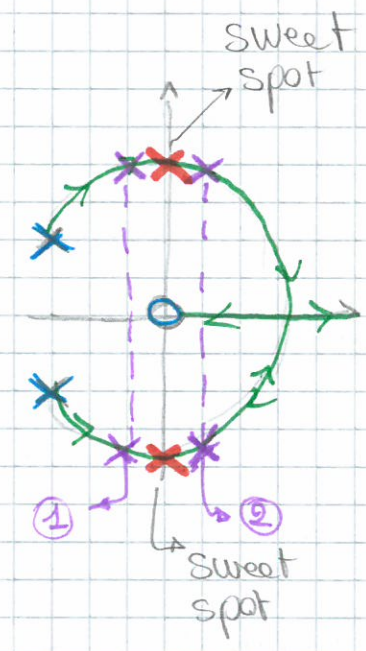
$$\text{So } \frac{4}{\pi} \frac{I_s}{A_0} \cdot R = 1 \rightarrow \text{OSC Amplitude } A_0 = \frac{4}{\pi} I_s R$$



we have a limit imposed by small signal gm of the hard limiter



- ① $A_0 > A_0^*$: $G_m R < 1$ poles in LHP $\Rightarrow A_0 \uparrow$ → fading
- ② $A_0 < A_0^*$: $G_m R > 1$ poles in RHP $\Rightarrow A_0 \uparrow$ → diverging



We can see that hard limiting compensates oscillation amplitude when it changes.

But, to compensate A_0 we first need to build up one. For this reason:

At startup \rightarrow divergence \rightarrow active power $>$ dissipated power

$$\frac{1}{2} G_m A_0^2 > \frac{1}{2} \frac{A_0^2}{R} \rightarrow G_m R > 1$$

Typically : $G_m R > EG$ where $EG = \text{excess gain} \sim 2$

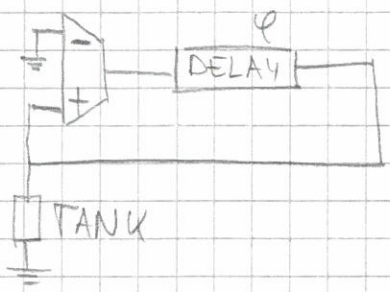
After startup, we then need to keep the oscillation up, so:

$$\underline{LG_h(j\omega_0) = 1}$$

These are the two important ingredients to be used when designing a real oscillator.

32) Frequency stability. Loop delay effect on OSC. Meaning of Q factor

Goal: achieve a stable frequency that is robust with respect to phase delays induced in the loop:



OSC condition:

- $|LG(j\omega_0)| = 1$
- $\angle LG(j\omega_0) = 0$ but $LG(j\omega) = Gm Z(j\omega) e^{-j\varphi}$

loop gain has now a phase shift

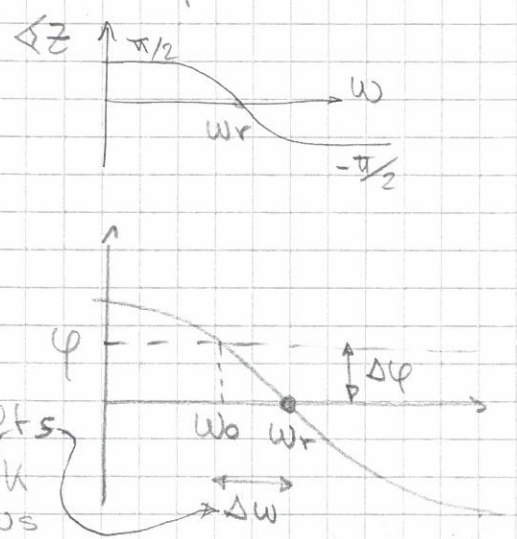
$$\angle LG(j\omega_0) = 0 \rightarrow -\varphi + \angle Z(j\omega_0) = 0$$

$$+\frac{\pi}{2} - \text{atan} \left\{ \frac{\omega\omega_r/Q}{\omega_r^2 - \omega^2} \right\} = \varphi$$

Since $\frac{\pi}{2} - \text{atan} x = \text{atan} \frac{1}{x}$ we get

$$\text{atan} \left\{ \frac{\omega_r^2 - \omega^2}{\omega\omega_r/Q} \right\} = \varphi$$

frequency shifts back from tank resonance ω_s

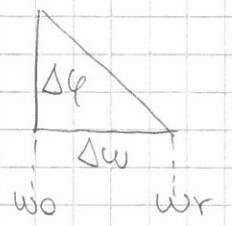


We can observe that $\omega_0 < \omega_r$ with a positive $\varphi > 0$

For ease of computing and understanding, we linear approx. the zone near $\Delta\omega$ and $\Delta\varphi$:

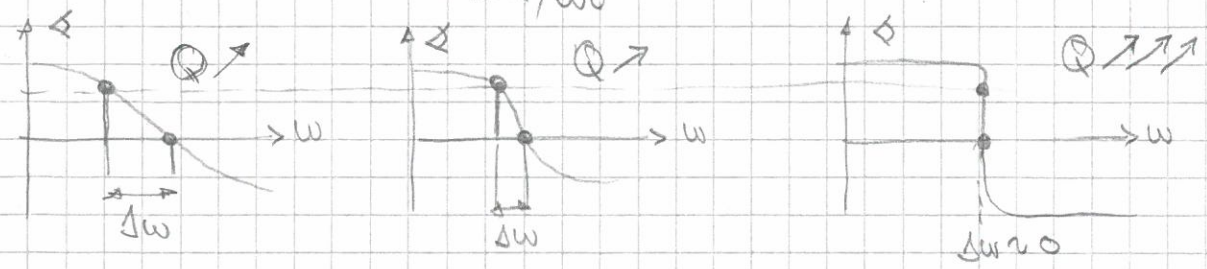
$$\Delta\omega_0 \approx \frac{\Delta\varphi}{\left. \frac{d\angle Z}{d\omega} \right|_{\omega_0 = \omega_r}}$$

where $\angle Z = \text{atan} \left\{ \frac{\omega_r^2 - \omega^2}{\omega\omega_r/Q} \right\}$



$$\Delta\omega_0 = \Delta\varphi \cdot \frac{1}{\left[1 + \left(Q \cdot \frac{\omega_r^2 - \omega_0^2}{\omega_0\omega_r} \right)^2 \right] \cdot \frac{Q}{\omega_r} \left[-\frac{2\omega_0^2 + \omega_r^2 - \omega_0^2}{\omega_0^2} \right] \Big|_{\omega_0 = \omega_r}}$$

We get $\Delta\omega_0 \approx \frac{\Delta\varphi}{-2Q/\omega_0}$ → The larger Q, the lower $\Delta\omega$



For high Q , the rapid phase variation "desensitivizes" the oscillator to delays in the loop:

$$\Delta \omega_0 \approx \frac{\Delta \varphi}{-\frac{2Q}{\omega_0}} \rightarrow \frac{\Delta \omega_0}{\omega_0} = -\frac{\Delta \varphi}{2Q}$$

Higher Q s therefore stabilize frequency changes

Definition frequency stability $\Delta = \frac{\Delta \omega / \omega_0}{\Delta \varphi} = -\frac{1}{2Q}$

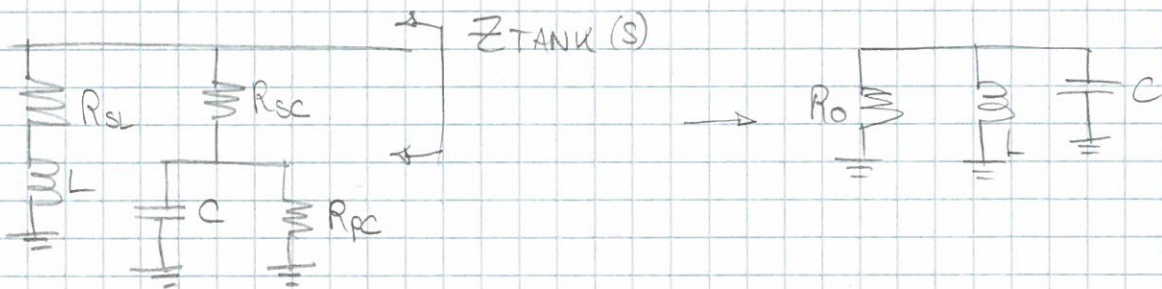
This result is valid for a basic LC oscillator, it changes with respect to topologies (e.g.: ring oscillators don't have a Q factor).

Recall: Q represents the ratio between maximum energy stored in the tank and the energy dissipated per cycle (see previous oral answers to matched networks):

$$Q = 2\pi \frac{E_r}{E_d} = \omega_0 RC = \frac{R}{\omega L} \rightarrow \text{for a // RLC reso}$$

EX CURSUS

In a real tank, parasitic resistors are modeled like:



Assume small losses: $R_{sl} \ll \omega_0 L$ $R_{sc} \ll \frac{1}{\omega_0 C}$ $R_{pc} \gg \frac{1}{\omega_0 C}$

Then

$$Q_{sc} = \frac{1}{\omega_0 C R_{sc}} \quad Q_{pc} = \omega_0 C R_{pc} \quad Q_{sl} = \frac{\omega_0 L}{R_{sl}}$$

$$P_{sc} \approx \frac{1}{2} (A_0 \omega_0 C)^2 R_{sc} \quad P_{pc} \approx \frac{1}{2} \frac{A_0^2}{R_{pc}} \quad P_{sl} = \frac{1}{2} \left(\frac{A_0}{\omega_0 L} \right)^2 R_{sl}$$

These three dissipated powers are equal to R_0 dissipated power:

$$P_{sl} + P_{sc} + P_{pc} = \frac{A_0^2}{2R_0} \rightarrow \frac{1}{Q} = \frac{1}{Q_{sl}} + \frac{1}{Q_{sc}} + \frac{1}{Q_{pc}}$$

We already know these results:

- $Z_{TANK} @ \text{resonance} = R$
- -3dB BW is $\frac{\omega_r}{2Q} = \frac{1}{2RC}$ for a RLC TANK
- Slope is -20dB/dec

Now, consider $S_{in} = \frac{kT}{R}$ Rice's Theorem

\rightarrow

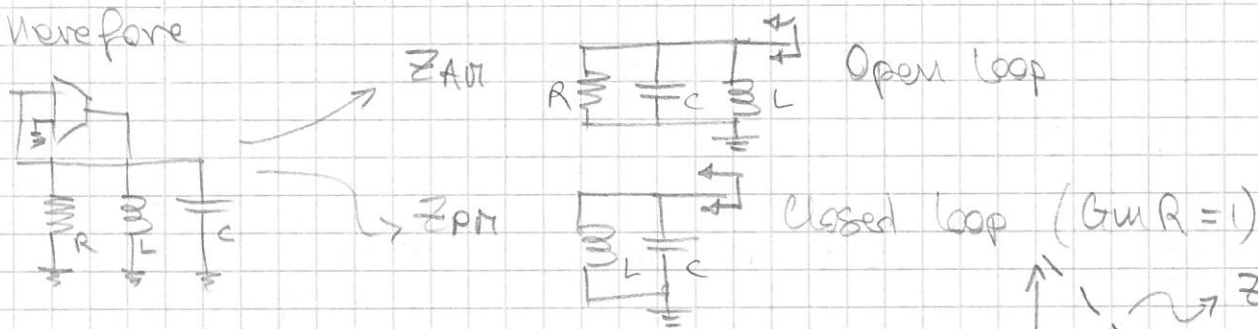
$\frac{2kT}{R}$ AM noise (in phase)

$\frac{2kT}{R}$ PM noise (in quadrature)

Oscillator system behaves differently:

- AM noise: Transconductor hard limits its current, therefore there is no change in amplitude. Any amplitude modulation will therefore see an open loop of the osc $\rightarrow Z_{AM}(j\omega) = Z|_{OL}(j\omega) = Z_{RLC}(j\omega)$
- PM noise: transconductor can't distinguish between the osc phase and the noise induced on the phase. Loop is therefore closed and noise directly couples into it $\rightarrow R$ is compensated by loop G_m

Therefore



$$S_V(\omega) = \frac{2kT}{R} |Z_{AM}(\omega)|^2 + \frac{2kT}{R} |Z_{PM}(\omega)|^2$$

Where, for a small $\Delta\omega$ offset $\ll \frac{\omega_r}{2Q} = \frac{1}{2RC}$

$$|Z_{AM}(\omega)| = R^2 \quad |Z_{PM}(\omega)|^2 = \left| \frac{1}{j2\Delta\omega C} \right|^2 = \frac{1}{4\Delta\omega^2 C^2}$$

$$S_V(\omega) \approx \frac{2kT}{R} \cdot R^2 + \frac{2kT}{R} \cdot \frac{1}{4\Delta\omega^2 C^2} \cdot \frac{R^2 \omega_r^2}{R^2 \omega_r^2} = \dots \rightarrow Q^2$$

$$S_V(\omega) = 2kTR + \frac{1}{2} kTR \left(\frac{\omega_r}{Q} \right)^2 \frac{1}{\Delta\omega^2} \approx \frac{1}{2} kTR \left(\frac{\omega_r}{Q} \right)^2 \frac{1}{\Delta\omega^2}$$

34) Noise / Power Trade-off

Consider phase noise estimation:

$$\text{Small } \Delta\omega \rightarrow \frac{1}{\Delta\omega^2} \gg 1$$

$$S_v(\Delta\omega) \approx 2kTR + \frac{1}{2} kT \cdot \frac{1}{C^2} \cdot \frac{1}{R \Delta\omega^2} \approx \frac{1}{2} kT \frac{1}{C^2} \frac{1}{R \Delta\omega^2}$$

To reduce S_v we could lower C value. This means that:

- $\omega_r = \frac{1}{\sqrt{LC}} \rightarrow \downarrow C \rightarrow \uparrow L \Rightarrow \uparrow R$ parasitic resistance of L
 $\rightarrow Q$ gets worse
- If $\uparrow R \rightarrow$ power dissipation increases \rightarrow the small L values used in integrated RF circuits aren't that stable for higher temperatures
- higher temperatures increases thermal noise

Consider now the $\alpha(\Delta\omega)$:

$$\alpha(\Delta\omega) = \frac{S_v(\omega_r + \Delta\omega)}{P_{\text{carrier}}} = \frac{\frac{1}{2} kT R \left(\frac{\omega_r}{Q}\right)^2 \frac{F_a}{\Delta\omega^2}}{\frac{A_0^2}{2}} = \frac{\frac{1}{2} kT \left(\frac{\omega_r}{Q}\right)^2 \frac{F_a}{\Delta\omega^2}}{\frac{A_0^2}{2R}}$$

Where $\frac{A_0^2}{2R} = P_{\text{dissipated}}$ and $P_{\text{supply}} = \eta P_{\text{diss}} = P_{\text{dc}}$

F_a is an additional noise term factor that accounts for active elements noise

$$\alpha(\Delta\omega) = \frac{1}{2} \frac{kT}{\eta P_{\text{dc}}} \left(\frac{\omega_r}{Q}\right)^2 \frac{F_a}{\Delta\omega^2}$$

Let us build a Figure of Merit that does not take into account osc frequency and dissipated power

Note $P_{\text{dc, mW}} = 10^3 P_{\text{dc}} [\text{W}]$

$$F_{\text{OM}} |_{\text{dB}} = 10 \log_{10} \left\{ \frac{1}{\alpha(\Delta\omega) P_{\text{dc, mW}}} \cdot \left(\frac{\omega_{\text{osc}}}{\Delta\omega}\right)^2 \right\}$$

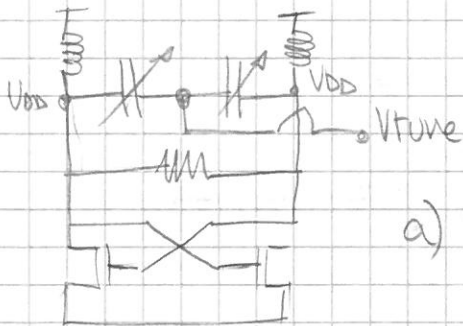
$$= 10 \log_{10} \left\{ 10^{-3} \cdot \frac{2\eta}{kT} Q^2 \cdot \frac{1}{F_a} \right\}$$

\nearrow IDEAL PSU \nearrow noiseless active elements

Thermodynamic limit is for $\eta = 1$ $F_a = 1$

$$F_{\text{OM}} |_{\text{dB}} = 10 \log_{10} \left\{ \frac{2}{kT} Q^2 \right\} - 30 \text{dB} \rightarrow \text{for } Q=10 \rightarrow F_{\text{OM}} |_{\text{min}} = -197 \text{ dB/Hz}$$

35) VCO and FM noise on tuning voltage



We need VCOs for channel selection

Most common used device is varactors.

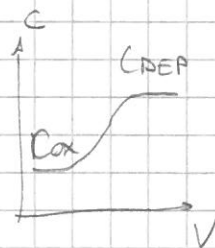
a) PN Junction in reverse biasing

$$C = \frac{C_{do}}{\left(1 + \frac{V}{V_{jo}}\right)^m} \quad m \sim \frac{1}{3} \div \frac{1}{2}$$

b) MOS Junctions:

1) from inversion region to depletion

2) from accumulation = = depletion



a) solution has DC current leakage but higher Q factors can be achieved

b) Very low DC leakage current

FM noise induced by V_{tune}

$$V_{tune} \rightarrow \frac{K_{VCO}}{s} \rightarrow X_{out}(t) = A_0 \cos\left(\omega_c t + \int_{-\infty}^t K_{VCO} V_{tune}(t') dt'\right)$$

Integral translates to $\frac{1}{s}$ in Laplace domain.

Consider a white noise on V_{tune} and select 1 harmonic at ω_m :



$$V_m \cos \omega_m t \rightarrow A_0 \cos\left(\omega_c t + \frac{K_{VCO}}{\omega_m} V_m \sin \omega_m t\right)$$

$$a^2(\omega_m) = \frac{S_y}{2} = \frac{K_{VCO}^2}{2\omega_m^2} \cdot S_{V_{tune}}(\omega_m)$$

\rightarrow white noise spectrum

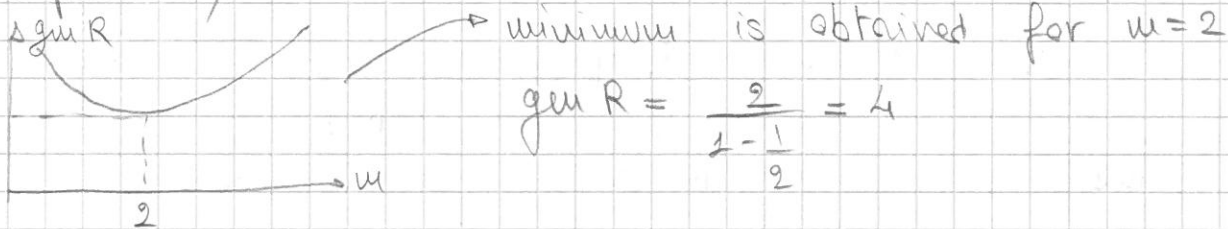
We see that integration of the white noise on V_{tune} generates a $\frac{1}{\omega^2}$ dependence on output phase spectrum

\rightarrow mod on V_{tune} generates FM mod on VCO

looking at $g_m R_T = 1$:

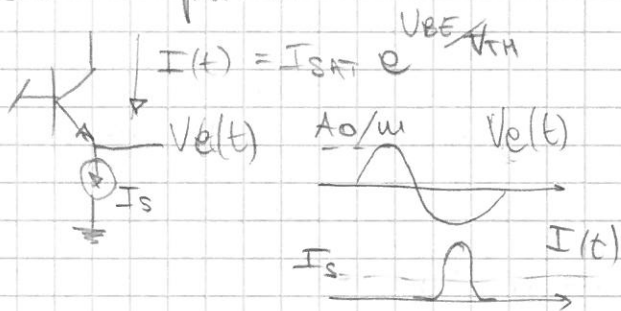
$$\frac{g_m}{\omega} \frac{\omega^2 R}{\omega^2 + g_m R} = 1 \quad \dots \rightarrow \quad g_m R = \frac{\omega}{1 - 1/\omega}$$

We have an usual $g_m R$ (for LC osc) but we have a ω dependency

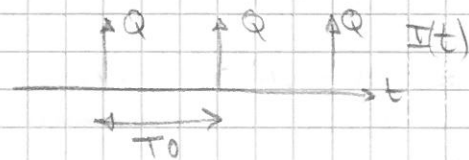


- Small $\omega \rightarrow$ more voltage amplification but more losses
- Large $\omega \rightarrow$ large voltage attenuation but small losses

Osc amplitude :

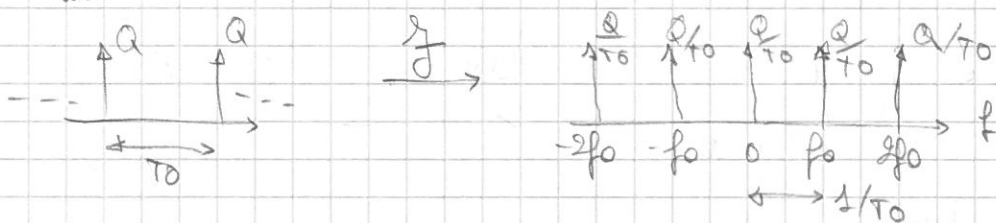


$I(t)$ increases exponentially only when $V_e(t)$ is at its most negative peak. We can approx to δ train



consider :

- A_0 output peak voltage
- $\frac{A_0}{\omega}$ is the emitter voltage $\rightarrow V_e(t)$ max voltage / $\frac{A_0}{\omega} \Rightarrow \frac{\omega T}{Q} = V_{TH}$



Remember that I_s is the bias current of the BJT

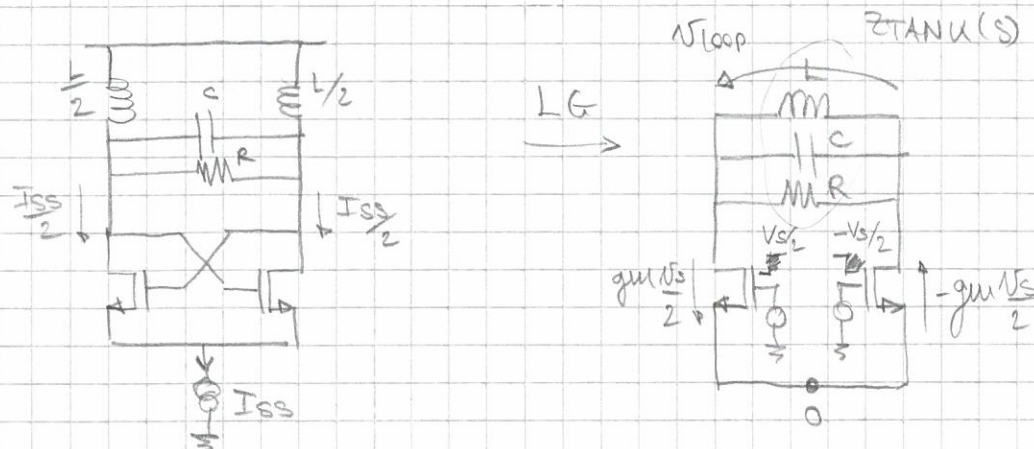
- We have two δ in $\pm f_0 \rightarrow \bar{I}_1 = 2Q/T$
- In 0 we have one $\delta \rightarrow$ DC current $\rightarrow I_s = Q/T_0$

$$\text{So } g_m R = \frac{\bar{I}_1}{V_1} = \frac{2Q/T_0}{A_0/\omega} = \frac{2I_s}{A_0/\omega}$$

Oscillation condition for large signals:

$$g_m R = \frac{\omega}{1 - 1/\omega} \rightarrow A_0 = 2I_s R \left(1 - \frac{1}{\omega}\right) \stackrel{\omega=2}{=} I_s R$$

Differential oscillator

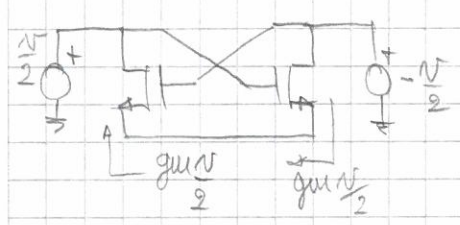


• \$LG(s)\$ can easily be derived: $LG(s) = \frac{V_{loop}(s)}{v_s} = \frac{gm}{2} Z_{TANK}(s)$

Oscillation condition

$$\begin{cases} \omega_0 = 1/\sqrt{LC} \\ \frac{gm}{2} R = 1 \end{cases}$$

• Negative resistance:



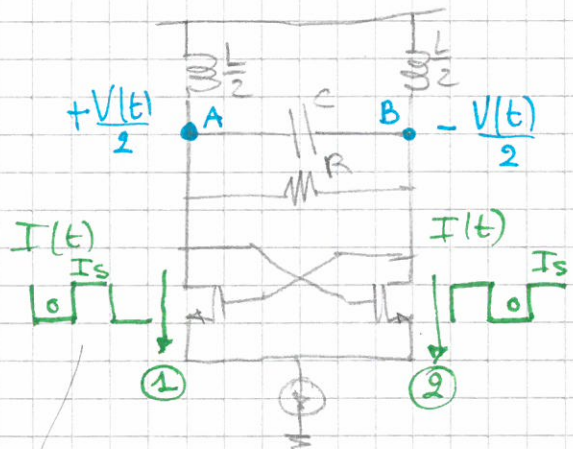
$$Z_a(s) = \frac{v_s}{-gm v_s/2} = -\frac{2}{gm}$$

$$Z_a = -Z_{TANK} \rightarrow \frac{gm}{2} R = 1$$

$$\omega_0 = \frac{1}{\sqrt{LC}}$$

Output amplitude:

Suppose abrupt switching of the FETs because of the large amplitude swing ($A_0 \gg \sqrt{2} V_{ov}$)



① and ② are in anti-phase

$$|G_{mh}| R = 1 \quad G_{mh} = \frac{\bar{I}_1}{\bar{V}_1} = \frac{\frac{2}{\pi} I_{SS}}{A_0}$$

$A_0 = V_A - V_B =$ differential voltage amplitude

Therefore $A_0 = \frac{2}{\pi} I_{SS} \cdot R$

Harmonic⁽¹⁾ = $\frac{2}{\pi} \Delta I_s = \frac{2}{\pi} (I_{SS} - 0) = \frac{2}{\pi} I_{SS}$

38) RTN passive mixers: conversion gain, noise

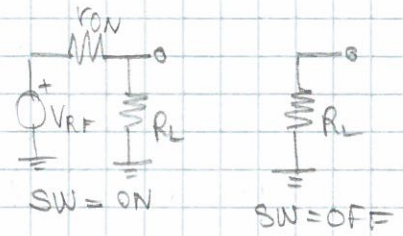
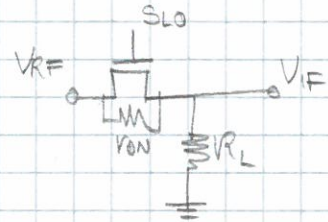
Consider

- $V_{RF}(t) = A \cos \omega_{RF} t$

- $S_{LO}(t) = \left[\frac{1}{2} + \frac{2}{\pi} \cos(\omega_{LO} t) + 0 \cdot t \right]$

$$V_{IF} \approx \frac{R_L}{R_L + r_{ON}} S_{LO}(t) \cdot V_{RF}(t)$$

→ assume r_{ON} independent from biasing over time



$V_{IF}(t) =$ Linear, Time, Variant equation (if r_{ON} fixed):

$$V_{IF}(t) = \frac{R_L}{R_L + r_{ON}} \left[\frac{1}{2} + \frac{2}{\pi} \cos(\omega_{LO} t) + 0 \cdot t \right] \cdot V_{RF}(t) =$$

→ RF-to-IF feedthrough → Wanted freq. conversion

$$V_{IF}(t) = \frac{R_L}{R_L + r_{ON}} \left[\frac{A}{2} \cos \omega_{RF} t + \frac{1}{2} A \cdot \frac{2}{\pi} \cos(\omega_{LO} - \omega_{RF}) + \frac{1}{2} A \cdot \frac{2}{\pi} \cos(\omega_{LO} + \omega_{RF}) t + 0 \cdot t \right]$$

RF-to-IF feedthrough Wanted IF signal

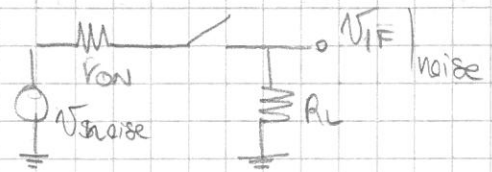
Conversion voltage gain $A_V \triangleq \frac{V_{IF}(\omega_{IF})}{V_{RF}(\omega_{RF})} = \left(\frac{R_L}{R_L + r_{ON}} \right) \frac{\frac{A}{\pi}}{A} = \frac{1}{\pi} \frac{R_L}{R_L + r_{ON}}$

max $A_V \Big|_{R_L \gg r_{ON}} = \frac{1}{\pi} \approx -10 \text{ dB}$

Noise analysis for RTN passive mixer:

• MOSFET noise

$$PSD|_{r_{on}} = 4kT r_{on}$$

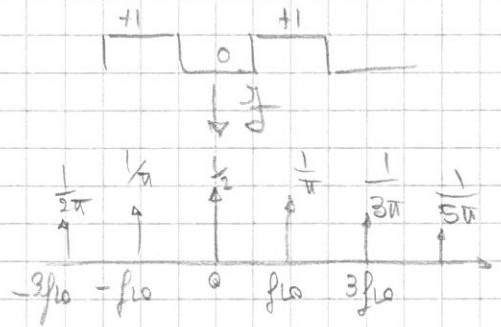


FET is in triode region:

$$V_{IFn}(t) = \underbrace{V_n}_{\frac{2}{\pi}} \cdot S_{LO}(t) \cdot \frac{R_L}{R_L + r_{on}} \rightarrow \text{use PSDs + Fourier analysis}$$

$$PSD_{V_{IF}}(f) = PSD_{V_n}(f) * |S_{LO}(f)|^2 \left(\frac{R_L}{R_L + r_{on}} \right)$$

$$PSD_{V_{IF}}(f) = 4kT r_{on} * \sum_{k=-\infty}^{\infty} |c_k|^2 \delta(f - k f_{LO}) \left(\frac{R_L}{R_L + r_{on}} \right)$$



In other words, convolution of white noise with infinite, weighted deltas \rightarrow spectrum folding



But:

$$\sum_{k=-\infty}^{\infty} |c_k|^2 = \text{power of } S_{LO}(t) = \int_{-\infty}^{\infty} |S_{LO}(f)|^2 df \stackrel{\text{Parseval}}{=} \frac{1}{T_0} \int_0^{T_0} |S_{LO}(t)|^2 dt$$

$$\frac{1}{T_0} \int_0^{T_0} |S_{LO}(t)|^2 dt = \frac{1}{T_0} \int_0^{T_0/2} 1 dt = \frac{1}{2}, \text{ so:}$$

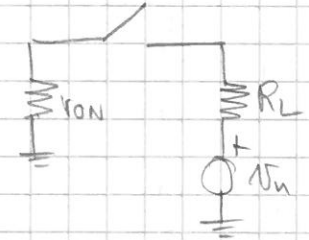
$$PSD_{V_{IF}}|_{r_{os}} = 4kT r_{on} \cdot \frac{1}{2} \left(\frac{R_L}{R_L + r_{on}} \right)^2$$

Intuitive result: V_n is transferred to V_{IF} for only half of the period with $\left(\frac{R_L}{R_L + r_{on}} \right)$ gain.

• R_L noise \rightarrow $PSD = 4kTR_L$

Following the same procedure for MOSFET noise:

$$V_{IFn}(t) = \sqrt{n}(t) S_{lo}(t) \frac{r_{on}}{r_{on} + R_L} + \sqrt{n}(t) S_{ho}(t)$$



Where $S_{lo}(t) = \begin{cases} 1 \\ 0 \end{cases}$ \rightarrow Necessary because

$S_{ho}(t) = \begin{cases} 1 \\ 0 \end{cases}$ We have two transfer functions for each half period

Same procedure + spectrum folding will lead to:

$$PSD_{V_{IF}}(f) = 4kTR_L \cdot \frac{1}{2} \left(\frac{r_{on}}{r_{on} + R_L} \right) + 4kTR_L \cdot \frac{1}{2}$$

when SW = closed

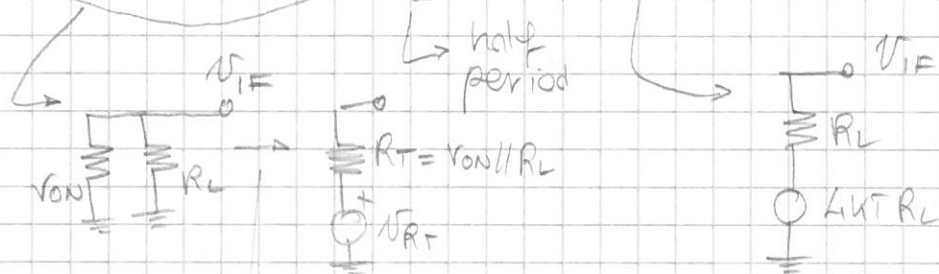
when SW = opened

Note: output noise varies over $\frac{1}{2}$ of the period \rightarrow we say it is cyclo-stationary. Moreover, the narrowband filter at the output stage will average this noise over time

• FET $r_{on} + R_L$ noise:

$$PSD_{V_{IF}} \Big|_{Tot} = 2kTr_{on} \left(\frac{R_L}{R_L + r_{on}} \right)^2 + 2kTR_L \left(\frac{r_{on}}{r_{on} + R_L} \right)^2 + 2kTR_L$$

$$= 4kT(r_{on} \parallel R_L) \cdot \frac{1}{2} + 4kTR_L \cdot \frac{1}{2} \quad \rightarrow \text{half period}$$



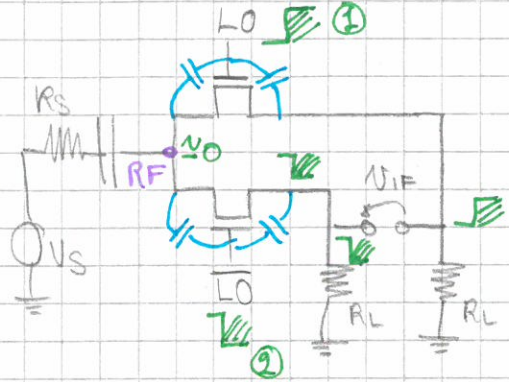
\rightarrow Nyquist Theorem

Note: see answer 40 for the justification of the use of a narrowband LPF to average $PSD_{V_{IF}}$

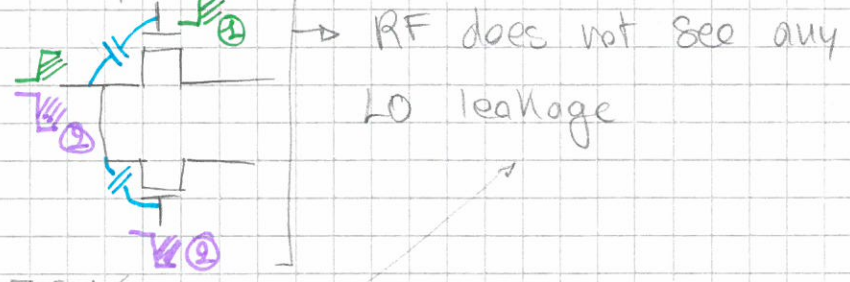
39) Single-balanced and double-balanced topologies, port-to-port isolation

• Single balanced

$$A_v = \frac{2}{\pi} \frac{R_L}{R_L + r_{on}} \text{ double with respect to the RTN mixer}$$



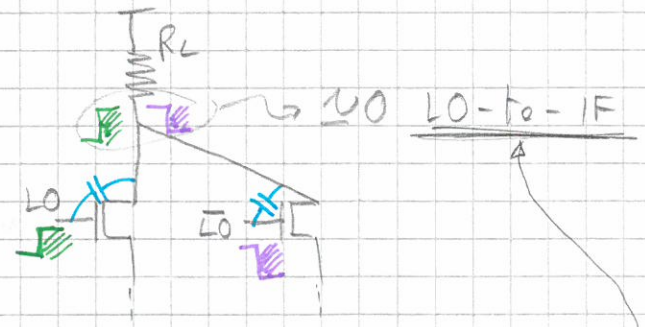
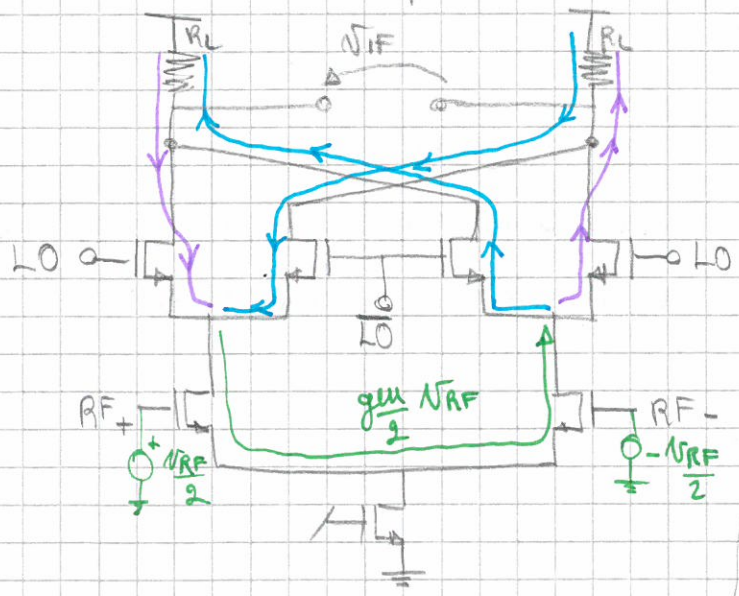
The doubled LO at RF part propagates its steps (1, 2) to the RF part through the parasitic capacitors:



If LO duty cycle is 50%:

- zero RF-to-IF (double mixer has no DC current from RF to IF)
- zero LO-to-RF
- As we can see on VIF node, LO will leak through: non-zero LO-to-IF

We can solve last point by using a double balanced mixer:



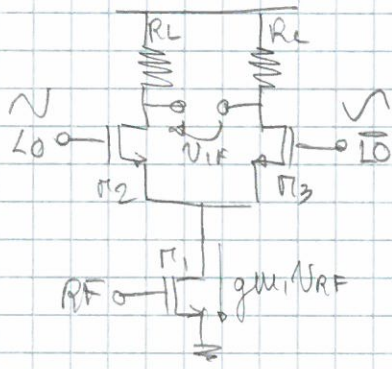
Gain is basically unchanged but LO-to-IF is solved

$$V_{IF} = \frac{g_m}{2} \cdot 2 \sqrt{V_{RF}} R_L \times L_0(t) \rightarrow A_v = \frac{2}{\pi} g_m R_L$$

40) Active CMOS mixers: conversion gain, noise, port-to-port isolation

Linear Time Variant model:

$$V_{IF}(t) = g_{m1} V_{RF}(t) \times I_{LO}(t) R_L$$



Hypothesis:

- 50% duty cycle
- M_1 is in saturation
- Abrupt switching of M_2, M_3

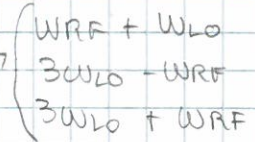
$$A_V = \frac{V_{IF}(\omega_{IF})}{V_{RF}}$$

where

$$V_{RF}(t) = A \cos \omega_{RF} t$$

$$V_{IF}(t) = g_{m1} R_L A \cos \omega_{RF} t \left(\frac{1}{\pi} \cos \omega_{LO} t + \frac{1}{3\pi} \cos 3\omega_{LO} t + o.t. \right)$$

$$= g_{m1} R_L A \cdot \frac{2}{\pi} \cos(\omega_{RF} - \omega_{LO})t + o.t.$$



$$A_V = \frac{2}{\pi} g_{m1} R_L \frac{A}{A} = \frac{2}{\pi} g_{m1} R_L$$

Zero RF-to-IF if duty cycle is at 50%

Zero LO-to-RF because of the LO and LO net balance on RF

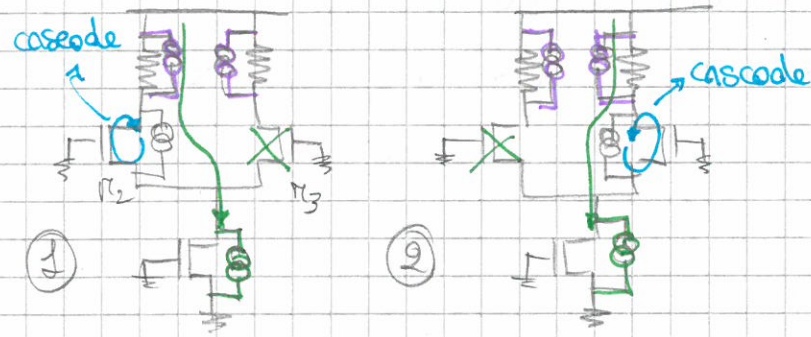
non-zero LO-to-IF (we should use a double balanced mixer)

Note: the presence of o.t. (other terms) needs to be removed from the output by the use of a lowpass filter.

This output filter (narrowband) becomes especially important when PSD is computed and result will be expressed in an averaged noise of the mixer cyclo-stationary noise (averaged because of the LPF action)

Noise in active single balanced mixer

- Unbalanced condition \rightarrow abrupt switching of π_2, π_3



We can easily see that R_L noise is always there

$$PSD|_{\omega_{IF}} = 2 \cdot 4KT R_L$$

\searrow double load \rightarrow double noise power

Scenario ①:

- π_3 is fully off
- π_2 is cascoded \rightarrow noise current does not reach the output
- π_1 current noise is steered to π_2 and reaches the output

Scenario ②: same as ① but process is mirrored

Therefore: $PSD|_{\omega_{IF}, \pi_2, \pi_3} = 0$ while for π_1

$$PSD|_{\omega_{IF}, \pi_1} = R_L^2 \cdot 4KT \frac{g_{m1}}{\alpha} \cdot \sum_{k=-\infty}^{+\infty} |c_k|^2 \quad \rightarrow \text{Same as previous answers}$$

$$\sum_{k=-\infty}^{+\infty} |c_k|^2 = X_{LO} \text{ power} = \int_{-\infty}^{+\infty} |X_{LO}(f)|^2 df = \frac{1}{T_0} \int_0^{T_0} |X_{LO}(t)|^2 dt$$

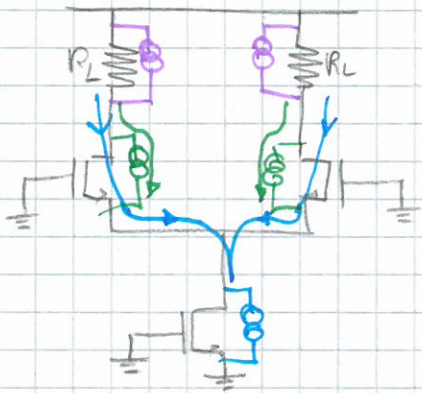
$$\frac{1}{T_0} \int_0^{T_0} |X_{LO}(t)|^2 dt = \frac{1}{T_0} \left[\int_0^{T/2} (1)^2 dt + \int_0^{T/2} (-1)^2 dt \right] = 1$$

Parseval

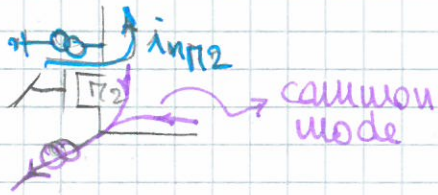
Intuitively: it does not matter whether π_1 noise current is steered left or right \rightarrow π_1 noise current always reaches the output, therefore

$$PSD^{UNBAL} = 8KT R_L + 4KT \frac{g_{m1}}{\alpha} R_L^2$$

• Balanced condition $\rightarrow M_2$ and M_3 are both on



- R_L noise reaches the output
- M_1 offers a common mode current \rightarrow does not reach the output
- M_2, M_3 fully contribute to output current (can be easily derived by splitting noise in two contributions)

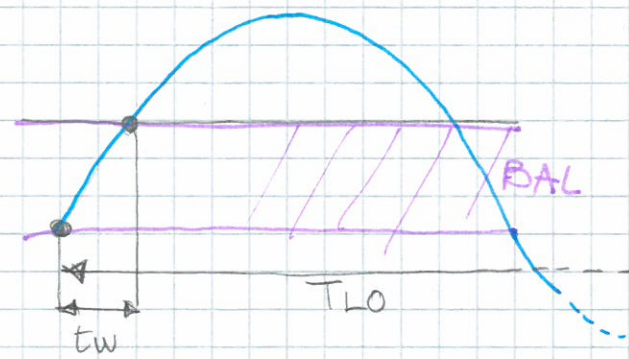
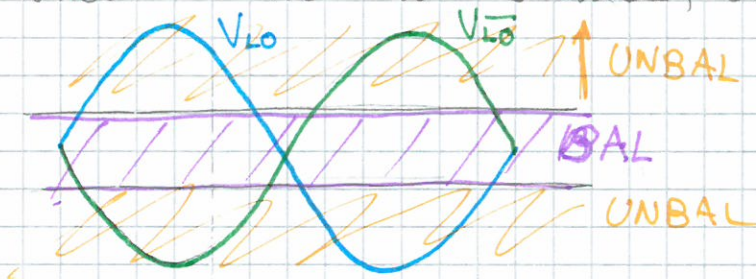


Therefore we can conclude that

$$PSD|_{R_L}^{BAL} = 8kTR_L + 8kT \frac{g_m}{\alpha} g_{m2} R_L^2$$

\swarrow \searrow
 $PSD|_{R_L}$ \rightarrow contribution of $M_2 + M_3$ $\rightarrow g_{m2} = g_{m3}$

When do we have balanced, unbalanced scenarios?



Hypothesis:

- When M_2, M_3 are fully switching

$$PSD \approx PSD|_{UNBAL}$$

- Lowpass filtering on mixer's out will lead to an averaging of

the cyclostationary noise.

This way, we can define a duty cycle $D = t_w / T_0$

where $t_w =$ time where circuit is balanced $T_0 =$ wave period

Averaged out PSD will then be:

$$PSD|_{AVERAGE} = PSD|_{UNBAL} \left(1 - \frac{2t_w}{T_0}\right) + PSD|_{BAL} \cdot \frac{2t_w}{T_0}$$

⊛: in a period there are $2t_w$ in which PSD is balanced